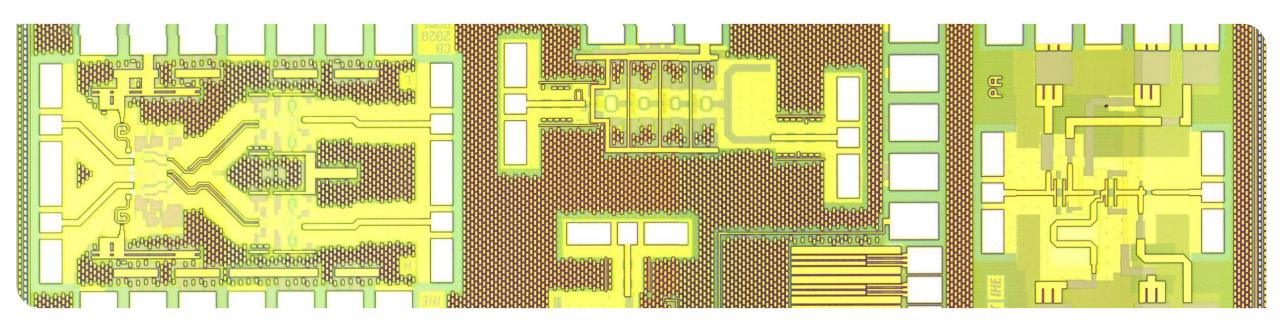




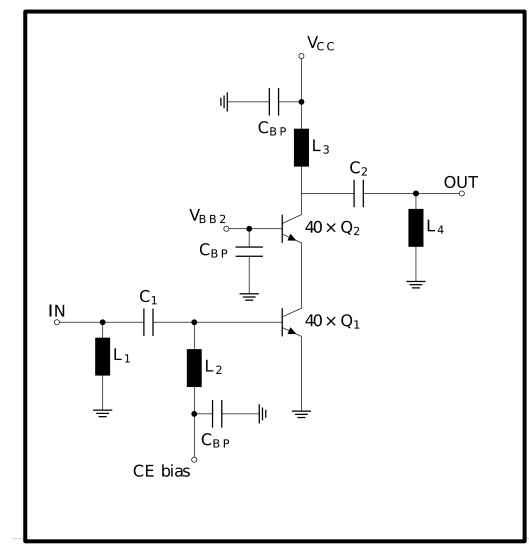
A 28 GHz Cascode Power Amplifier

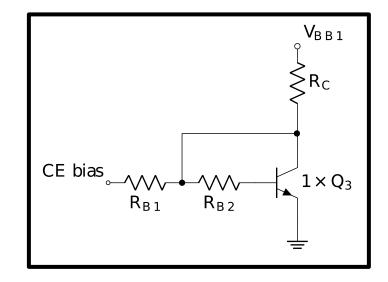
Henning Poensgen



Schematic of the PA





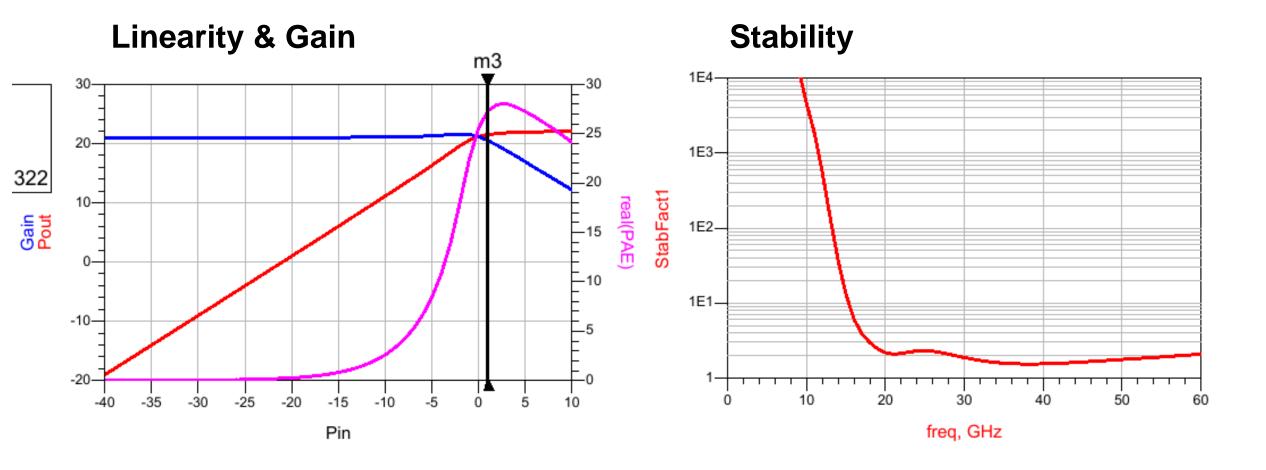


Design Goals:

- High Output Power
- Few stages as possible
- single ended design

Simulation – Linearity & Stability

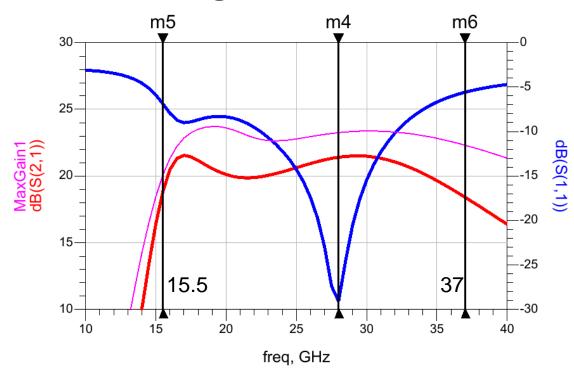




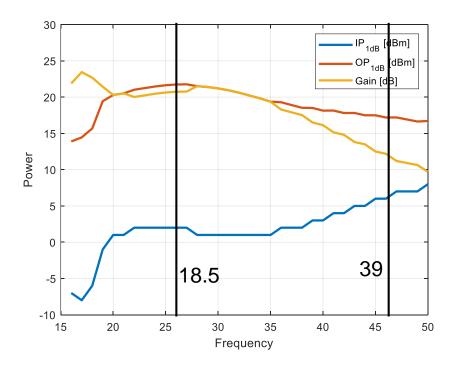
Simulation – Frequency Behavior



Small Signal

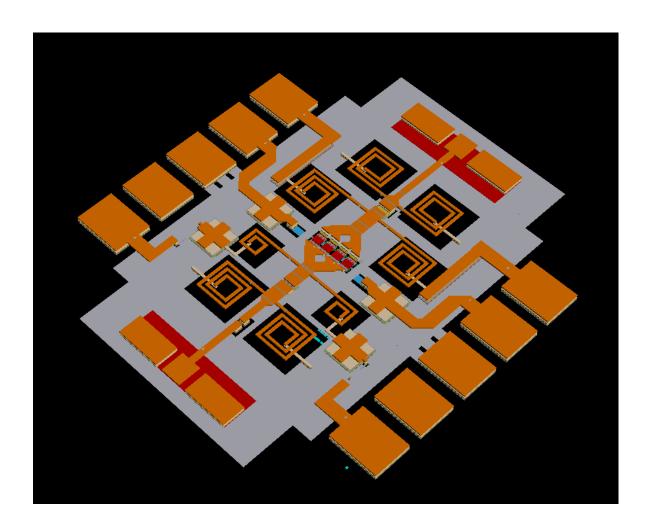


Large Signal



Proposed Power Amplifier





- Size without pads: 500 x 410 μm
- Size with pads: 760 x 725 μm