**SRI MUTHUKUMARAN ARTS AND SCIENCE COLLEGE**

**CHIKKARAYAPURAM CHENNAI-600 069**

**CYCLETEST - II**

**Subject :Operating System Max marks : 50**

**Subject code : Time : 2hrs**

**Answers all questions**

**Part – A (5\*2 =10)**

1. **What is Process Synchronization?**
2. **What is Monitor?**
3. **Define Race condition.**
4. **Define Deadlock.**
5. **What is base and limit register?**

**Part – B (4\*5 =20)**

1. **Explain the Critical Section Problem.**
2. **What is Semaphore? What are the types of Semaphore in OS?**
3. **Explain Deadlock Characterization.**
4. **Explain in detail about Monitor.**

**Part – C (2\*10 =20)**

1. **Explain in detail Classical Problem of Synchronization.**
2. **Write about briefly Deadlock avoidance.**

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1. **What is Process Synchronization?**
2. **What is Monitor?**
3. **Define Race condition.**
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5. **What is base and limit register?**

**Part – B (4\*5 =20)**

1. **Explain the Critical Section Problem.**
2. **What is Semaphore? What are the types of Semaphore in OS?**
3. **Explain Deadlock Characterization.**
4. **Explain the Binding Mechanism.**

**Part – C (2\*10 =20)**

1. **Explain in detail Classical Problem of Synchronization.**
2. **Write about briefly Deadlock avoidance?**