

1

2

3

4

A

B

C

D

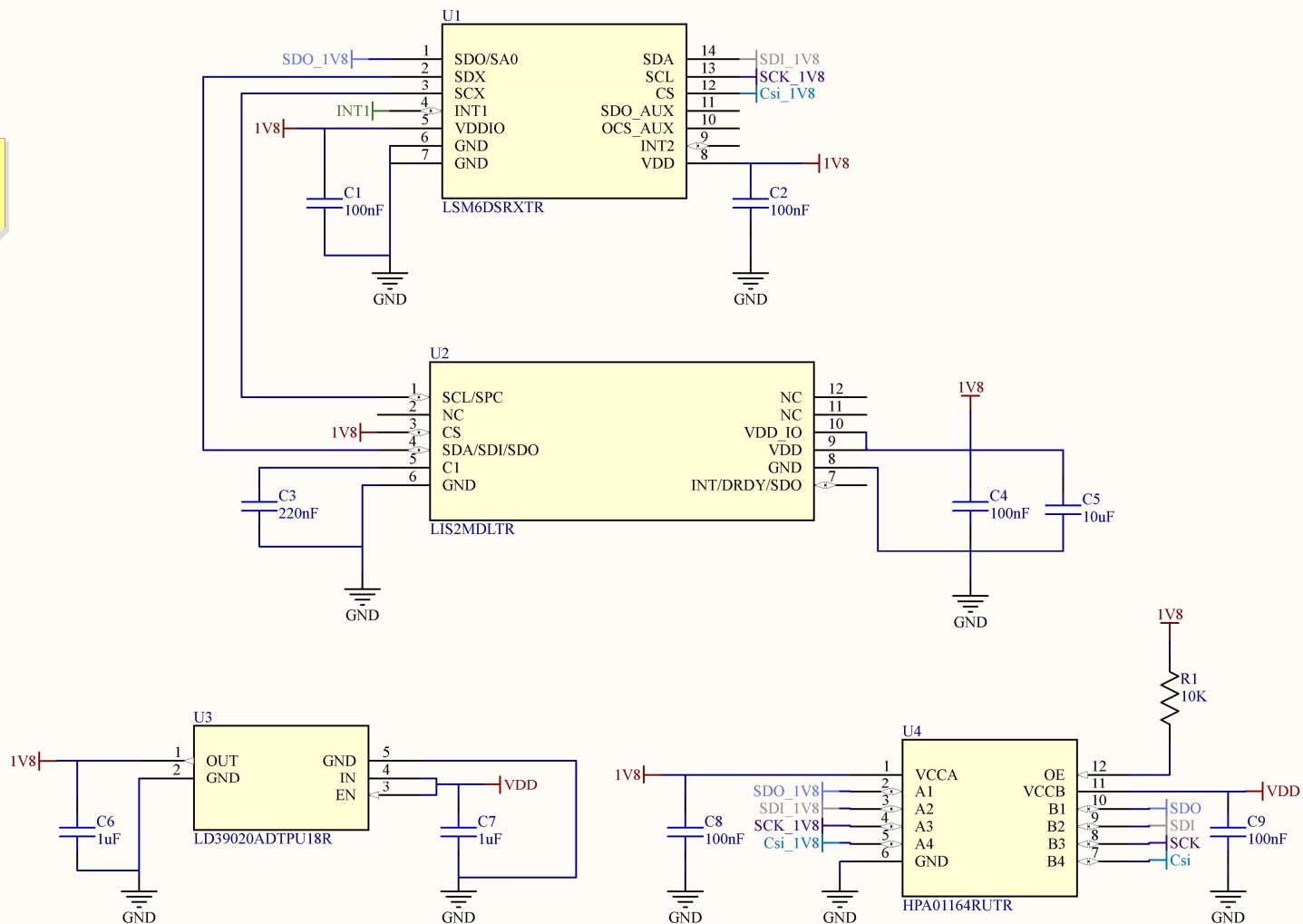
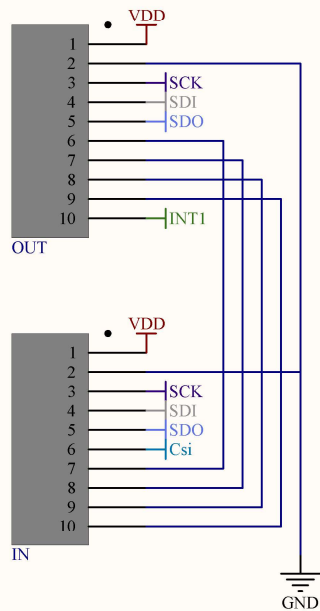
A

B

C

D

Added physical connection to unconnected pin 10 of OUT port to pin 4 (INT1) of LSM6DSRX for debug purposes



Title IMU\_PCB\_2024\_VIAS02.PrjPcb

Date: 11/07/2024 Time: 16:24:42 Author: MG

File: IMU\_PCB\_2024.SchDoc

Centro "E. Piaggio"  
University of Pisa  
Largo L. Lazzarino, 1  
56122 Pisa  
Italy

