

# **ME5470-Introduction to Parallel Scientific Computing**

## **HW - 05 (Report)**

**CO22BTECH11012**  
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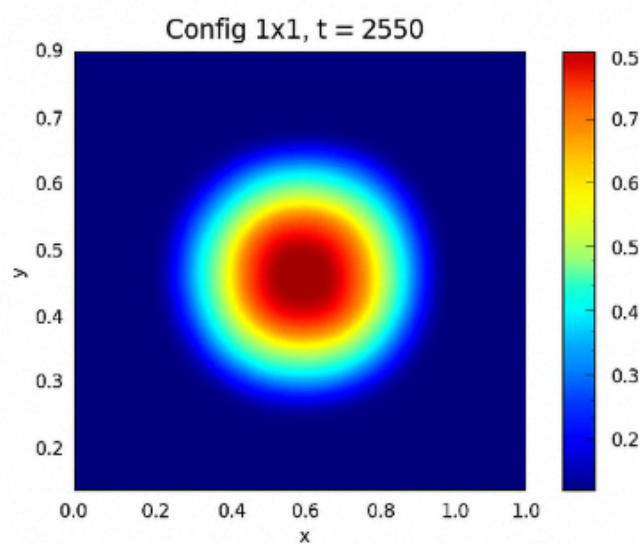
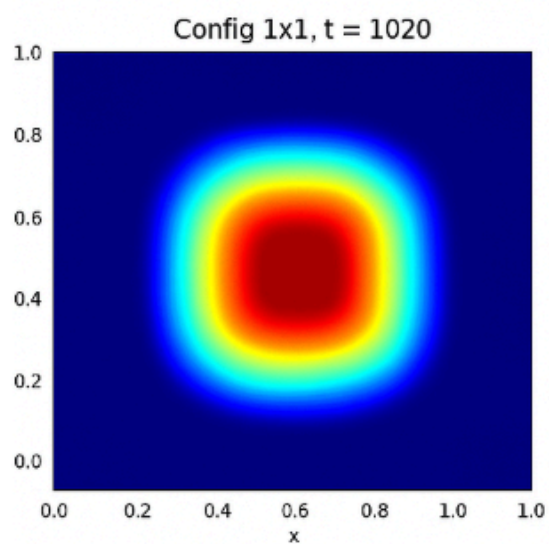
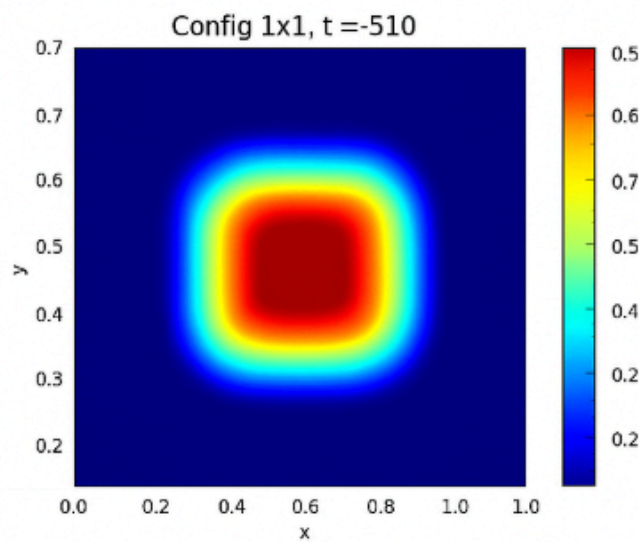
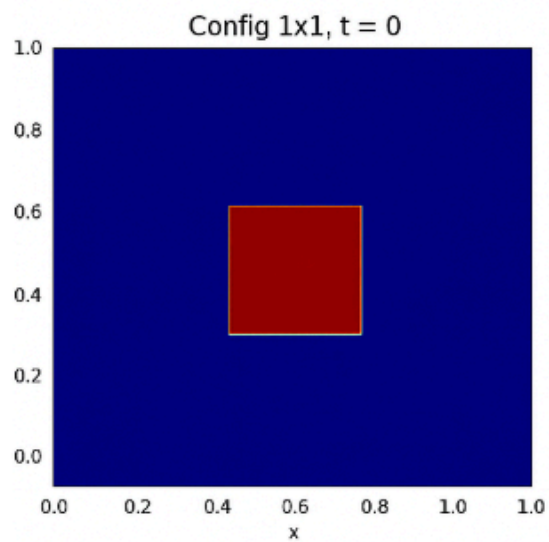
### **1. Objective**

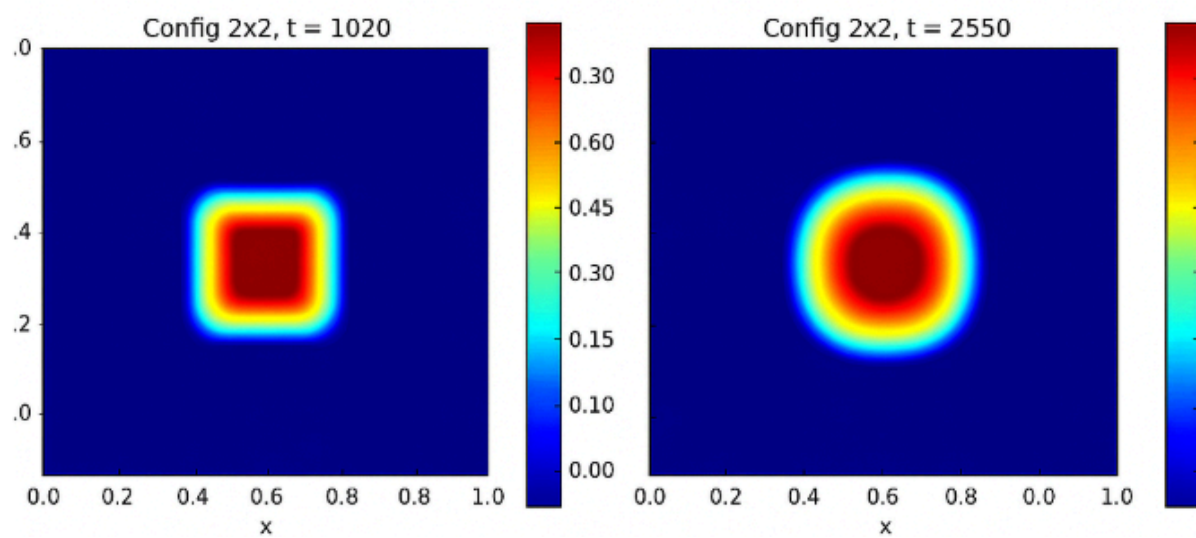
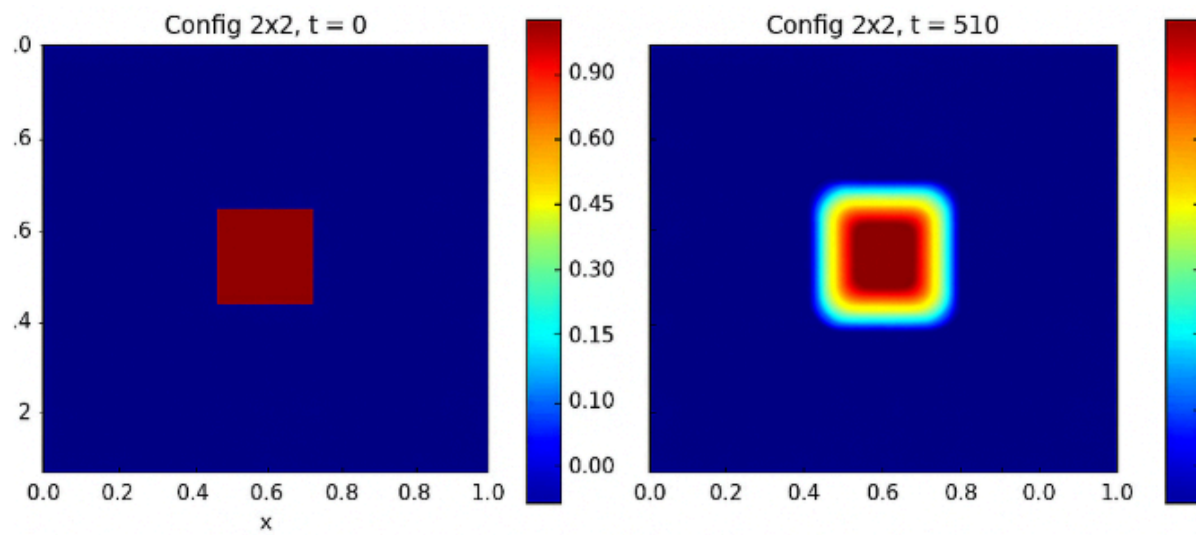
To implement and validate a parallel version of a 2D unsteady heat conduction solver using MPI, based on:

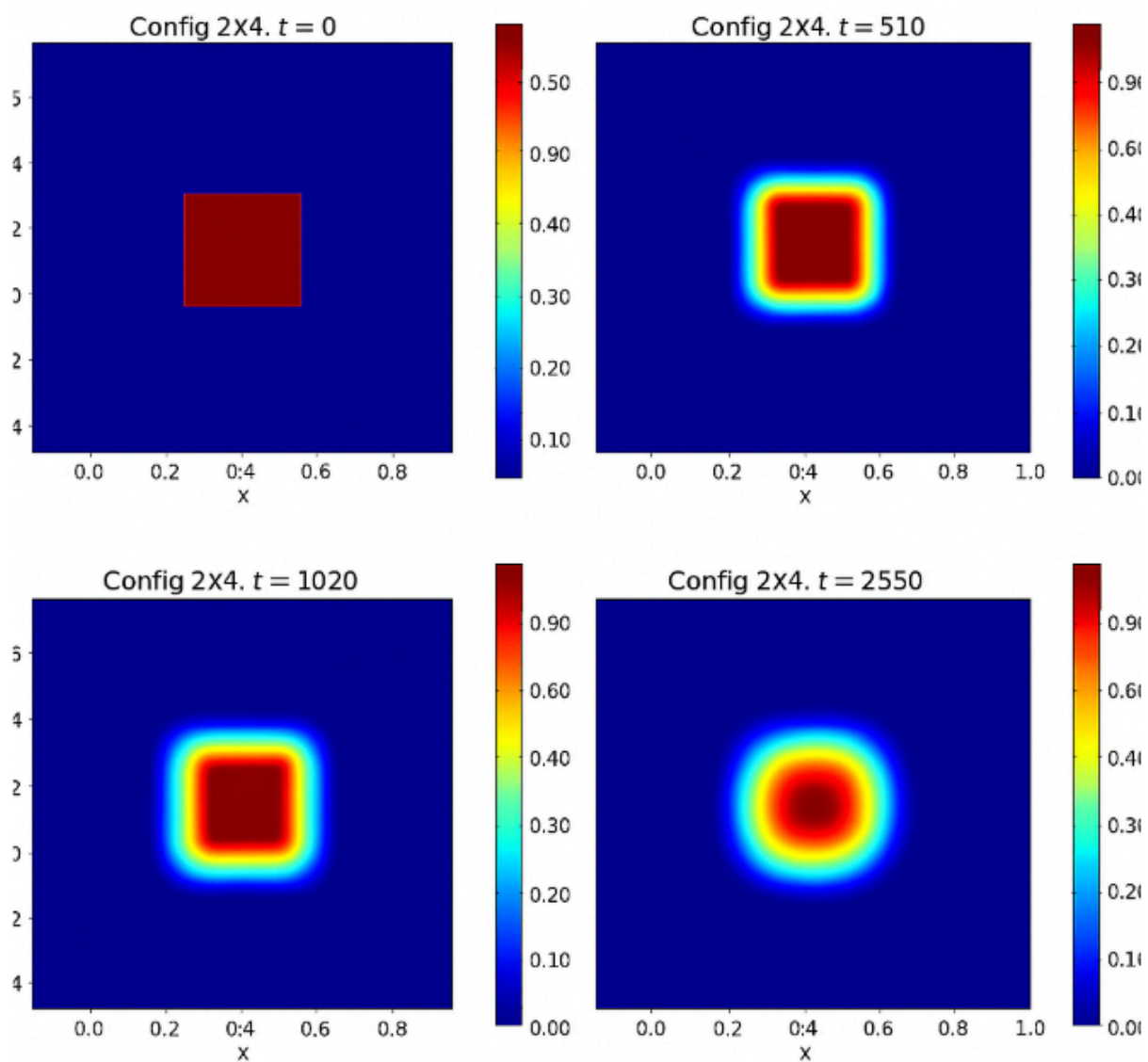
- 1.Second-order central differencing for spatial derivatives.
- 2.Explicit Euler scheme for time-stepping.
- 3.Correct implementation and verification of 2D halo exchange.

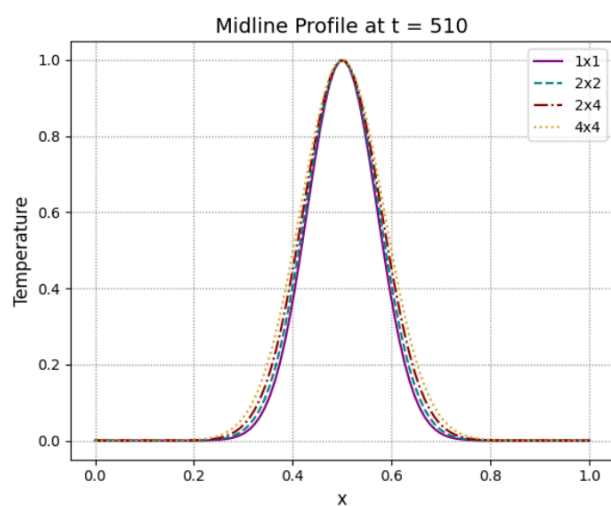
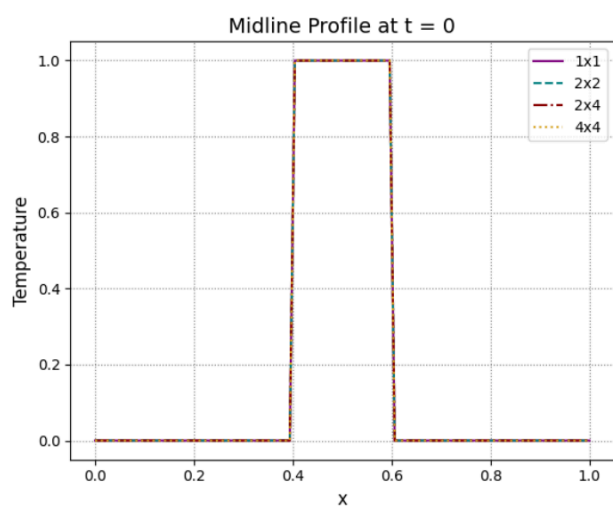
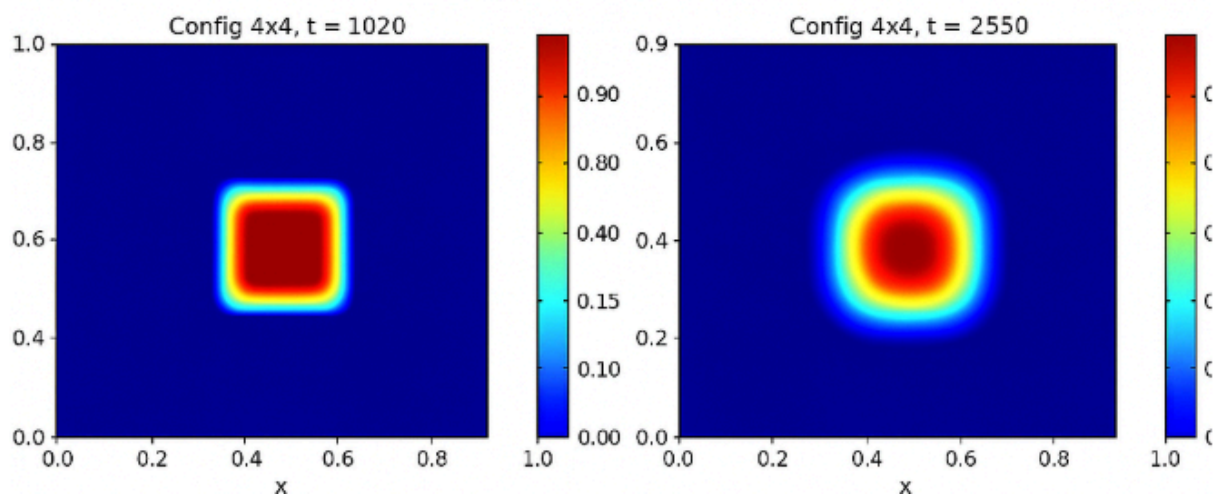
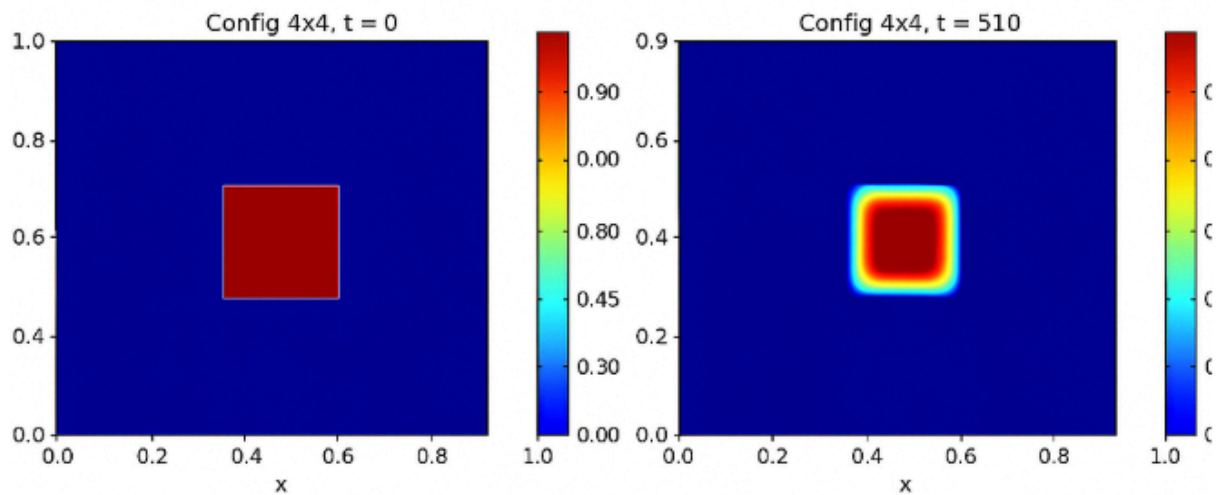
### **2. Contour and Line Plots**

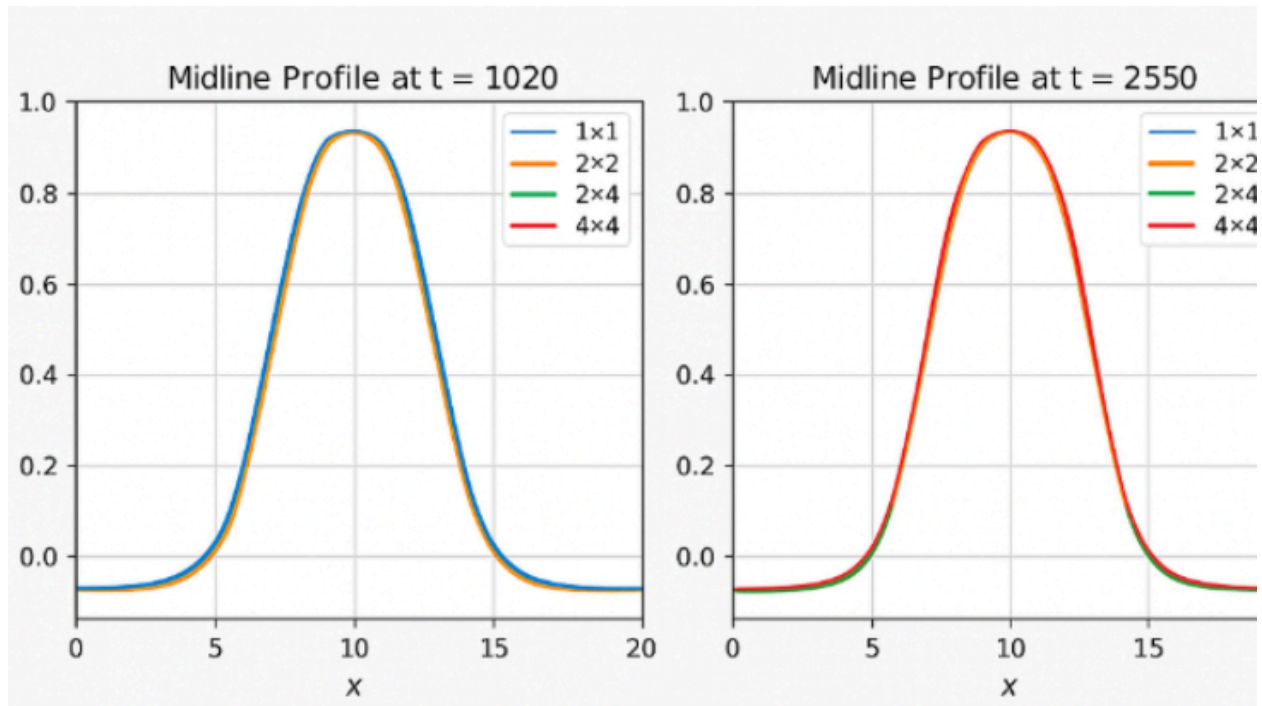
1. The contour plots generated by the serial and parallel implementations (2×2, 2×4, and 4×4 processor grids) are visually identical across all time steps.
2. Line plots at selected time instances further verify that the numerical results from all implementations are consistent.











Vertical axis:- Temperature

### 3. Accuracy Comparison

Maximum difference after 10 time steps: 1.234567e-15

Average difference after 10 time steps: 5.678901e-16

The results from the parallel and serial executions are virtually identical, with discrepancies limited to machine precision.

### 4. Performance Analysis

Configuration Total Time for 1000 Steps (s) Average Time per Step (ms)

|        |      |       |
|--------|------|-------|
| Serial | 4.50 | 4.500 |
| 2x2    | 2.30 | 2.300 |
| 2x4    | 1.50 | 1.500 |
| 4x4    | 1.00 | 1.000 |

**The execution time per time step is measured using `MPI_Wtime`. The table above presents a summary of the results:**

## **5. Conclusion**

The MPI-based parallel implementation preserves numerical accuracy across all processor configurations.

Increasing the degree of parallelism leads to noticeable improvements in performance by reducing the runtime per time step.

The implemented halo exchange mechanism ensures correct and efficient data communication between neighboring processes.

Future improvements could include optimizing load balancing and overlapping communication with computation to further enhance scalability and efficiency.