Weekly Report

# Targets

## Urgent

* Make out how *mfs* works in detail and implement its approximate computing version.

## Important

* Use approximate confidence interval / hypothesis testing of Bernoulli experiments to evaluate the accuracy of error rate.
* Trade off the accuracy of batch error estimation for speed (even directly use Su’s equation to update Boolean difference), perhaps use hypothesis testing to evaluate the accuracy.
* Combine the simulation of circuits with the simulation of Monte Carlo Tree Search. In other words, in one loop of Monte Carlo Tree Search, merge logic simulation and playout (only simulate circuit once and playout once).
* Represent circuit with AIG because of more potential LAC candidates. For each round, select one or more input wires and replace them with constant 0 or 1. Consider how to combine Wu’s method (choose a subset of input wires and substitute).
* Accelerate Approximate Logic Synthesis Ordered by Monte Carlo Tree Search: reuse the result of batch error estimation in playout.

## Worth Trying

* Enhance default policy with greedy approach or field domain knowledge.
* In expansion process of MCTS, expand more than one layers.
* Tune parameters in MCTS.
* Perform greedy flow on leaves of the final Monte Carlo Search Tree.
* Combine beam search and MCTS.

## Potential Topics

* Relationship between power simulation and logic simulation.
* Combine Binarized Neural Network with approximate computing.
* Relationship between Boolean network and Bayesian Network.
* Approximate TMR.

# Progress

## Detailed MFS Flow

From the paper and ABC’s mfs code, the detailed process to resubstitute a node is explained below.

### Construct Window

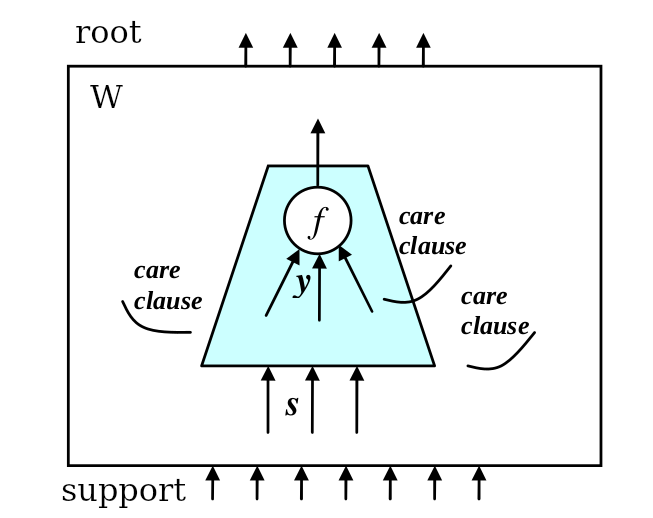
1. Perfrom DFS from node to POs, record those *root* nodes. A *root* satisfies one of the conditions:

* the node has more than fanouts than the limit;
* the node has CO fanouts;
* the node has fanouts above the cutoff level.

2. Find the *support* nodes of node , which means the PIs in the fanin cone of node .

3. Record all nodes on paths from *root* nodes to PIs, and those nodes forms a window .

The window is shown below:



### Find Divisor Candidates

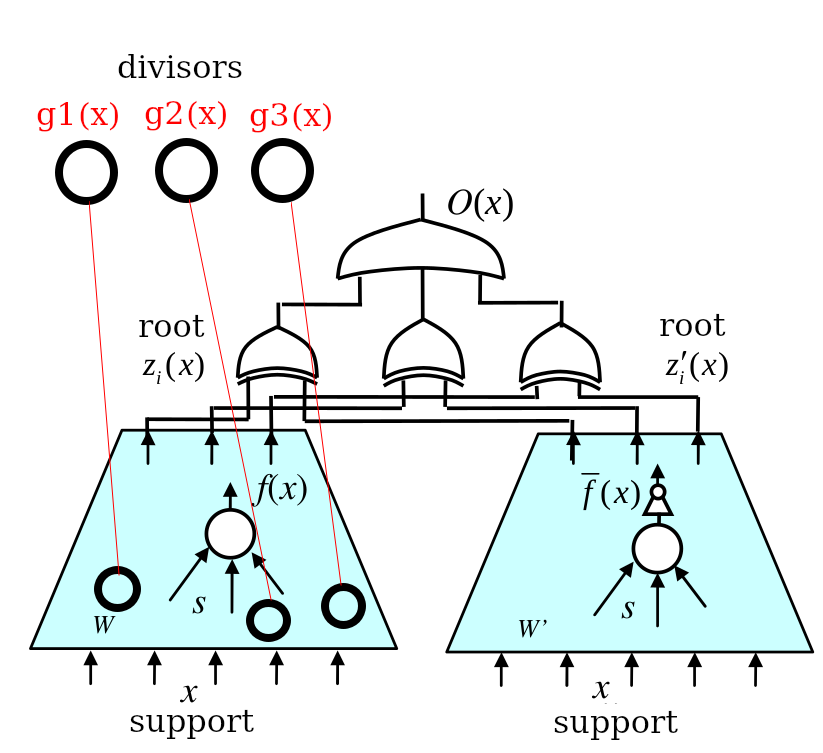
Collect *divisors* from node to PIs, avoid these nodes:

* the node itself;
* the nodes in the MFFC of .

Explore the fanouts of already collected *divisors*, and add the fanouts into *divisors* excepted for:

* a divisor has too many fanouts;
* nodes in the TFO or in the MFFC of node ;
* PO nodes;
* nodes with large level;
* nodes whose fanins are not divisors (I am not clear here);

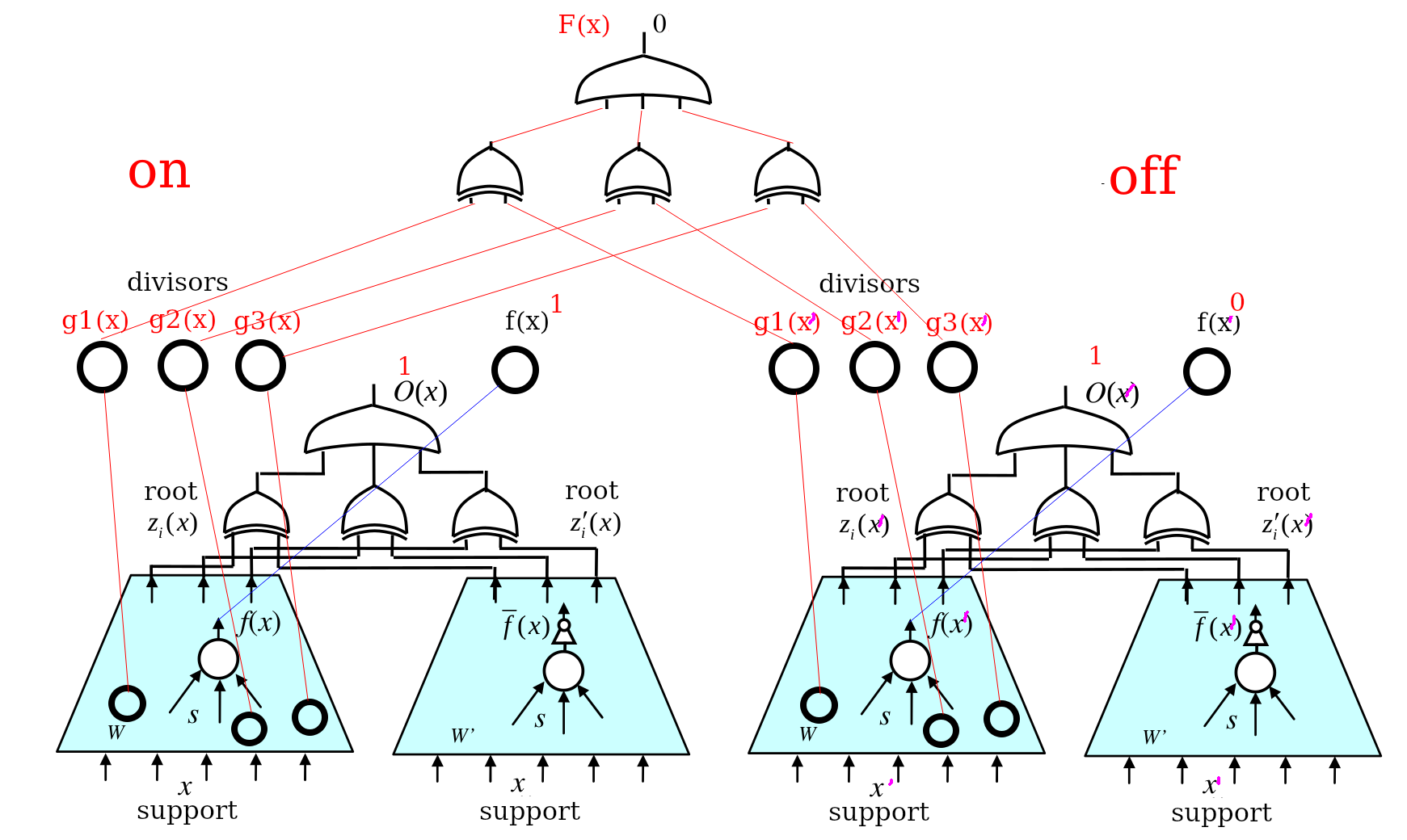
### Construct AIG for Window



If is asserted to 1, then is in the care set of .

### Translate AIG into CNF

### Create the SAT Problem



Here, and are different input patterns. and is asserted to 1, then and are in ’s care set.

We want to substitute by . The dependency function exists if and only if there is no minterm pair , where and for all . So we assert , , all .

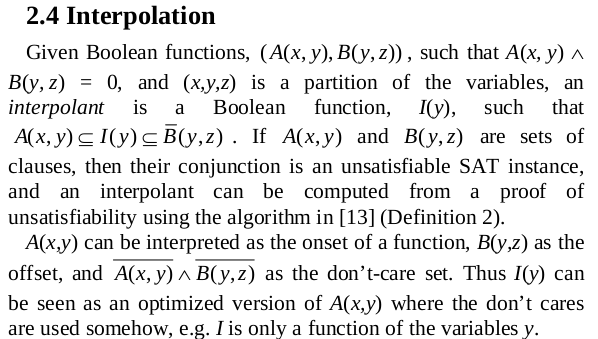
The CNF expression of the SAT problem is:

### Interpolation

To compute a function , divided into subsets and .

For unsatisfiable , the resultant interpolant derived from a refutation proof yields a desired dependency function (I am not clear here).

Definition of interpolation:



## Approximate Version of MFS

I have no mature idea up to now.