

Digital Circuits and Logical Programming



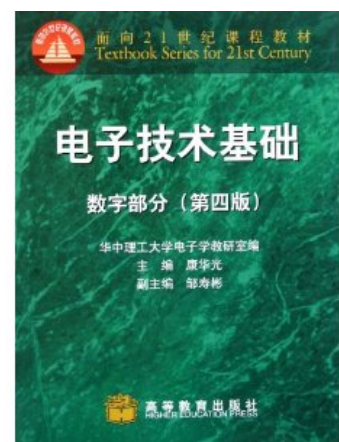
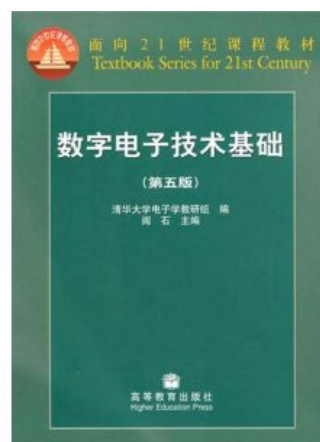
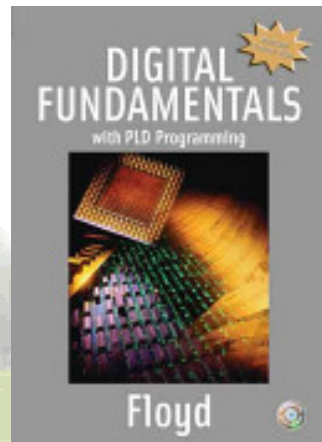
Digital Circuits and Logical Programming

- **Instructor: Dong Zhang (张东)**
- **Affiliation: IST of SYSU**
- **Email: zhangd@mail.sysu.edu.cn**
- **Office: 617-C, Building of IST**



References

- T. L. Floyd, Digital Fundamentals, Tenth Edition, Prentice Hall,
- 阎石.数字电子技术基础 (第五版).高等教育出版社,2006.5
- 康华光.电子技术基础. (数字部分) 第四版.高等教育出版社,1999.7



Resources

- ftp://172.18.184.29
- ID: ftpView
- PW: view@41
- Please use a specific FTP software to access this server
 - Upload to
 - /UpLoad/数字电路与逻辑设计_14级电自2班上载目录/
 - Download from
 - /DownLoad/数字电路与逻辑设计_14级电自2班下载目录/

Acknowledgement

- Publishing House of Electronics Industry
- Prof. D. J. Lee and Prof. B. Nelson, Dept. of Electronic and Computer Engineering, Brigham Young University
- Colleagues of IST, SYSU



Schedule

- **Class Lecture: Week 1 to Week 17**
 - Public Teaching Building: C104
- **Quiz**
- **Final Exam: Week 18**
- **Lab Section:**
 - Week 5 to Week 16
 - Lab Center: C104



Lectures

- Attendance is required
- Suggestions for success
 - Do the assigned reading BEFORE coming to class.
 - Prepare for the labs BEFORE coming to the lab.
 - Do all assignments on time.
 - DO NOT let yourself fall behind.



Grading Policy

- Homework 20%
- Attendance 10%
- Midterm or Quiz 10%
- Final Exam 60%



How to Succeed

- Do the reading carefully!
- Do all the homework carefully!
- Make sure you've mastered the skills outlined at the end of each chapter
- If you're struggling, get help from the instructor

