

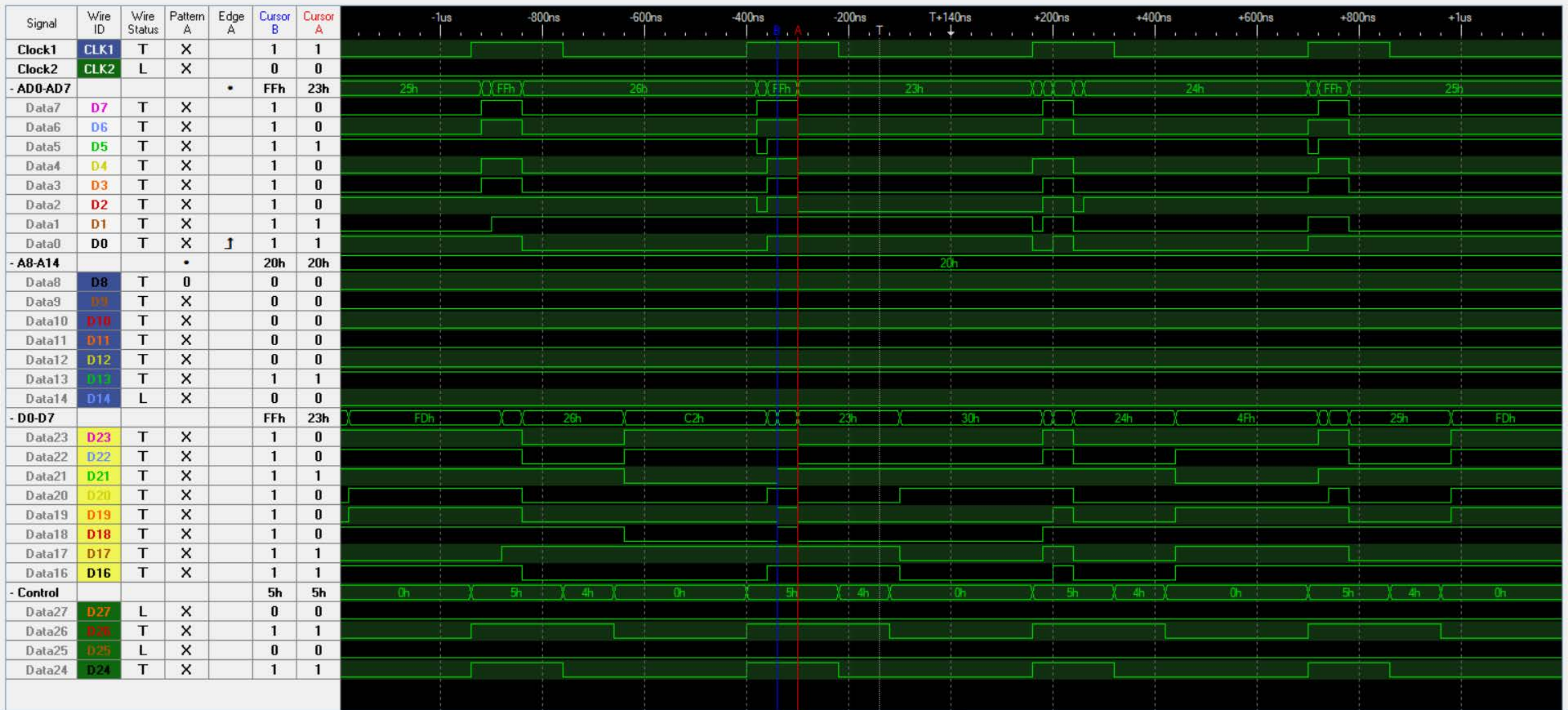
Buffer Position:

Sample Rate: 50MHz

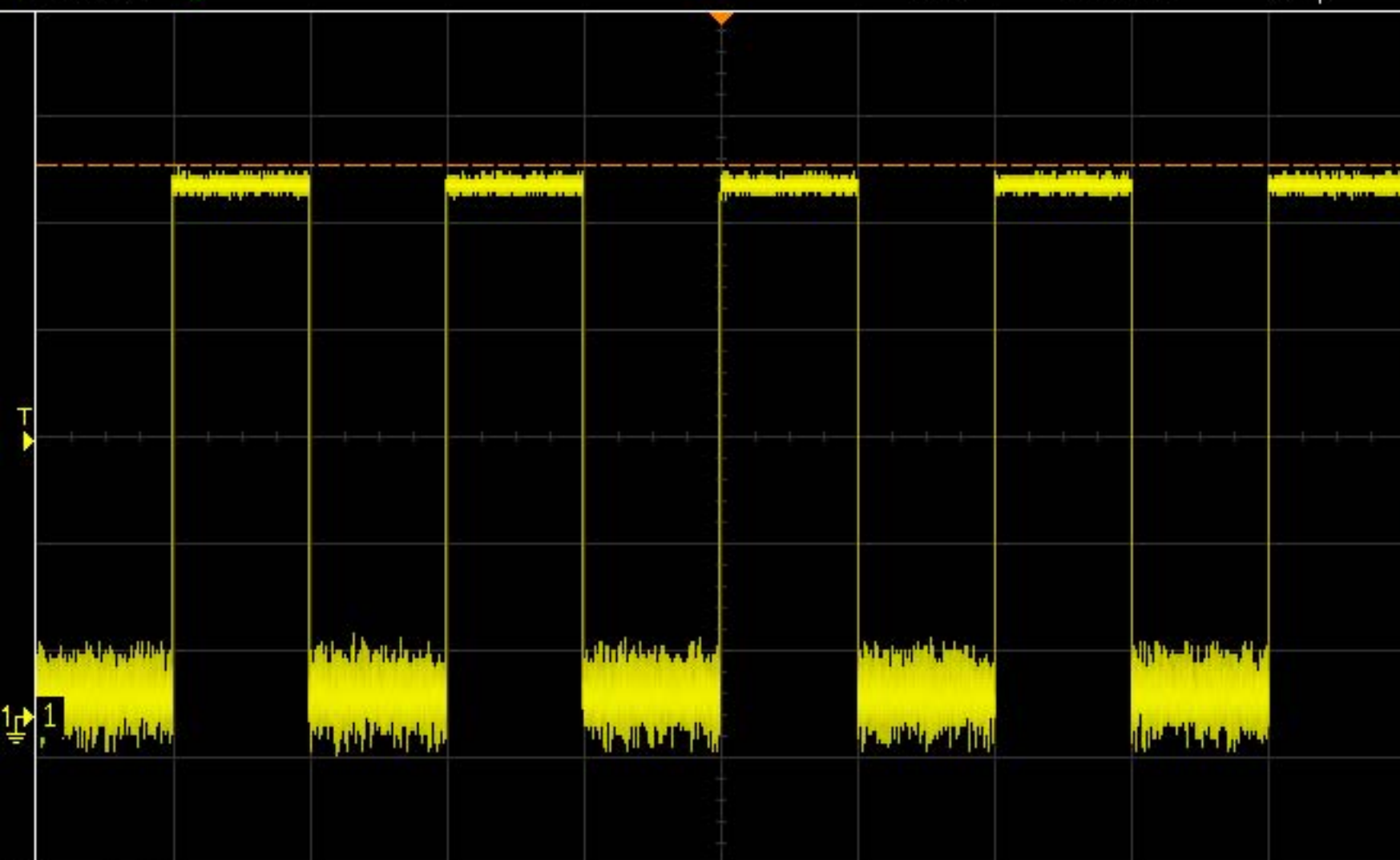
Logic Threshold: 2.50v

Pre-Trigger Buffer: 50%

Measurement A Source: CLK2



1 1.00V/ 2 0.0s 500.0ns/ Stop 1 2.57V



Acquisition

Normal

10.0kSa/s

Channels

DC 10.0:1

DC 10.0:1

Measurements

Freq(1):  
999.4mHzPeriod(1):  
1.0006sMin(1):  
-360mVMax(1):  
5.14V

Autoscale Menu

Undo  
Autoscale

Fast Debug

 Channels  
Displayed Acq Mode  
Normal