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| --- | --- | --- | --- | --- | --- | --- | --- |
|  | Monday | Tuesday | Wednesday | Thursday | Friday | Saturday | Sunday |
| 8:00  8:30 |  |  | Module: Compiler Design  Room: 11011 /  Faculty: PRATEEK GUPTA  Start Time: 08:00:00  End Time: 08:55:00  Batch  BT-CSE-Spz-CCVT-V-B2 |  |  | Module: Cloud Performance  Room: 11011 /  Faculty: VISHAL KAUSHIK  Start Time : 08:00:00  End Time : 08:55:00  Batch :  BT-CSE-Spz-CCVT-V-B2 |  |
|
| 9:00  9:30 |  | Module: Compiler Design  Room: 11011 /  Faculty: PRATEEK GUPTA  Start Time : 09:00:00  End Time: 09:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 | Module: Cloud Performance Tuning  Room: 11011 /  Faculty: VISHAL KAUSHIK  Start Time: 09:00:00  End Time : 09:55:00  Batch  BT-CSE-Spz-CCVT-V-B2 | Module: Micro Processor & Embedded Systems  Room: 11011 /  Faculty: HARSHIT MOHAN  Start Time : 09:00:00  End Time : 09:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 | Module: Micro Processor & Embedded Systems  Room: 11011  Faculty : HARSHIT MOHAN  Start Time: 09:00:00  End Time: 09:55:00 | Module: Container Orchestre  Room: 11011  Faculty: ABHIRUP KHANNA  Start Time : 09:00:00  End Time: 09:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 |  |
|
| 10:00  10:30 |  |  | Module: Business Strategy: Tools and Techniques  Room : VR\_  4104 /  Faculty: RAGHAVENDRAN S  Start Time: 10:00:00  End Time: 10:55:00 | Module: Business Strategy: Tools and Techniques  Room : VR\_  4104 /  Faculty: RAGHAVENDRAN S  Start Time: 10:00:00  End Time: 10:55:00 |  |  |  |
|
| 11:00  11:30 |  | Module : Micro Processor & Embedded Systems Lab  Room : Basic Electrical Lab-Network Theory Lab /  Faculty: PIYUSH RAWAT  Start Time: 11:00:00  End Time: 12:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 | Module: Container Orchestration & Infra Auto Lab  Room: 10005 1  Faculty: ABHIRUP KHANNA  Start Time: 11:00:00  End Time : 12:55:00  Baren•  BT-CSE-Spz-CCVT-V-B2 | Module: Container Orchestration & Infra Auto Lab  Room: 10005 1  Faculty: ABHIRUP KHANNA  Start Time: 11:00:00  End Time : 12:55:00  Baren•  BT-CSE-Spz-CCVT-V-B2 |  |  |  |
|
| 12:00  12:30 |  | Module : Micro Processor & Embedded Systems Lab  Room : Basic Electrical Lab-Network Theory Lab /  Faculty: PIYUSH RAWAT  Start Time: 11:00:00  End Time: 12:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 | Module: Container Orchestration & Infra Auto Lab  Room: 10005 1  Faculty: ABHIRUP KHANNA  Start Time: 11:00:00  End Time : 12:55:00  Baren•  BT-CSE-Spz-CCVT-V-B2 | Module: Micro Processor & Embedded Systems  Room: 11013 /  Faculty: HARSHIT MOHAN  Start Time : 12:00:00  End Time: 12:55:00  Batch:  BT-CSE-Spz-CCVT-V-B2 |  |  |  |
|
| 1:00  1:30 |  |  |  | Module: Cloud Performance Tuning Lab  Room: IT 201 /  Faculty: VISHAL KAUSHIK  Start Time: 13:00:00  End Time: 14:55:00  batch  BT-CSE-Spz-CCVT-V-B2 |  |  |  |
|
| 2:00  2:30 |  | Module: Information Security Fundamentals  Room: 9108 /  Faculty: PANKAJ KUNDAN DADURE  Start Time : 14:00:00  End Time : 14:55:00 | Module: Information Security Fundamentals  Room : 9207 /  Faculty: PANKAJ KUNDAN DADURE  Start Time: 14:00:00  End Time: 14:55:00  Batch: |  |  |  |  |
|
| 3:00  3:30 |  | Module: EDGE - Advance Communication  Room: 11114 /  Faculty : GUPTA TARUNI  Start Time: 15:00:00  End Time: 15:55:00 |  | Module: Container Orchestration & Infra. Auto.  Room: 11011 7  Faculty: ABHIRUP KHANNA  End Time: 15:55:00 |  |  |  |
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