# **Collaborative Processor Performance Control (CPPC)**

### **CPPC**

CPPC defined in the ACPI spec describes a mechanism for the OS to manage the performance of a logical processor on a contiguous and abstract performance scale. CPPC exposes a set of registers to describe abstract performance scale, to request performance levels and to measure per-cpu delivered performance.

For more details on CPPC please refer to the ACPI specification at:

#### http://uefi.org/specifications

Some of the CPPC registers are exposed via sysfs under:

```
/sys/devices/system/cpu/cpuX/acpi cppc/
```

#### for each cpu X:

- highest\_perf: Highest performance of this processor (abstract scale).
- nominal perf: Highest sustained performance of this processor (abstract scale).
- lowest nonlinear perf: Lowest performance of this processor with nonlinear power savings (abstract scale).
- lowest perf: Lowest performance of this processor (abstract scale).
- lowest\_freq: CPU frequency corresponding to lowest\_perf (in MHz).
- nominal\_freq: CPU frequency corresponding to nominal\_perf (in MHz). The above frequencies should only be used to report processor performance in frequency instead of abstract scale. These values should not be used for any functional decisions.
- feedback\_ctrs: Includes both Reference and delivered performance counter. Reference counter ticks up proportional to processor's reference performance. Delivered counter ticks up proportional to processor's delivered performance.
- wraparound time: Minimum time for the feedback counters to wraparound (seconds).
- reference\_perf: Performance level at which reference performance counter accumulates (abstract scale).

## **Computing Average Delivered Performance**

Below describes the steps to compute the average performance delivered by taking two different snapshots of feedback counters at time T1 and T2.