# **Tracepoints in ALSA**

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### Tracepoints in ALSA PCM core

ALSA PCM core registers <code>snd\_pcm</code> subsystem to kernel tracepoint system. This subsystem includes two categories of tracepoints; for state of PCM buffer and for processing of PCM hardware parameters. These tracepoints are available when corresponding kernel configurations are enabled. When <code>CONFIG\_SND\_DEBUG</code> is enabled, the latter tracepoints are available. When additional <code>SND\_PCM\_XRUN\_DEBUG</code> is enabled too, the former trace points are enabled.

#### Tracepoints for state of PCM buffer

This category includes four tracepoints; hwptr, applptr, xrun and hw ptr error.

#### Tracepoints for processing of PCM hardware parameters

This category includes two tracepoints; hw mask param and hw interval param.

In a design of ALSA PCM core, data transmission is abstracted as PCM substream. Applications manage PCM substream to maintain data transmission for PCM frames. Before starting the data transmission, applications need to configure PCM substream. In this procedure, PCM hardware parameters are decided by interaction between applications and ALSA PCM core. Once decided, runtime of the PCM substream keeps the parameters.

The parameters are described in struct snd\_pcm\_hw\_params. This structure includes several types of parameters. Applications set preferable value to these parameters, then execute ioctl(2) with SNDRV\_PCM\_IOCTL\_HW\_REFINE or SNDRV\_PCM\_IOCTL\_HW\_PARAMS. The former is used just for refining available set of parameters. The latter is used for an actual decision of the parameters.

The struct snd pcm hw params structure has below members:

flags

Configurable. ALSA PCM core and some drivers handle this flag to select convenient parameters or change their behaviour.

masks

Configurable. This type of parameter is described in struct snd\_mask and represent mask values. As of PCM protocol v2.0.13, three types are defined.

- SNDRV PCM HW PARAM ACCESS
- SNDRV PCM HW PARAM FORMAT
- SNDRV PCM HW PARAM SUBFORMAT

intervals

Configurable. This type of parameter is described in struct snd\_interval and represent values with a range. As of PCM protocol v2.0.13, twelve types are defined.

- SNDRV\_PCM\_HW\_PARAM\_SAMPLE\_BITS
- SNDRV\_PCM\_HW\_PARAM\_FRAME\_BITS
- SNDRV PCM HW PARAM CHANNELS
- SNDRV\_PCM\_HW\_PARAM\_RATE
- SNDRV PCM HW PARAM PERIOD TIME
- SNDRV PCM HW PARAM PERIOD SIZE
- SNDRV PCM HW PARAM PERIOD BYTES
- SNDRV PCM HW PARAM PERIODS
- SNDRV\_PCM\_HW\_PARAM\_BUFFER\_TIME
- SNDRV PCM HW PARAM BUFFER SIZE
- SNDRV PCM HW PARAM BUFFER BYTES
- SNDRV\_PCM\_HW\_PARAM\_TICK\_TIME

rmask

Configurable. This is evaluated at ioctl(2) with SNDRV\_PCM\_IOCTL\_HW\_REFINE only. Applications can select which mask/interval parameter can be changed by ALSA PCM core. For SNDRV\_PCM\_IOCTL\_HW\_PARAMS, this mask is ignored and all of parameters are going to be changed.

cmask

Read-only. After returning from ioctl(2), buffer in user space for struct snd\_pcm\_hw\_params includes result of each operation. This mask represents which mask/interval parameter is actually changed.

info

Read-only. This represents hardware/driver capabilities as bit flags with SNDRV\_PCM\_INFO\_XXX. Typically, applications execute ioctl(2) with SNDRV\_PCM\_IOCTL\_HW\_REFINE to retrieve this flag, then decide candidates of parameters and execute ioctl(2) with SNDRV\_PCM\_IOCTL\_HW\_PARAMS to configure PCM substream

mshits

Read-only. This value represents available bit width in MSB side of a PCM sample. When a parameter of SNDRV\_PCM\_HW\_PARAM\_SAMPLE\_BITS was decided as a fixed number, this value is also calculated according to it. Else, zero. But this behaviour depends on implementations in driver side.

rate num

Read-only. This value represents numerator of sampling rate in fraction notation. Basically, when a parameter of SNDRV\_PCM\_HW\_PARAM\_RATE was decided as a single value, this value is also calculated according to it. Else, zero. But this behaviour depends on implementations in driver side.

rate\_den

Read-only. This value represents denominator of sampling rate in fraction notation. Basically, when a parameter of SNDRV\_PCM\_HW\_PARAM\_RATE was decided as a single value, this value is also calculated according to it. Else, zero. But this behaviour depends on implementations in driver side.

fifo\_size

Read-only. This value represents the size of FIFO in serial sound interface of hardware. Basically, each driver can assigns a proper value to this parameter but some drivers intentionally set zero with a care of hardware design or data transmission protocol.

ALSA PCM core handles buffer of struct snd\_pcm\_hw\_params when applications execute ioctl(2) with SNDRV\_PCM\_HW\_REFINE or SNDRV\_PCM\_HW\_PARAMS. Parameters in the buffer are changed according to struct snd\_pcm\_hardware and rules of constraints in the runtime. The structure describes capabilities of handled hardware. The rules describes dependencies on which a parameter is decided according to several parameters. A rule has a callback function, and drivers can register arbitrary functions to compute the target parameter. ALSA PCM core registers some rules to the runtime as a default.

Each driver can join in the interaction as long as it prepared for two stuffs in a callback of struct snd pcm ops.open.

- 1. In the callback, drivers are expected to change a member of struct snd\_pcm\_hardware type in the runtime, according to capacities of corresponding hardware.
- 2. In the same callback, drivers are also expected to register additional rules of constraints into the runtime when several parameters have dependencies due to hardware design.

The driver can refers to result of the interaction in a callback of struct snd\_pcm\_ops.hw\_params, however it should not change the content.

Tracepoints in this category are designed to trace changes of the mask/interval parameters. When ALSA PCM core changes them, hw mask param or hw interval parameter is probed according to type of the changed parameter.

ALSA PCM core also has a pretty print format for each of the tracepoints. Below is an example for hw mask param.

Below is an example for hw interval param.

```
hw interval param: pcmCODOp 000/023 BUFFER SIZE 0 0 [0 4294967295] 0 1 [0 4294967295]
```

The first three fields are common. They represent name of ALSA PCM character device, rules of constraint and name of the changed parameter, in order. The field for rules of constraint consists of two sub-fields; index of applied rule and total number of rules added to the runtime. As an exception, the index 000 means that the parameter is changed by ALSA PCM core, regardless of the rules.

The rest of field represent state of the parameter before/after changing. These fields are different according to type of the parameter. For parameters of mask type, the fields represent hexadecimal dump of content of the parameter. For parameters of interval type, the fields represent values of each member of empty, integer, openmin, min, max, openmax in struct snd\_interval in this order.

## Tracepoints in drivers

Some drivers have tracepoints for developers' convenience. For them, please refer to each documentation or implementation.