KVM CPUID bits

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A guest running on a kvm host, can check some of its features using cpuid. This is not always guaranteed to work, since userspace can mask-out some, or even all KVM-related cpuid features before launching a guest.

KVM cpuid functions are:

function: KVM_CPUID_SIGNATURE (0x40000000)

returns:

```
eax = 0x40000001

ebx = 0x4b4d564b

ecx = 0x564b4d56

edx = 0x4d
```

Note that this value in ebx, ecx and edx corresponds to the string "KVMKVMKVM". The value in eax corresponds to the maximum cpuid function present in this leaf, and will be updated if more functions are added in the future. Note also that old hosts set eax value to 0x0. This should be interpreted as if the value was 0x40000001. This function queries the presence of KVM cpuid leafs.

function: define KVM_CPUID_FEATURES (0x40000001)

returns:

```
ebx, ecx
eax = an OR'ed group of (1 << flag)</pre>
```

where flag is defined as below:

value	meaning
0	kvmclock available at msrs 0x11 and 0x12
1	not necessary to perform delays on PIO operations
2	deprecated
3	kvmclock available at msrs 0x4b564d00 and 0x4b564d01
4	async pf can be enabled by writing to msr 0x4b564d02
5	steal time can be enabled by writing to msr 0x4b564d03
6	paravirtualized end of interrupt handler can be enabled by writing to msr 0x4b564d04
7	guest checks this feature bit before enabling paravirtualized spinlock support
9	guest checks this feature bit before enabling paravirtualized tlb flush
10	paravirtualized async PF VM EXIT can be enabled by setting bit 2 when writing to msr 0x4b564d02
11	guest checks this feature bit before enabling paravirtualized send IPIs
12	host-side polling on HLT can be disabled by writing to msr 0x4b564d05.
13	guest checks this feature bit before using paravirtualized sched yield.
14	guest checks this feature bit before using the second async pf control msr 0x4b564d06 and async pf acknowledgment msr 0x4b564d07.
15	guest checks this feature bit before using extended destination ID bits in MSI address bits 11-5.
16	guest checks this feature bit before using the map gpa range hypercall to notify the page state change
17	guest checks this feature bit before using MSR_KVM_MIGRATION_CONTROL
24	host will warn if no guest-side per-cpu warps are expected in kymclock
	0 1 2 3 4 5 6 7 9 10 11 12 13 14 15 16

```
edx = an OR'ed group of (1 << flag)
```

Where flag here is defined as below:

flag	value	meaning

flag	value	meaning
KVM_HINTS_REALTIME	0	guest checks this feature bit to determine that vCPUs are never preempted for an unlimited time allowing optimizations