Kernel driver w83795

Supported chips:

• Winbond/Nuvoton W83795G

Prefix: 'w83795g'

Addresses scanned: I2C 0x2c - 0x2f

Datasheet: Available for download on nuvoton.com

• Winbond/Nuvoton W83795ADG

Prefix: 'w83795adg'

Addresses scanned: I2C 0x2c - 0x2f

Datasheet: Available for download on nuvoton.com

Authors:

• Wei Song (Nuvoton)

• Jean Delvare < jdelvare@suse.de>

Pin mapping

Here is a summary of the pin mapping for the W83795G and W83795ADG. This can be useful to convert data provided by board manufacturers into working libsensors configuration statements.

• W83795G

Pin	Name	Register	Sysfs attribute
13	VSEN1 (VCORE1)	10h	in0
14	VSEN2 (VCORE2)	11h	in1
15	VSEN3 (VCORE3)	12h	in2
16	VSEN4	13h	in3
17	VSEN5	14h	in4
18	VSEN6	15h	in5
19	VSEN7	16h	in6
20	VSEN8	17h	in7
21	VSEN9	18h	in8
22	VSEN10	19h	in9
23	VSEN11	1Ah	in10
28	VTT	1Bh	in11
24	3VDD	1Ch	in12
25	3VSB	1Dh	in13
26	VBAT	1Eh	in14
3	VSEN12/TR5	1Fh	in15/temp5
4	VSEN13/TR5	20h	in16/temp6
5/6	VDSEN14/TR1/TD1	21h	in17/temp1
7/8	VDSEN15/TR2/TD2	22h	in18/temp2
9/ 10	VDSEN16/TR3/TD3	23h	in19/temp3
11/12	VDSEN17/TR4/TD4	24h	in20/temp4
40	FANIN1	2Eh	fan1
42	FANIN2	2Fh	fan2
44	FANIN3	30h	fan3
46	FANIN4	31h	fan4
48	FANIN5	32h	fan5
50	FANIN6	33h	fan6
52	FANIN7	34h	fan7
54	FANIN8	35h	fan8
57	FANIN9	36h	fan9
58	FANIN10	37h	fan10
59	FANIN11	38h	fan11
60	FANIN12	39h	fan12
31	FANIN13	3Ah	fan13
35	FANIN14	3Bh	fan14

Pin	Name	Register	Sysfs attribute
41	FANCTL1	10h (bank 2)	pwml
43	FANCTL2	11h (bank 2)	pwm2
45	FANCTL3	12h (bank 2)	pwm3
47	FANCTL4	13h (bank 2)	pwm4
49	FANCTL5	14h (bank 2)	pwm5
51	FANCTL6	15h (bank 2)	pwm6
53	FANCTL7	16h (bank 2)	pwm7
55	FANCTL8	17h (bank 2)	pwm8
29/30	PECI/TSI (DTS1)	26h	temp7
29/30	PECI/TSI (DTS2)	27h	temp8
29/30	PECI/TSI (DTS3)	28h	temp9
29/30	PECI/TSI (DTS4)	29h	temp10
29/30	PECI/TSI (DTS5)	2Ah	temp11
29/30	PECI/TSI (DTS6)	2Bh	temp12
29/30	PECI/TSI (DTS7)	2Ch	temp13
29/30	PECI/TSI (DTS8)	2Dh	temp14
27	CASEOPEN#	46h	intrusion0

• W83795ADG

• W83/95ADG					
Name	Register	Sysfs attribute			
VSEN1 (VCORE1)	10h	in0			
VSEN2 (VCORE2)	11h	in1			
VSEN3 (VCORE3)	12h	in2			
VSEN4	13h	in3			
VSEN5	14h	in4			
VSEN6	15h	in5			
VSEN7	16h	in6			
VSEN8	17h	in7			
VTT	1Bh	inl 1			
3VDD	1Ch	in12			
3VSB	1Dh	in13			
VBAT	1Eh	in14			
VSEN12/TR5	1Fh	in15/temp5			
VSEN13/TR5	20h	in16/temp6			
VDSEN14/TR1/TD1	21h	in17/temp1			
VDSEN15/TR2/TD2	22h	in18/temp2			
VDSEN16/TR3/TD3	23h	in19/temp3			
VDSEN17/TR4/TD4	24h	in20/temp4			
FANIN1	2Eh	fan1			
FANIN2	2Fh	fan2			
FANIN3	30h	fan3			
FANIN4	31h	fan4			
FANIN5	32h	fan5			
FANIN6	33h	fan6			
FANIN7	34h	fan7			
FANIN8	35h	fan8			
FANIN9	36h	fan9			
FANIN10	37h	fan10			
FANIN11	38h	fan11			
FANIN12	39h	fan12			
FANIN13	3Ah	fan13			
FANIN14	3Bh	fan14			
FANCTL1	10h (bank 2)	pwml			
FANCTL2	11h (bank 2)	pwm2			
PECI (DTS1)	26h	temp7			
PECI (DTS2)	27h	temp8			
		temp9			
		temp10			
		temp11			
PECI (DTS6)	2Bh	temp12			
	Name VSEN1 (VCORE1) VSEN2 (VCORE2) VSEN3 (VCORE3) VSEN4 VSEN5 VSEN6 VSEN7 VSEN8 VTT 3VDD 3VSB VBAT VSEN12/TR5 VSEN13/TR5 VDSEN14/TR1/TD1 VDSEN15/TR2/TD2 VDSEN16/TR3/TD3 VDSEN16/TR3/TD3 VDSEN16/TR3/TD3 VDSEN16/TR4/TD4 FANIN1 FANIN2 FANIN3 FANIN4 FANIN5 FANIN6 FANIN7 FANIN8 FANIN10 FANIN11 FANIN12 FANIN13 FANIN14 FANCTL2 PECI (DTS1) PECI (DTS2) PECI (DTS4) PECI (DTS5)	Name Register VSEN1 (VCORE1) 10h VSEN2 (VCORE2) 11h VSEN3 (VCORE3) 12h VSEN4 13h VSEN5 14h VSEN6 15h VSEN7 16h VSEN8 17h VTT 1Bh 3VDD 1Ch 3VSB 1Dh VBAT 1Eh VSEN12/TR5 1Fh VSEN13/TR5 20h VDSEN14/TR1/TD1 21h VDSEN15/TR2/TD2 22h VDSEN16/TR3/TD3 23h VDSEN16/TR3/TD3 23h VDSEN17/TR4/TD4 24h FANIN1 2Eh FANIN2 2Fh FANIN3 30h FANIN4 31h FANIN5 32h FANIN6 33h FANIN7 34h FANIN10 37h FANIN11 38h FANIN12 39h FANIN13 3Ah			

Pin	Name	Register	Sysfs attribute
23	PECI (DTS7)	2Ch	temp13
23	PECI (DTS8)	2Dh	temp14
21	CASEOPEN#	46h	intrusion0