# ioctl VIDIOC\_DBG\_G\_REGISTER, VIDIOC DBG S REGISTER

System Message: ERROR/3 (D:\onboarding-resources\sample-onboarding-resources\linux-master\Documentation\userspace-api\media\v41\(linux-master\) (Documentation) (userspace-api) (media) (v41) vidioc-dbg-g-register.rst, line 2)
Unknown directive type "c:namespace".
.. c:namespace:: V4L

## Name

VIDIOC DBG G REGISTER - VIDIOC DBG S REGISTER - Read or write hardware registers

# **Synopsis**

```
System Message: ERROR/3 (D:\onboarding-resources\sample-onboarding-resources\linux-master\Documentation\userspace-api\media\v41\(linux-master\) (Documentation) (userspace-api) (media) (v41) vidioc-dbg-g-register.rst, line 18)

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.. c:macro:: VIDIOC_DBG_G_REGISTER
```

int ioctl(int fd, VIDIOC DBG G REGISTER, struct v412 dbg register \*argp)

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Unknown directive type "c:macro".

.. c:macro:: VIDIOC\_DBG\_S\_REGISTER

int ioctl(int fd, VIDIOC DBG S REGISTER, const struct v412 dbg register \*argp)

## **Arguments**

fd

File descriptor returned by :c:func:'open()'.

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argp

Pointer to struct :c:type:'v412\_dbg\_register'.

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# **Description**

#### Note

This is an <a href="ref">ref</a> experimental interface and may change in the future.

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For driver debugging purposes these ioctls allow test applications to access hardware registers directly. Regular applications must not use them.

Since writing or even reading registers can jeopardize the system security, its stability and damage the hardware, both ioctls require superuser privileges. Additionally the Linux kernel must be compiled with the <code>CONFIG\_VIDEO\_ADV\_DEBUG</code> option to enable these ioctls.

To write a register applications must initialize all fields of a struct <code>:c.type:`v4l2\_dbg\_register`</code> except for <code>size</code> and call <code>VIDIOC\_DBG\_S\_REGISTER</code> with a pointer to this structure. The <code>match.type</code> and <code>match.addr</code> or <code>match.name</code> fields select a chip on the TV card, the <code>reg</code> field specifies a register number and the <code>val</code> field the value to be written into the register.

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Unknown interpreted text role "c:type".

To read a register applications must initialize the match.type, match.addr or match.name and reg fields, and call <code>VIDIOC\_DBG\_G\_REGISTER</code> with a pointer to this structure. On success the driver stores the register value in the <code>val</code> field and the size (in bytes) of the value in <code>size</code>.

When match.type is V4L2\_CHIP\_MATCH\_BRIDGE, match.addr selects the nth non-sub-device chip on the TV card. The number zero always selects the host chip, e. g. the chip connected to the PCI or USB bus. You can find out which chips are present with the ref: VIDIOC\_DBG\_G\_CHIP\_INFO` ioctl.

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When match.type is V4L2 CHIP MATCH SUBDEV, match.addr selects the nth sub-device.

These ioctls are optional, not all drivers may support them. However when a driver supports these ioctls it must also support ref. VIDIOC DBG G CHIP INFO. Conversely it may support VIDIOC DBG G CHIP INFO but not these ioctls.

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VIDIOC\_DBG\_G\_REGISTER and VIDIOC\_DBG\_S\_REGISTER were introduced in Linux 2.6.21, but their API was changed to the one described here in kernel 2.6.29.

We recommended the v412-dbg utility over calling these ioctls directly. It is available from the LinuxTV v41-dvb repository; see <a href="https://linuxtv.org/repo/">https://linuxtv.org/repo/</a> for access instructions.

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.. tabularcolumns:: |p{3.5cm}|p{3.5cm}|p{6.6cm}|

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Unknown directive type "c:type".

```
.. c:type:: v412_dbg_match
```

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```
.. flat-table:: struct v412 dbg match
   :header-rows: 0
   :stub-columns: 0
   :widths:
                  1 1 2
         u32
     - ``type``
     - See :ref:`chip-match-types` for a list of possible types.
      - (anonymous)
     - __u32
- ``addr`
     - Match a chip by this number, interpreted according to the ``type``
    * - char
     - ``name[32]``
     - Match a chip by this name, interpreted according to the ``type``
       field. Currently unused.
    * - }
```

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Unknown directive type "c:type".

```
.. c:type:: v4l2_dbg_register
```

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Unknown directive type "flat-table".

```
.. flat-table:: struct v412_dbg_register
    :header-rows: 0
    :stub-columns: 0

* - struct v412_dbg_match
    - ``match``
    - How to match the chip, see :c:type:`v412_dbg_match`.

* - _ u32
    - ``size``
    - The register size in bytes.

* - _ u64
    - ``reg``
    - A register number.

* - _ u64
    - ``val``
    - The value read from, or to be written into the register.
```

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### **Return Value**

On success 0 is returned, on error -1 and the errno variable is set appropriately. The generic error codes are described at the ref. Generic Error Codes <gen-errors>` chapter.

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#### **EPERM**

Insufficient permissions. Root privileges are required to execute these ioctls.