## **Operators with Channels Last support**

## **CPU** operators

- abs
- abs\_
- acos
- acos\_
- add
- add\_
- addcdiv
- addcdiv\_
- addcmul
- addcmul
- asin
- asin\_
- atan2
- atan2\_
- batch\_norm
- bfloat16
- bool
- byte
- ceil
- ceil\_
- char
- clone
- contiguous
- copy\_
- digamma
- digamma
- div
- div\_
- double
- empty\_like
- erfinv
- erfinv
- expm1
- expm1\_
- fill\_
- float
- floor
- floor\_
- full\_like
- gt
- half
- int

- isnan
- log
- log\_
- log10
- log10\_
- log1p
- log1p\_
- log2
- log2\_
- long
- lt
- mul
- mul\_
- ne
- neg
- neg\_
- ones\_like
- pow
- pow\_
- randint\_like
- rand\_like
- randn\_like
- relu
- relu\_
- resize\_
- resize\_as\_
- round
- round
- rsqrt
- rsqrt\_
- short
- sigmoid
- sigmoid\_
- sign
- sign\_
- sin
- sin
- sinh
- sinh\_
- sqrt
- sqrt\_
- to
- trunc
- trunc\_
- type
- zero\_

## **GPU** operators

- abs
- abs\_
- acos
- acos\_
- adaptive\_avg\_pool2d
- add
- add\_
- addcdiv
- addcdiv
- addcmul
- addcmul\_
- asin
- asin\_
- atan2
- atan2\_
- batch\_norm
- bfloat16
- bool
- byte
- cat
- ceil
- ceil\_
- char
- clone
- contiguous
- conv2d
- conv\_transpose2d
- copy\_
- cpu
- cuda
- cudnn\_convolution
- cudnn\_convolution\_transpose
- detach
- digamma
- digamma\_
- div
- div\_
- double
- empty\_like
- erfinv
- erfinv\_
- expm1
- expm1\_

- fill\_
- float
- floor
- floor\_
- full\_like
- gt
- half
- int
- isnan
- log
- log\_
- log10
- log10\_
- log1p
- log1p\_
- log2
- log2\_
- long
- 101
- lt
- max\_pool2d
- mul
- mul\_
- ne
- neg
- neg\_
- ones\_like
- pow
- pow\_
- randint\_like
- rand\_like
- randn\_like
- relu
- relu\_
- requires\_grad\_
- resize\_
- resize\_as\_
- round
- round\_
- rsqrt
- rsqrt\_
- short
- sigmoid
- sigmoid\_
- sign
- sign\_
- sin

- sin\_
- sinh
- sinh\_
- sqrt
- sqrt\_
- to
- trunc
- trunc\_
- type
- zero\_
- zeros\_like