Automatic Mixed Precision package - torch.amp

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Unknown directive type "py:module".

.. py:module:: torch.cpu

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Unknown directive type "py:module".

.. py:module:: torch.cpu.amp

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Unknown directive type "py:module".

.. py:module:: torch.cuda.amp

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Unknown directive type "automodule".

.. automodule:: torch.amp

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Unknown directive type "currentmodule".

.. currentmodule:: torch.amp

class: torch.amp' provides convenience methods for mixed precision, where some operations use the torch.float32 (float) datatype and other operations use lower precision floating point datatype (lower_precision_fp): torch.float16 (half) or torch.bfloat16. Some ops, like linear layers and convolutions, are much faster in lower_precision_fp. Other ops, like reductions, often require the dynamic range of float32. Mixed precision tries to match each op to its appropriate datatype.

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Unknown interpreted text role "class".

Ordinarily, "automatic mixed precision training" with datatype of torch.float16 uses: class: torch.autocast` and class: torch.cuda.amp.GradScaler` together, as shown in the ref. CUDA Automatic Mixed Precision examples amp-examples and CUDA Automatic Mixed Precision recipe. However, class: torch.autocast` and class: torch.cuda.amp.GradScaler` are modular, and may be used separately if desired.

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For CUDA and CPU, APIs are also provided seperately:

- torch.autocast("cuda", args...) is equivalent to torch.cuda.amp.autocast(args...).
- torch.autocast ("cpu", args...) is equivalent to torch.cpu.amp.autocast (args...). For CPU, only lower precision floating point datatype of torch.bfloat16 is supported for now.
- Autocasting
- Gradient Scaling
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 - CUDA Ops that can autocast to float16
 - CUDA Ops that can autocast to float32
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 - Prefer binary cross entropy with logits over binary cross entropy
 - CPU Op-Specific Behavior
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Autocasting

```
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```

Unknown directive type "currentmodule".

```
.. currentmodule:: torch
```

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Unknown directive type "autoclass".

```
.. autoclass:: autocast
:members:
```

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Unknown directive type "currentmodule".

```
.. currentmodule:: torch.cuda.amp
```

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Unknown directive type "autoclass".

```
.. autoclass:: autocast
:members:
```

```
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Unknown directive type "autofunction".

... autofunction:: custom_fwd
```

```
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Unknown directive type "autofunction".
```

```
.. autofunction:: custom_bwd
```

```
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Unknown directive type "currentmodule".

.. currentmodule:: torch.cpu.amp
```

```
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```

```
Unknown directive type "autoclass".
```

```
.. autoclass:: autocast
   :members:
```

Gradient Scaling

If the forward pass for a particular op has float16 inputs, the backward pass for that op will produce float16 gradients. Gradient values with small magnitudes may not be representable in float16. These values will flush to zero ("underflow"), so the update for the corresponding parameters will be lost.

To prevent underflow, "gradient scaling" multiplies the network's loss(es) by a scale factor and invokes a backward pass on the scaled loss(es). Gradients flowing backward through the network are then scaled by the same factor. In other words, gradient values have a larger magnitude, so they don't flush to zero.

Each parameter's gradient (.grad attribute) should be unscaled before the optimizer updates the parameters, so the scale factor does not interfere with the learning rate.

```
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Unknown directive type "currentmodule".

... currentmodule:: torch.cuda.amp
```

```
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Unknown directive type "autoclass".

... autoclass:: GradScaler
:members:
```

Autocast Op Reference

Op Eligibility

Ops that run in float64 or non-floating-point dtypes are not eligible, and will run in these types whether or not autocast is enabled. Only out-of-place ops and Tensor methods are eligible. In-place variants and calls that explicitly supply an out-... Tensor are

allowed in autocast-enabled regions, but won't go through autocasting. For example, in an autocast-enabled region a .addmm (b, c) can autocast, but a .addmm_(b, c) and a .addmm (b, c, out=d) cannot. For best performance and stability, prefer out-of-place ops in autocast-enabled regions.

Ops called with an explicit dtype=... argument are not eligible, and will produce output that respects the dtype argument.

CUDA Op-Specific Behavior

The following lists describe the behavior of eligible ops in autocast-enabled regions. These ops always go through autocasting whether they are invoked as part of a :class:`torch.nn.Module`, as a function, or as a :class:`torch.Tensor` method. If functions are exposed in multiple namespaces, they go through autocasting regardless of the namespace.

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Ops not listed below do not go through autocasting. They run in the type defined by their inputs. However, autocasting may still change the type in which unlisted ops run if they're downstream from autocasted ops.

If an op is unlisted, we assume it's numerically stable in float16. If you believe an unlisted op is numerically unstable in float16, please file an issue.

CUDA Ops that can autocast to float16

__matmul__, addbmm, addmw, addr, baddbmm, bmm, chain_matmul, multi_dot, conv1d, conv2d, conv3d, conv_transpose1d, conv_transpose2d, conv_transpose3d, GRUCell, linear, LSTMCell, matmul, mm, mv, prelu, RNNCell

CUDA Ops that can autocast to float32

__pow__, __rdiv__, __rpow__, __rtruediv__, acos, asin, binary_cross_entropy_with_logits, cosh, cosine_embedding_loss, cdist, cosine_similarity, cross_entropy, cumprod, cumsum, dist, erfinv, exp, expm1, group_norm, hinge_embedding_loss, kl_div, ll_loss, layer_norm, log, log_softmax, log10, log1p, log2, margin_ranking_loss, mse_loss, multilabel_margin_loss, multi_margin_loss, nll_loss, norm, normalize, pdist, poisson_nll_loss, pow, prod, reciprocal, rsqrt, sinh, smooth_ll_loss, soft_margin_loss, softmax, softmin, softplus, sum, renorm, tan, triplet margin_loss

CUDA Ops that promote to the widest input type

These ops don't require a particular dtype for stability, but take multiple inputs and require that the inputs' dtypes match. If all of the inputs are float16, the op runs in float16. If any of the inputs is float32, autocast casts all inputs to float32 and runs the op in float32.

addcdiv, addcmul, atan2, bilinear, cross, dot, grid sample, index put, scatter add, tensordot

Some ops not listed here (e.g., binary ops like add) natively promote inputs without autocasting's intervention. If inputs are a mixture of float16 and float32, these ops run in float32 and produce float32 output, regardless of whether autocast is enabled.

Prefer binary_cross_entropy_with_logits over binary_cross_entropy

The backward passes of :fine: 'torch.nn.functional.binary_cross_entropy' (and :mod: 'torch.nn.BCELoss', which wraps it) can produce gradients that aren't representable in float16. In autocast-enabled regions, the forward input may be float16, which means the backward gradient must be representable in float16 (autocasting float16 forward inputs to float32 doesn't help, because that cast must be reversed in backward). Therefore, binary_cross_entropy and BCELoss raise an error in autocast-enabled regions.

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Many models use a sigmoid layer right before the binary cross entropy layer. In this case, combine the two layers using fine: torch.nn.functional.binary_cross_entropy_with_logits` or :mod:`torch.nn.BCEWithLogitsLoss`.

binary cross entropy with logits and BCEWithLogits are safe to autocast.

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Unknown interpreted text role "func".

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Unknown interpreted text role "mod".

CPU Op-Specific Behavior

The following lists describe the behavior of eligible ops in autocast-enabled regions. These ops always go through autocasting whether they are invoked as part of a :class:`torch.nn.Module`, as a function, or as a :class:`torch.Tensor` method. If functions are exposed in multiple namespaces, they go through autocasting regardless of the namespace.

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Unknown interpreted text role "class".

Ops not listed below do not go through autocasting. They run in the type defined by their inputs. However, autocasting may still change the type in which unlisted ops run if they're downstream from autocasted ops.

If an op is unlisted, we assume it's numerically stable in bfloat16. If you believe an unlisted op is numerically unstable in bfloat16, please file an issue.

CPU Ops that can autocast to bfloat16

convld, conv2d, conv3d, bmm, mm, baddbmm, addbmm, linear, convolution

CPU Ops that can autocast to float32

conv transposeld, conv transposeld, conv transposeld, batch norm, dropout, avg poolld, avg poolld, avg pool3d, gelu, upsample nearest1d, upsample nearest2d, upsample nearest exact2d, upsample nearest3d, upsample nearest exact3d, upsample linear1d, upsample_bilinear2d, upsample_trilinear3d, binary_cross_entropy, binary_cross_entropy_with_logits, instance_norm, grid_sampler, polar, multinomial, poisson, fmod, prod, quantile, nanquantile, stft, cdist, cross, cumprod, cumsum, diag, diagflat, histc, logcumsumexp, searchsorted, trace, tril, triu, vander, view as complex, cholesky inverse, cholesky solve, dot, inverse, lu solve, matrix rank, orgqr, inverse, ormqr, pinverse, vdot, im2col, col2im, max_pool3d, max_unpool2d, max_unpool3d, adaptive_avg_pool3d, reflection_padld, reflection_pad2d, replication_pad1d, replication_pad2d, replication_pad3d, elu, hardshrink, hardsigmoid, hardswish, log sigmoid, prelu, selu, celu, softplus, softshrink, group norm, smooth 11 loss, mse loss, ctc loss, kl div, multilabel margin loss, fft fft, fft ifft, fft fft2, fft ifft2, fft fftn, fft ifftn, fft rfft, fft irfft, fft rfft2, fft irfft2, fft rfftn, fft irfftn, fft hfft, fft ihfft, conv tbc, linalg matrix norm, linalg cond, linalg matrix rank, linalg solve, linalg cholesky, linalg svdvals, linalg eigvals, linalg eigvalsh, linalg inv, linalg householder product, ${\tt linalg_tensorinv, linalg_tensorsolve, fake_quantize_per_tensor_affine, glu, cummax, cummin, eig, geqrf, and {\tt linalg_tensorinv, linalg_tensorsolve, fake_quantize_per_tensor_affine, glu, cummax, cummin, eig, geqrf, {\tt linalg_tensorinv, linalg_tensorsolve, fake_quantize_per_tensor_affine, glu, cummax, cummin, eig, geqrf, {\tt linalg_tensorinv, linalg_tensorsolve, fake_quantize_per_tensor_affine, glu, cummax, cummin, eig, geqrf, {\tt linalg_tensorinv, linalg_tensorinv, linalg_tensorinv, linalg_tensorinv, linalg_tensorinv, linalg_tensor$ lstsq, lu with info, lu unpack, qr, solve, svd, symeig, triangular solve, fractional max pool2d, fractional max pool3d, adaptive max pool1d, adaptive max pool2d, adaptive max pool3d, multilabel_margin_loss_forward, linalg_qr, linalg_cholesky_ex, linalg_svd, linalg_eig, linalg_eigh, linalg_lstsq, linalg_inv_ex

CPU Ops that promote to the widest input type

These ops don't require a particular dtype for stability, but take multiple inputs and require that the inputs' dtypes match. If all of the

inputs are bfloat16, the op runs in bfloat16. If any of the inputs is float32, autocast casts all inputs to float32 and runs the op in float32.

cat, stack, index_copy

Some ops not listed here (e.g., binary ops like add) natively promote inputs without autocasting's intervention. If inputs are a mixture of bfloat16 and float32, these ops run in float32 and produce float32 output, regardless of whether autocast is enabled.