Kernel driver w1-gpio

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Description

GPIO 1-wire bus master driver. The driver uses the GPIO API to control the wire and the GPIO pin can be specified using GPIO machine descriptor tables. It is also possible to define the master using device tree, see Documentation/devicetree/bindings/w1/w1-gpio.yaml

Example (mach-at91)

```
#include <linux/gpio/machine.h>
#include <linux/w1-gpio.h>
static struct gpiod_lookup_table foo_w1_gpiod_table = {
      .dev id = "w1-gpio",
      .tab\overline{l}e = {
             GPIO LOOKUP IDX("at91-gpio", AT91 PIN PB20, NULL, 0,
                     GPIO ACTIVE HIGH|GPIO OPEN DRAIN),
};
static struct w1_gpio_platform_data foo_w1_gpio_pdata = {
      .ext_pullup_enable_pin = -EINVAL,
static struct platform device foo w1 device = {
               = "w1-gpio",
                             = -1,
     .dev.platform_data = &foo_w1_gpio_pdata,
};
     at91 set GPIO periph (foo w1 gpio pdata.pin, 1);
     at91_set_multi_drive(foo_w1_gpio_pdata.pin, 1);
      gpiod add lookup table (&foo w1 gpiod table);
     platform device register (&foo w1 device);
```