

Other Firmware Interfaces

DMI Interfaces

System Message: ERROR/3 (D:\onboarding-resources\sample-onboarding-resources\linux-master\Documentation\driver-api\firmware\linux-master) (Documentation) (driver-api) (firmware)other_interfaces.rst, line 7)

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```
.. kernel-doc:: drivers/firmware/dmi_scan.c
   :export:
```

EDD Interfaces

System Message: ERROR/3 (D:\onboarding-resources\sample-onboarding-resources\linux-master\Documentation\driver-api\firmware\linux-master) (Documentation) (driver-api) (firmware)other_interfaces.rst, line 13)

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```
.. kernel-doc:: drivers/firmware/edd.c
   :internal:
```

Intel Stratix10 SoC Service Layer

Some features of the Intel Stratix10 SoC require a level of privilege higher than the kernel is granted. Such secure features include FPGA programming. In terms of the ARMv8 architecture, the kernel runs at Exception Level 1 (EL1), access to the features requires Exception Level 3 (EL3).

The Intel Stratix10 SoC service layer provides an in kernel API for drivers to request access to the secure features. The requests are queued and processed one by one. ARM[®]'s SMCCC is used to pass the execution of the requests on to a secure monitor (EL3).

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```
.. kernel-doc:: include/linux/firmware/intel/stratix10-svc-client.h
   :functions: stratix10_svc_command_code
```

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```
.. kernel-doc:: include/linux/firmware/intel/stratix10-svc-client.h
   :functions: stratix10_svc_client_msg
```

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```
.. kernel-doc:: include/linux/firmware/intel/stratix10-svc-client.h
   :functions: stratix10_svc_command_config_type
```

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```
.. kernel-doc:: include/linux/firmware/intel/stratix10-svc-client.h
:functions: stratix10_svc_cb_data
```

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```
.. kernel-doc:: include/linux/firmware/intel/stratix10-svc-client.h
:functions: stratix10_svc_client
```

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```
.. kernel-doc:: drivers/firmware/stratix10-svc.c
:export:
```