

bnxt devlink support

This document describes the devlink features implemented by the `bnxt` device driver.

Parameters

Generic parameters implemented

Name	Mode
<code>enable_sriov</code>	Permanent
<code>ignore_ari</code>	Permanent
<code>msix_vec_per_pf_max</code>	Permanent
<code>msix_vec_per_pf_min</code>	Permanent
<code>enable_remote_dev_reset</code>	Runtime

The `bnxt` driver also implements the following driver-specific parameters.

Driver-specific parameters implemented

Name	Type	Mode	Description
<code>gre_ver_check</code>	Boolean	Permanent	Generic Routing Encapsulation (GRE) version check will be enabled in the device. If disabled, the device will skip the version check for incoming packets.

Info versions

The `bnxt_en` driver reports the following versions

devlink info versions implemented :widths: 5 5 90

Name	Type	Description
<code>board.id</code>	fixed	Part number identifying the board design
<code>asic.id</code>	fixed	ASIC design identifier
<code>asic.rev</code>	fixed	ASIC design revision
<code>fw.psid</code>	stored, running	Firmware parameter set version of the board
<code>fw</code>	stored, running	Overall board firmware version
<code>fw.mgmt</code>	stored, running	NIC hardware resource management firmware version
<code>fw.mgmt.api</code>	running	Minimum firmware interface spec version supported between driver and firmware
<code>fw.nsci</code>	stored, running	General platform management firmware version
<code>fw.roce</code>	stored, running	RoCE management firmware version