



# Arribada turtle gps tag Lora

Lora module addon board for the Arribada turtle gps tag

## CONTENTS:

PAGE1 - CONTENTS

PAGE2 - MCU,LORA

## Version Revision:

### DESIGN CONSIDERATIONS

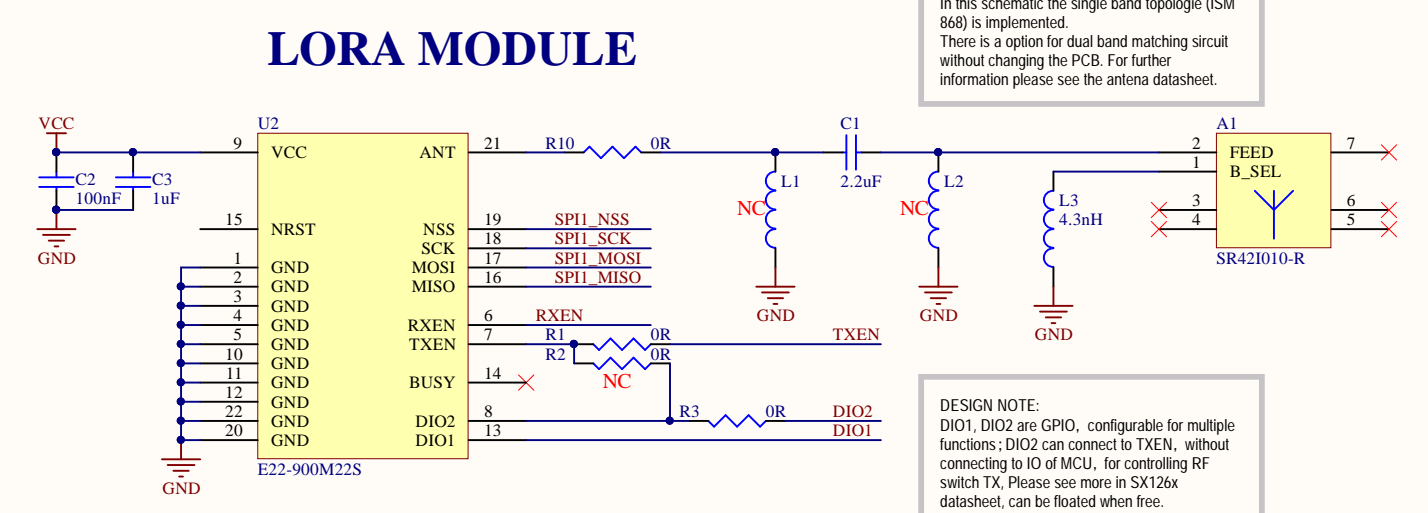
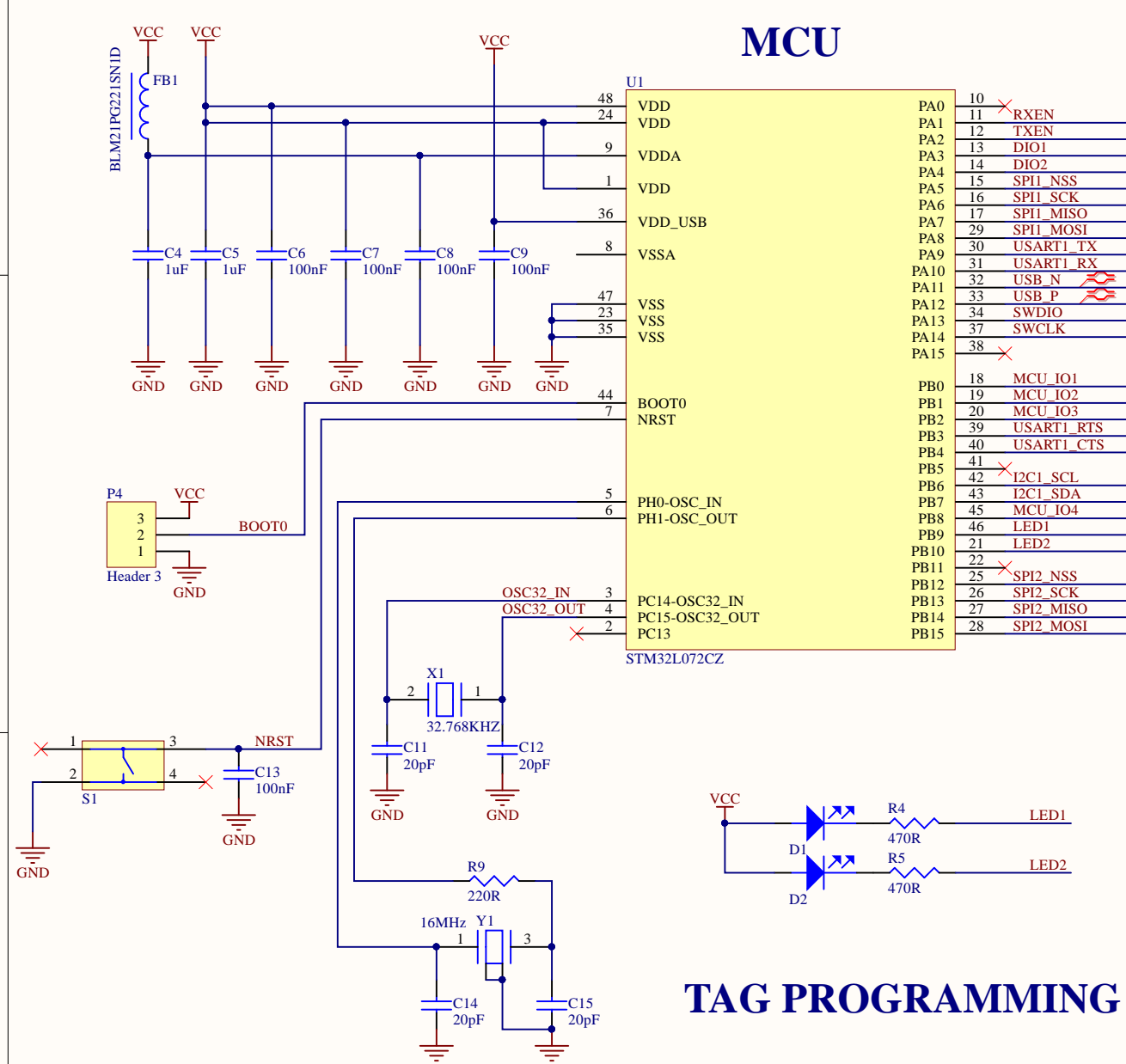
DESIGN NOTE:  
Example text for informational  
design notes.

DESIGN NOTE:  
Example text for critical  
design notes.

LAYOUT NOTE:  
Example text for critical  
layout guidelines.

Irnas		www.irnas.eu	
Title: Arribada-turtle-gps-tag-lora-addon.PrjPcb			
Page Contents: PAGE1-CONTENT.SchDoc			
Size:	DWG NO		Revision: v0_1
Date: *			Sheet * of *

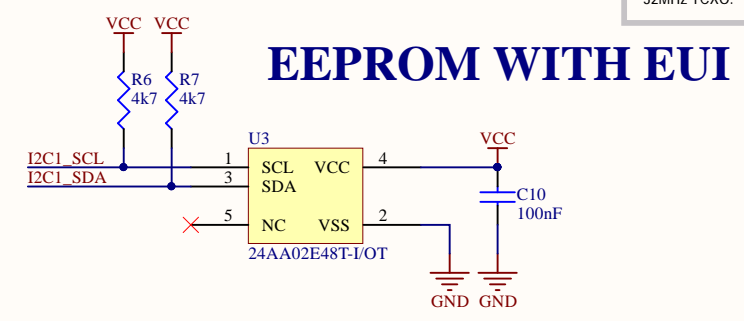
# PAGE2 - MCU, LORA



DESIGN NOTE:  
In this schematic the single band topologie (ISM 868) is implemented.  
There is a option for dual band matching circuit without changing the PCB. For further information please see the antenna datasheet.

DESIGN NOTE:  
DIO1, DIO2 are GPIO, configurable for multiple functions; DIO2 can connect to TXEN, without connecting to IO of MCU, for controlling RF switch TX, Please see more in SX126x datasheet, can be floated when free.

DESIGN NOTE:  
E22(433M22S) uses DIO3 internally to power 32MHz TCXO.



## EXPANSION CONNECTOR

