

Cannot open file C:\Users\vojislav\Documents\koruza-compute-module\pics\koruza-logo-colour-med.png. File does not exist.

# Koruza

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PAGE3 - HUB, USB, ETHERNET

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## Version Revision:

v0.2 - 09.03.2017.

v0.3 - 30.03.2017.

v0.3.1 - 05.05.2017.

v0.3.2 - 26.02.2021.

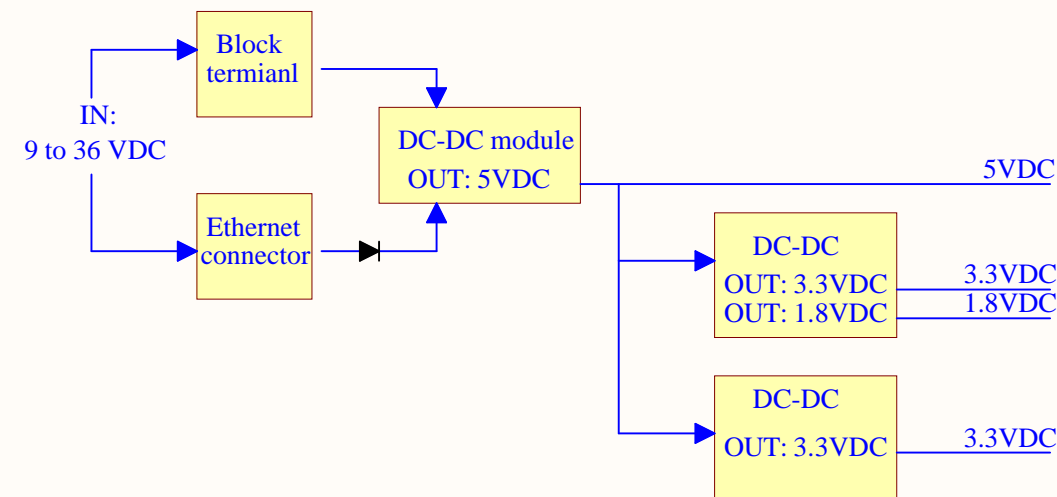
## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational  
design notes.

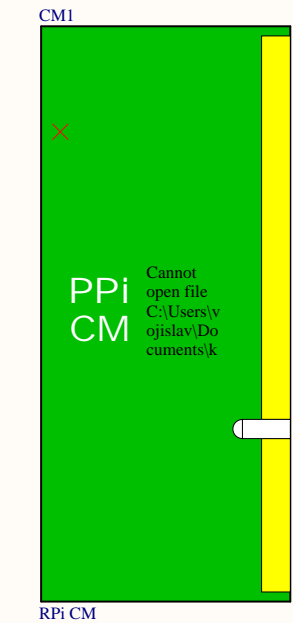
DESIGN NOTE:  
Example text for critical  
design notes.

LAYOUT NOTE:  
Example text for critical  
layout guidelines.

## POWER DIAGRAM



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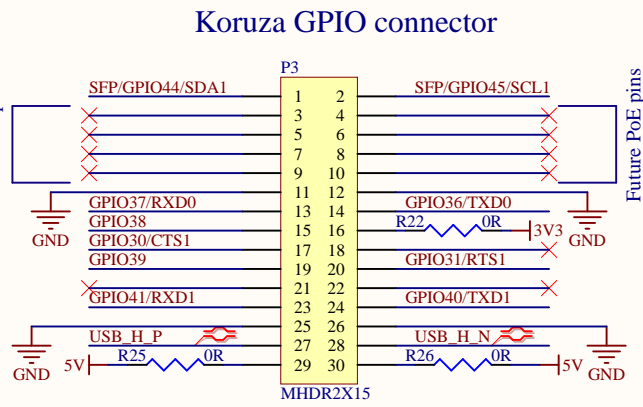


DESIGN NOTE:  
Route red ringed signals as matched length 100R differential pair

DESIGN NOTE:  
For USB to be connected to the header zero ohm resistors need to be soldered.

DESIGN NOTE:  
Route red bold ringed signals as matched length 90R differential pair.

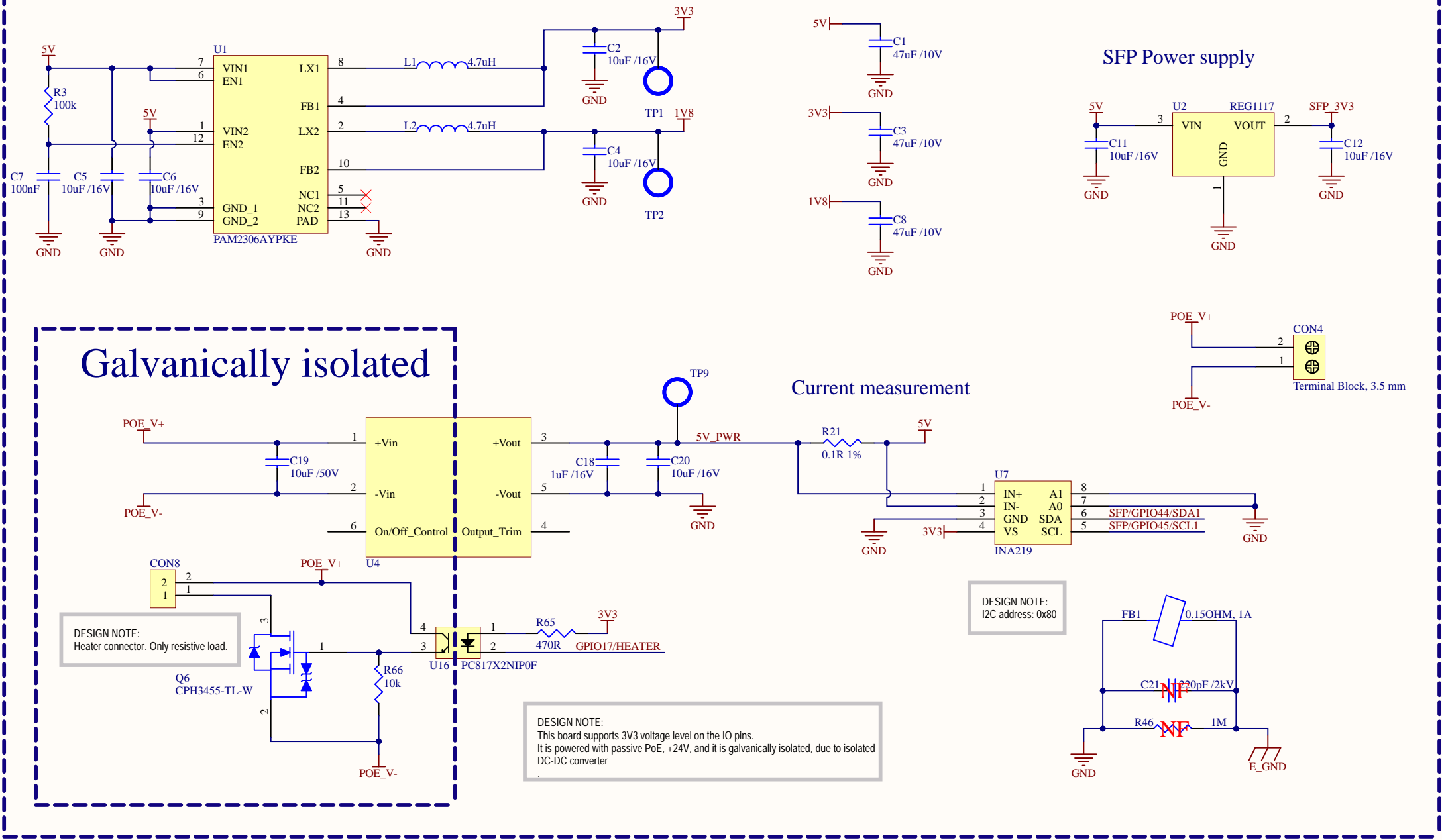
DESIGN NOTE:  
VDD\_CORE used for module test only (do not use in normal operation, do not draw current from this pin)



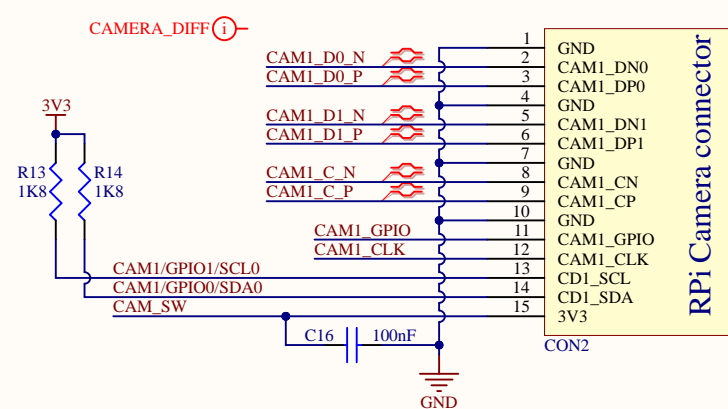
Koruzan GPIO connector - GPIO Pins Alternative Function Assignment

Cannot open file C:\Users\vojislav\Documents\koruzan-compute-module\pics\GPIO\_ALT.png. File does not exist.

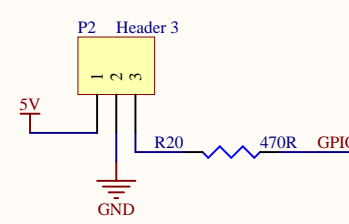
System power



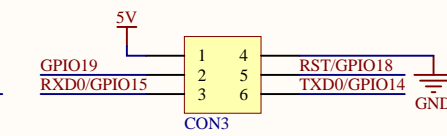
Koruzan Camera connector



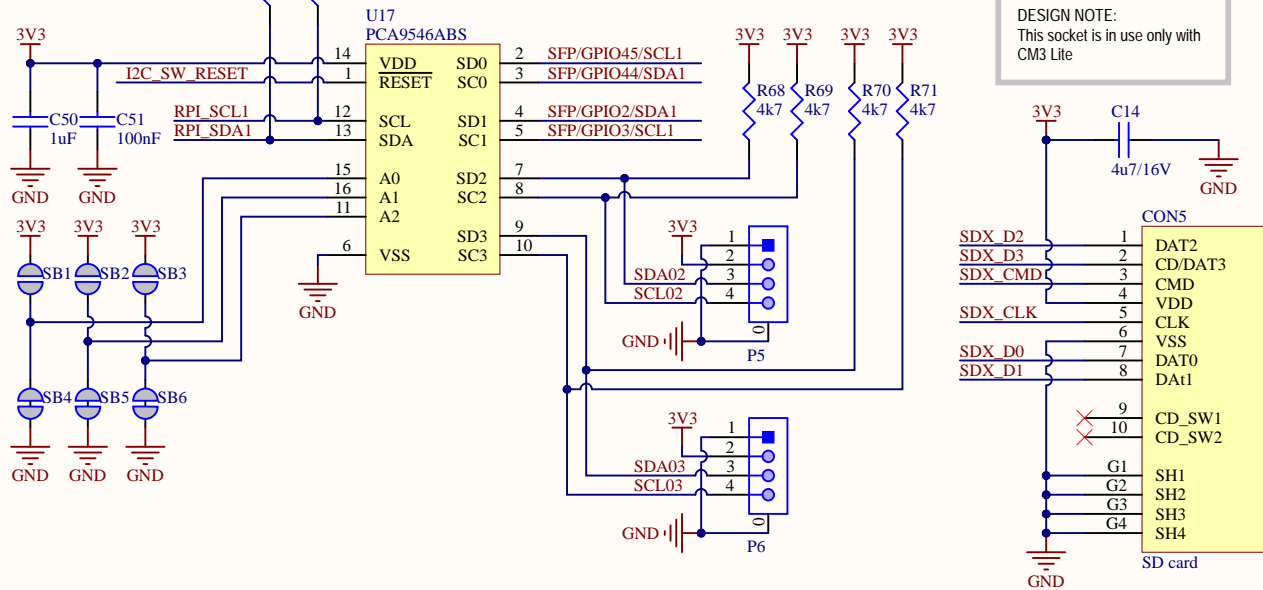
RGB Status LED



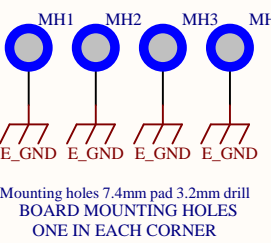
Koruzan move driver connector



Micro SD card socket



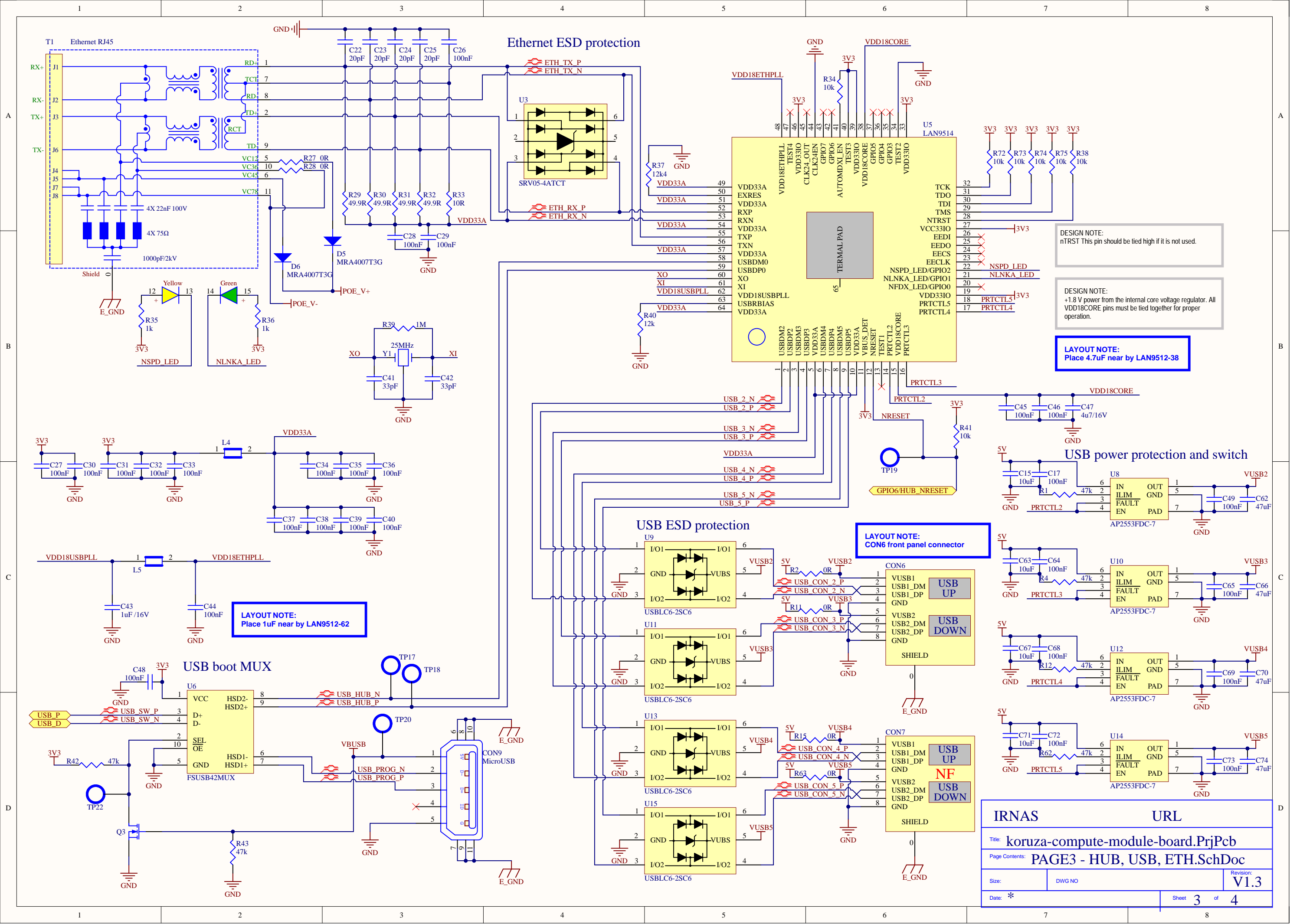
Mounting holes



Fiducials

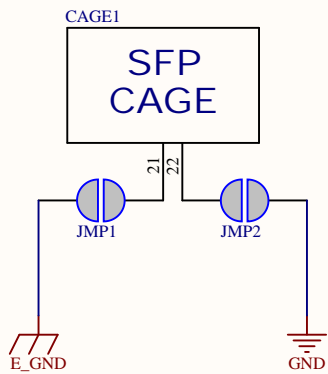


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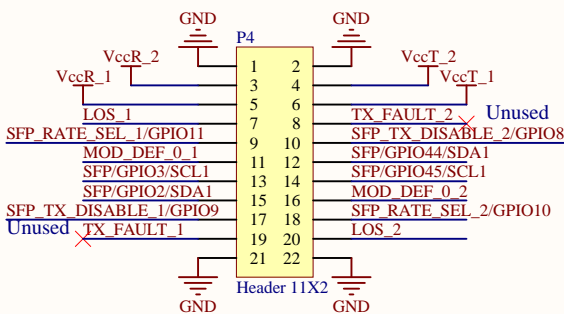


# Rigid to Flex PCB connection

SFP Cage Rigid to flex connector



Koruzza SFP rigid to flex GPIO connector



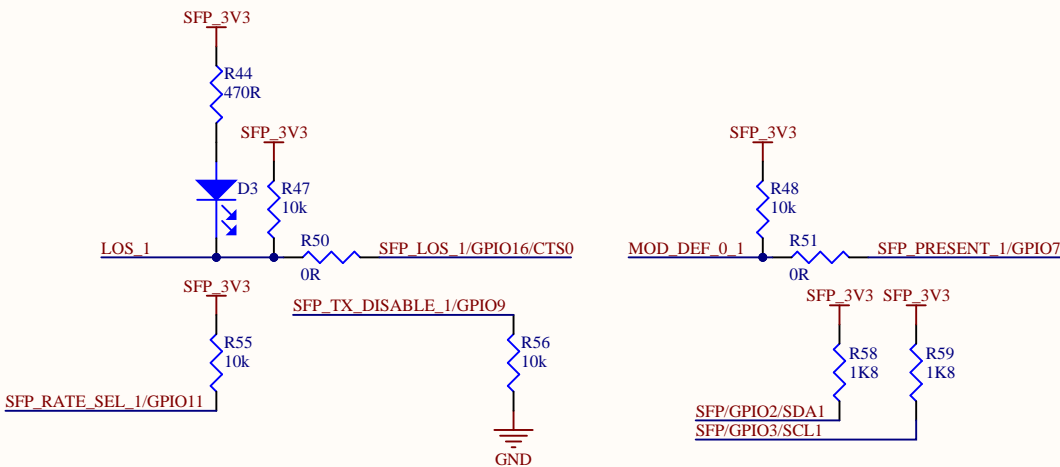
SFP\_PRESENT\_1/GPIO7  
SFP\_LOS\_1/GPIO16/CTS0  
SFP\_LOS\_2/GPIO43  
SFP\_PRESENT\_2/GPIO42

SFP/GPIO44/SDA1  
SFP/GPIO45/SCL1  
SFP/GPIO2/SDA1  
SFP/GPIO3/SCL1

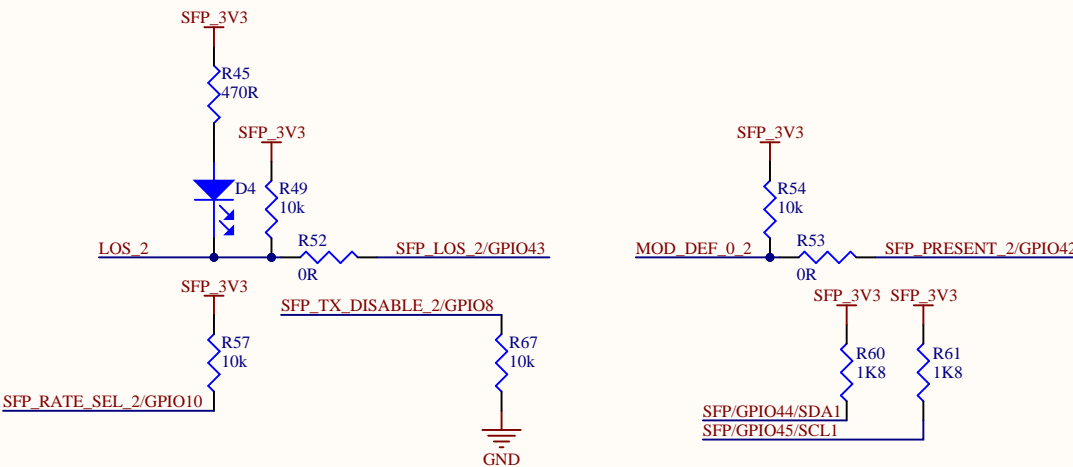
SFP\_TX\_DISABLE\_2/GPIO8  
SFP\_TX\_DISABLE\_1/GPIO9

SFP\_RATE\_SEL\_2/GPIO10  
SFP\_RATE\_SEL\_1/GPIO11

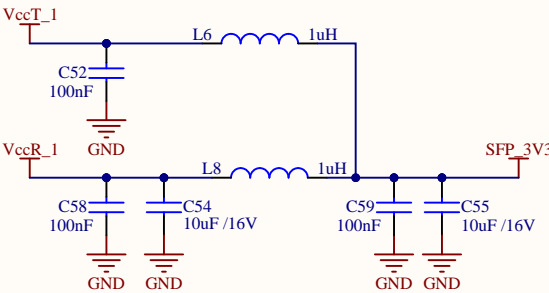
SFP1 GPIO config



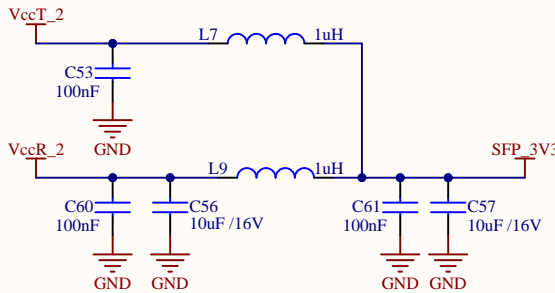
SFP2 GPIO config



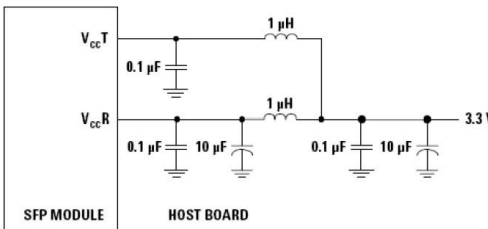
SFP1 Power filtering network



SFP2 Power filtering network



DESIGN NOTE:  
Note: Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin at 3.3V. When the recommended supply filtering circuit is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value.



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