



PiRa-Smart

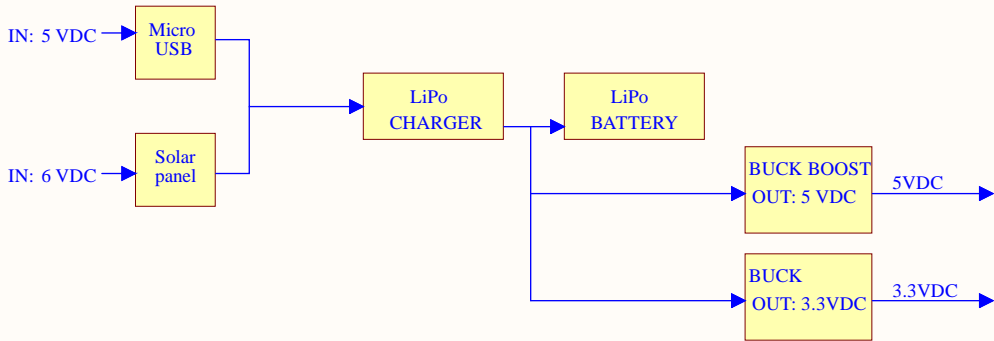
Power in Responsive Applications with BLE and RTC sleep mode.
Designed as a Raspberry Pi Zero HAT.

POWER DIAGRAM

CONTENTS:

PAGE1 - CONTENTS
PAGE2 - POWER, RTC, RPI

Version Revision:
v0.1 - 23.03.2018.



DESIGN CONSIDERATIONS

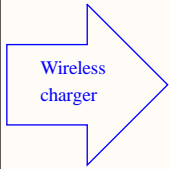
DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

IRNAS		iras.eu	
Title: pira-smart-PCB.PrjPcb			
Page Contents: PAGE1-CONTENTS.SchDoc			
Size:	DWG NO		Revision: v0.1
Date: *	Sheet 1 of 2		

CHARGER INPUTS



CHARGER

POWER, RTC, RPI

BATTERY CONNECTOR

BOOST CONVERTER

MOSFET OUT

BLE

RPI CONNECTOR

RTC

LoRa

DESIGN NOTE:
Resistor divider was adjusted for 1.2V
which is the value
of internal reference with 1/1 prescaling.
Divider can be selected to use maximum
voltages
of 2.4 or 3.6V since VCC is 3.3V.

LAYOUT NOTE:
One uFL connector is
on TOP and
second on BOTTOM

