

The circuit diagram shows the following connections:

- VDD**: Connected to pin 16, which is also connected to a 100nF/16V capacitor (C16) to ground.
- VDDA**: Connected to pin 5, which is also connected to a 100nF/16V capacitor (C2) to ground.
- GND**: Connected to pins 17, 18, 19, 20, and 21.
- RST_BTN**: Connected to pin 6 (NRST).
- BOOT0**: Connected to pin 1.
- PF1/B_L**: Connected to pin 2 (PFI0/RCC_OSC_IN).
- USR_BTN**: Connected to pin 3 (PFI1/RCC_OSC_OUT).
- Buzzer**: Connected to pin 14 (PB1/ADC_N9/TIM14_CH1/TIM14_CH3N/TIM3_CH4).
- RCV2**: Connected to pin 7.
- LED3**: Connected to pin 8.
- LED1**: Connected to pin 9.
- PAS/MISO**: Connected to pin 10.
- PA/SCK**: Connected to pin 11.
- PA/MSO**: Connected to pin 12.
- PA/MOSI**: Connected to pin 13.
- PA/SCL**: Connected to pin 14.
- PA/I2C_SDA**: Connected to pin 15.
- SWIO**: Connected to pin 16.
- SWK**: Connected to pin 17.
- VBAT_1**: Connected to pin 18.
- VBUS**: Connected to pin 19.
- PA**: Connected to pin 20.
- D0**: Connected to pin 21.

CON2

PA5/SCK 1
PA6/MISO 2
PA7/MOSI 3
PA9/SCL 4
PA10/SDA 5
B_LIGHT 6
GND 7
+3V3 8

B_LIGHT J1
Jumper_NO

Diagram showing four mounting holes labeled MK1, MK2, MK3, and MK4. Each hole is represented by a red circle with a white center. The labels are arranged in a 2x2 grid: MK1 and MK3 in the top row, MK2 and MK4 in the bottom row. Each label is positioned above its corresponding hole.



Sheet: /
File: pulseox.sch

Title: PULSEOX

Title: PULSEOX

Size: A4	
----------	--

KiCad E.D.A.	kicad
--------------	-------

4

Rev: 2.0

Id: 1/1
