Grade Sheet for Lab #1   Fall 2015.

Group Number: _10_

Group Member Name: _Raluca Niti_   Student Number: _260 379 389_

Group Member Name: _Benjamin Landry_   Student Number: _260583598_

Marks			TA Signatures
2	1.	Schematic diagram for the 6-bit comparator	AK
2	2.	VHDL file for the 6-bit comparator	AK
2	3.	Initial partial simulation results for the 6-bit comparator	AK
2	4.	Complete simulation results for the 6-bit comparator	AK
2	5.	Boolean equations for the 1's counter circuit	Arash
2	6.	VHDL description for the 1's counter circuit	Arash
2	7.	Simulation Testbench VHDL for the 1's counter circuit	AK
2	8.	Simulation results for the 1's counter circuit	AK

Each part should be demonstrated to one of the TAs who will then give a grade and sign the grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if everything is done correctly. A grade of 1 will be given if there are significant problems, but an attempt was made. A grade of 0 will be given for parts that were not done at all, or for which there is no TA signature.

McGill University ECSE-323   Digital System Design / Prof. J. Clark