

# OrCAD 사용법

# 설치

The screenshot shows the NP IT (주) website. The navigation bar includes links for '로그인 | 회원가입', '제품소개', '다운로드/기술지원', '견적/제품문의', '교육/세미나/이벤트', and '회사소개'. The main content area features a large banner for 'OrCAD DFM Checker' with the text 'OrCAD 17.2 release' and '포괄적이고 강력한 DFM 검증'. Below this, it states: 'OrCAD® DFM Checker는 PCB의 부품 실장 시 악영향을 미칠수있는 부분에 대하여 제조 시 잠재적으로 문제될 부분을 미리 인지하고 제품 생산/출시 시간 지연을 방지할 수 있습니다.' and a link '자세히 보기 +'. A dropdown menu is open under '다운로드', showing options: 'Product(Install/Patch)', 'Demo(Lite)', 'License Manager', and '자료실'. The 'Demo(Lite)' option is highlighted. On the right side of the banner, there are two callouts: 'Error Charting' and 'Get to the root of the failure quickly'. The background of the banner is a blue circuit board.

로그인 | 회원가입

NP IT 나인플러스 IT (주)

제품소개 다운로드/기술지원 견적/제품문의 교육/세미나/이벤트 회사소개

다운로드

원격지원

기술지원 (FAQ, Q&A)

NAVER BLOG

Product(Install/Patch)

Demo(Lite)

License Manager

자료실

OrCAD 17.2 release

## OrCAD DFM Checker

**포괄적이고 강력한 DFM 검증**

OrCAD® DFM Checker는 PCB의 부품 실장 시 악영향을 미칠수있는 부분에 대하여 제조 시 잠재적으로 문제될 부분을 미리 인지하고 제품 생산/출시 시간 지연을 방지할 수 있습니다.

[자세히 보기 +](#)






Error Charting


Hierarchical Rule-Set-Driven Analysis

Get to the root of the failure quickly

<http://www.npit.co.kr/> 접속 -> 다운로드/기술지원 -> Demo(Lite) -> OrCAD Lite (All Product)

# 설치

No.	프로그램 명	프로그램 다운로드	등록일	다운로드횟수
5	OrCAD 17.4-2019 Trial (설치방법)		2019-12-20	32
4	OrCAD 17.2-2016 Lite (Capture, PSpice)		2018-10-29	2,891
3	OrCAD 17.2-2016 Lite (All Product)		2018-10-29	1,180
2	OrCAD 16.6-2015 Lite (Capture, PSpice)		2017-03-10	5,547
1	OrCAD 16.6-2015 Lite (All Products)		2017-03-10	5,336


**나인플러스IT(주)**

[제품소개](#)
[다운로드/기술지원](#)
[견적/제품문의](#)
[교육/세미나/이벤트](#)
[회사소개](#)

Q

A

S

D

F

Y

X

C

V

B

N

M

L

K

J

LOGIN

나인플러스IT에 오신 것을 환영합니다.

회원 로그인

나인플러스IT에 가입하신 아이디와 비밀번호를 입력해주세요.  
비밀번호는 대소문자를 구분합니다.

아이디

비밀번호

로그인

회원가입

- 회원이 되시면 더욱 편리하게 서비스를 이용하실 수 있습니다.

로그인 정보를 잊으셨나요?

아이디/비밀번호 찾기

비회원 교육신청조회

나인플러스IT에서 신청시 기입하신 이름, 연락처, 이메일 주소를 입력해주세요.

이름

연락처

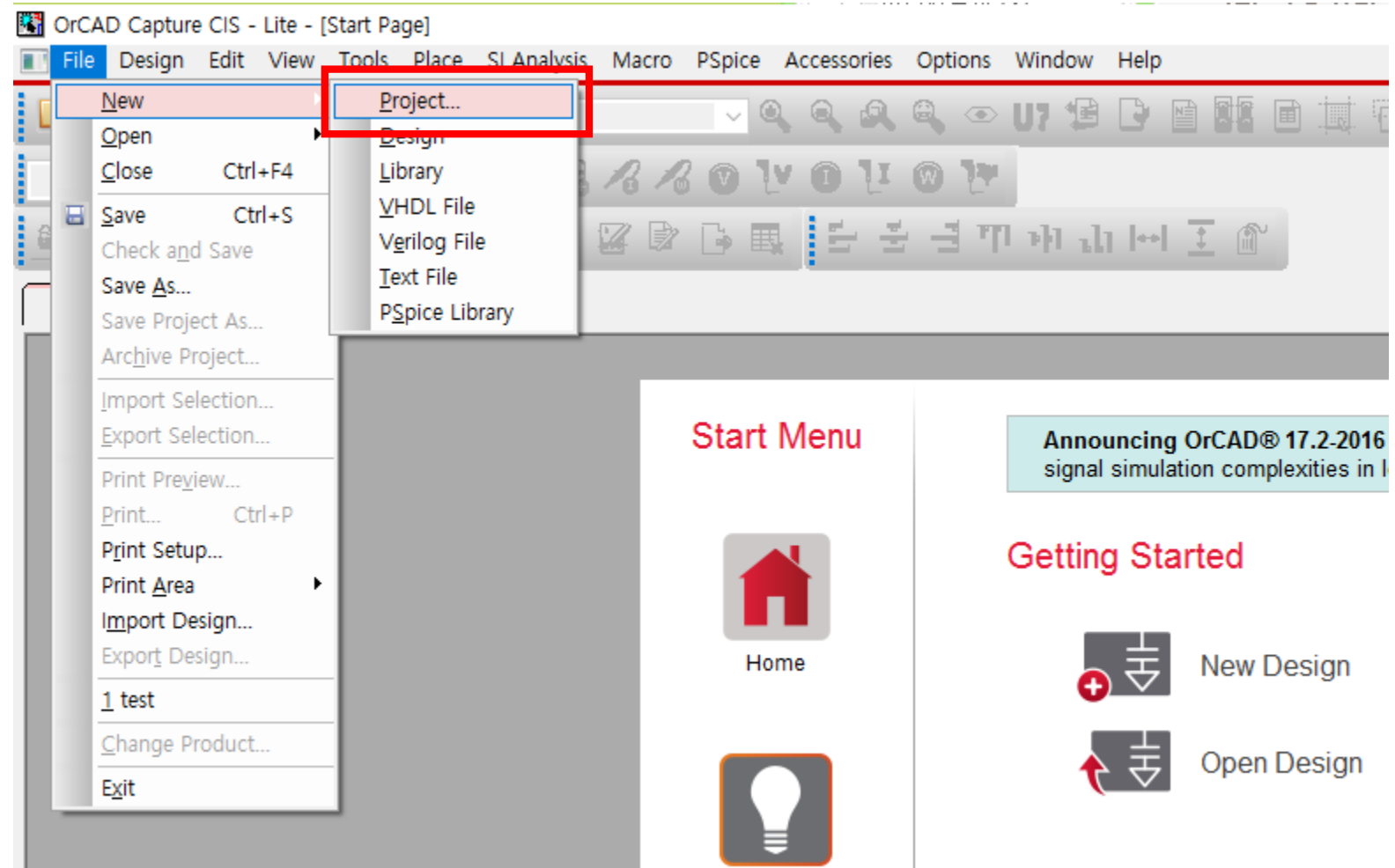
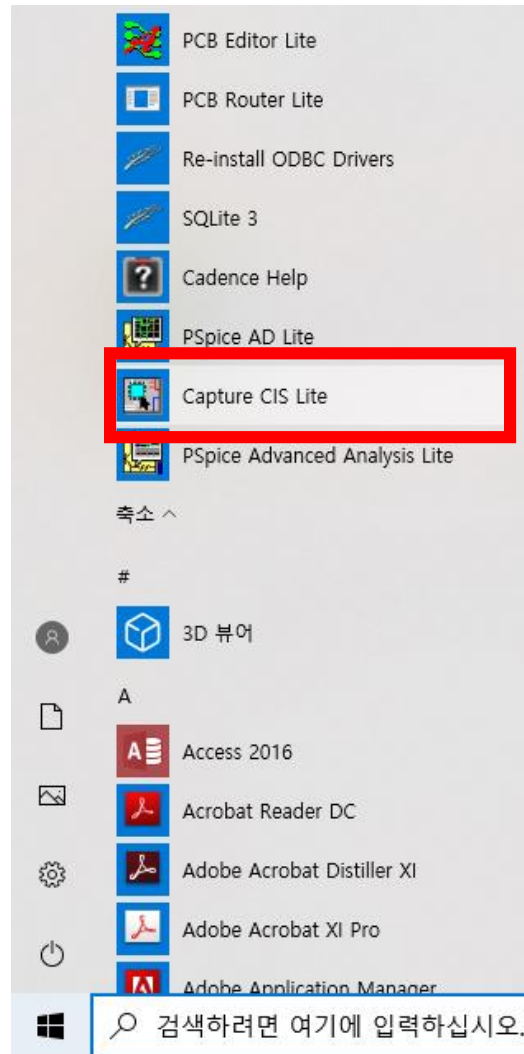
이메일

비회원 교육신청조회

The image displays a sequence of five screenshots from the Cadence OrCAD PCB Designer Lite 17.2 installation wizard, showing the progression from the initial welcome screen to the final installation summary.

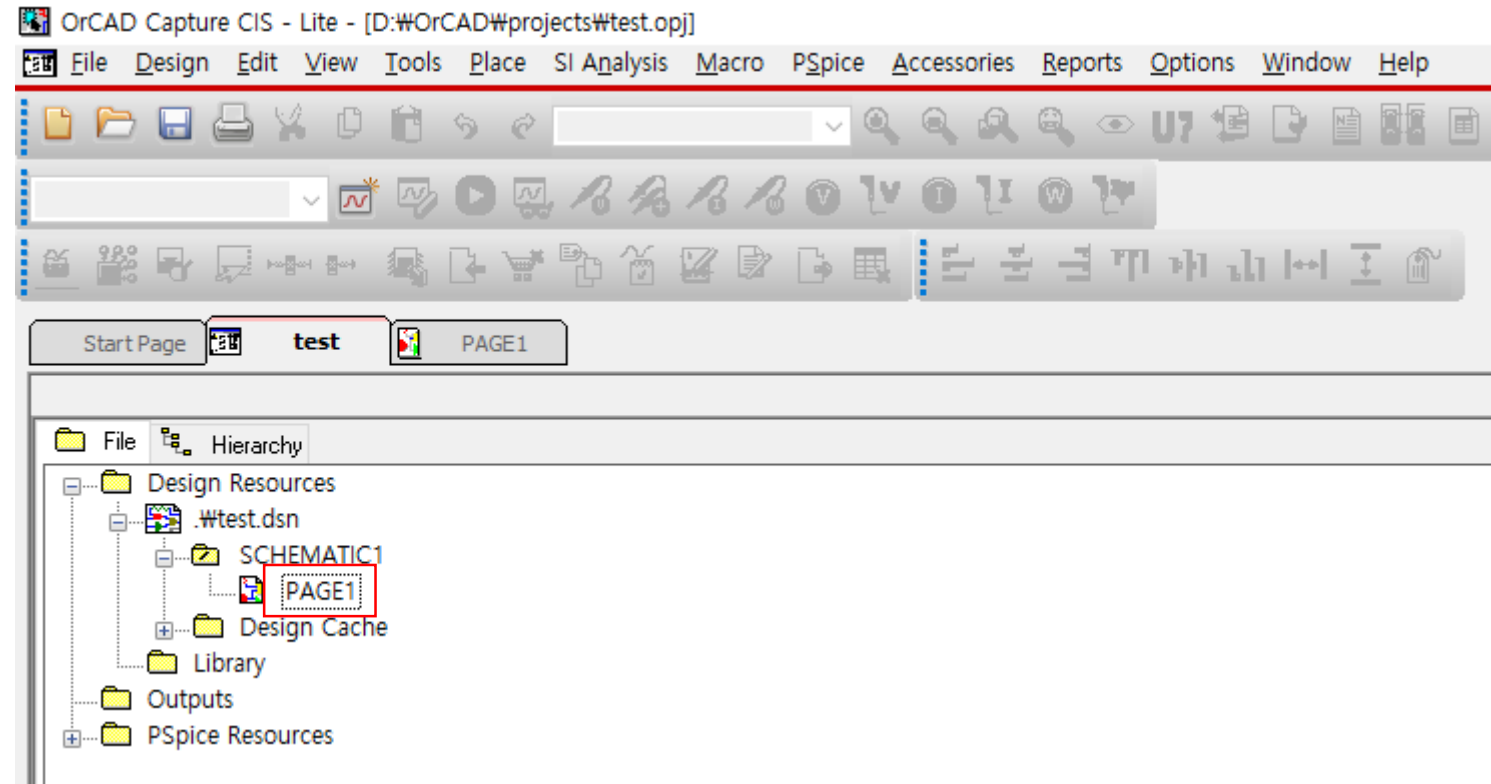
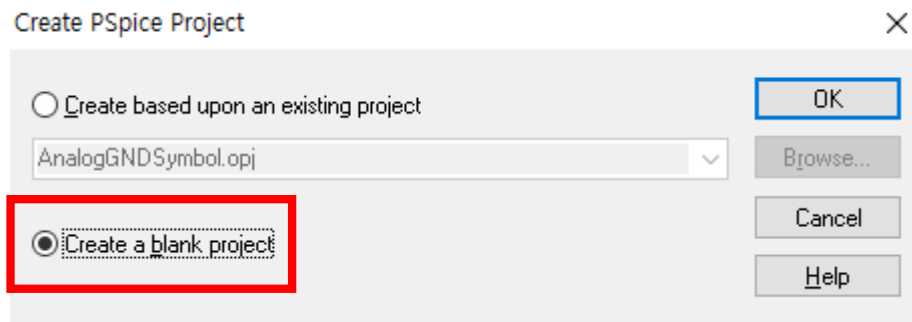
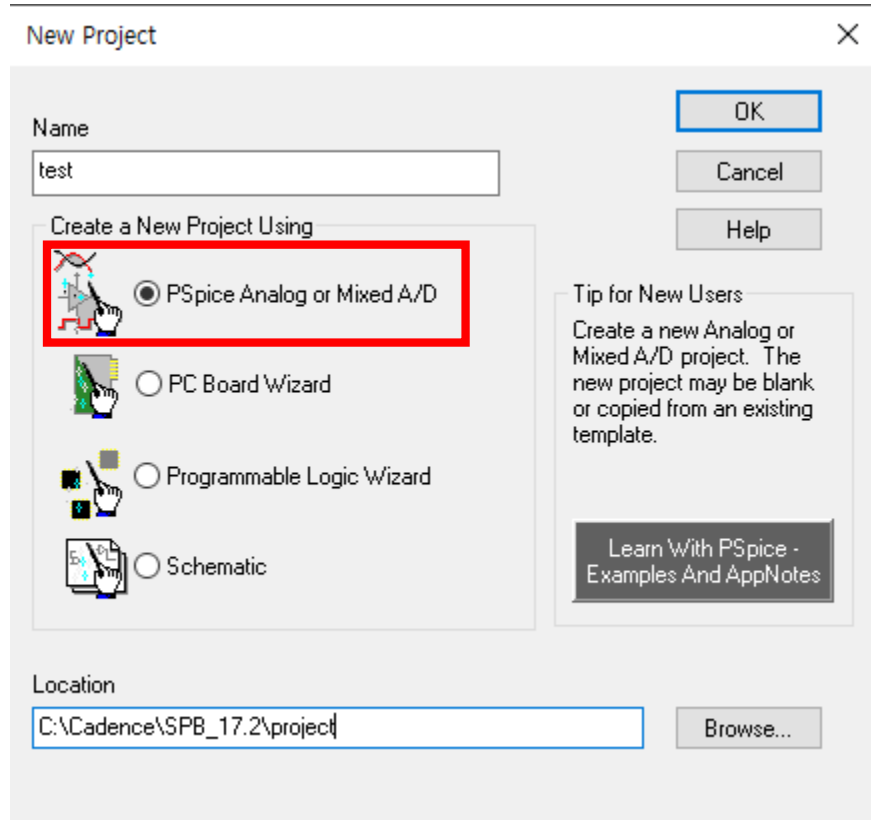
- Welcome to the InstallShield Wizard for Cadence OrCAD PCB Designer Lite 17.2:** The first screen shows the Cadence logo and a message advising the user to turn off virus detection, firewall, and spyware programs to avoid installation failure. It includes 'Back', 'Next >', and 'Cancel' buttons.
- License Agreement:** The second screen displays the 'License Agreement' text, which states that the user is entering into a legal agreement with Cadence Design Systems, Inc. The user is prompted to read the agreement carefully. There are radio buttons for 'I accept the terms of the license agreement' (selected) and 'I do not accept the terms of the license agreement'. A 'Print' button is also present.
- Setup Type:** The third screen asks the user to 'Select the setup type to install'. There are two radio buttons: 'Only for me (Recommended)' (selected) and 'Anyone who uses this computer (all users)'.
- Installation Settings:** The fourth screen shows the 'Installation Directory' field, which is set to 'C:\Cadence\SPB\_17.2'. A 'Browse...' button is available to change the directory.
- Installation Summary:** The fifth screen provides a 'Start Copying Files' summary, reviewing the settings before copying. It lists the products to install (PCB Designer Lite and PSpiceAD Lite), the product destination path (C:\Cadence\SPB\_17.2), and the working directory. The 'Install' button is highlighted.

# simulation

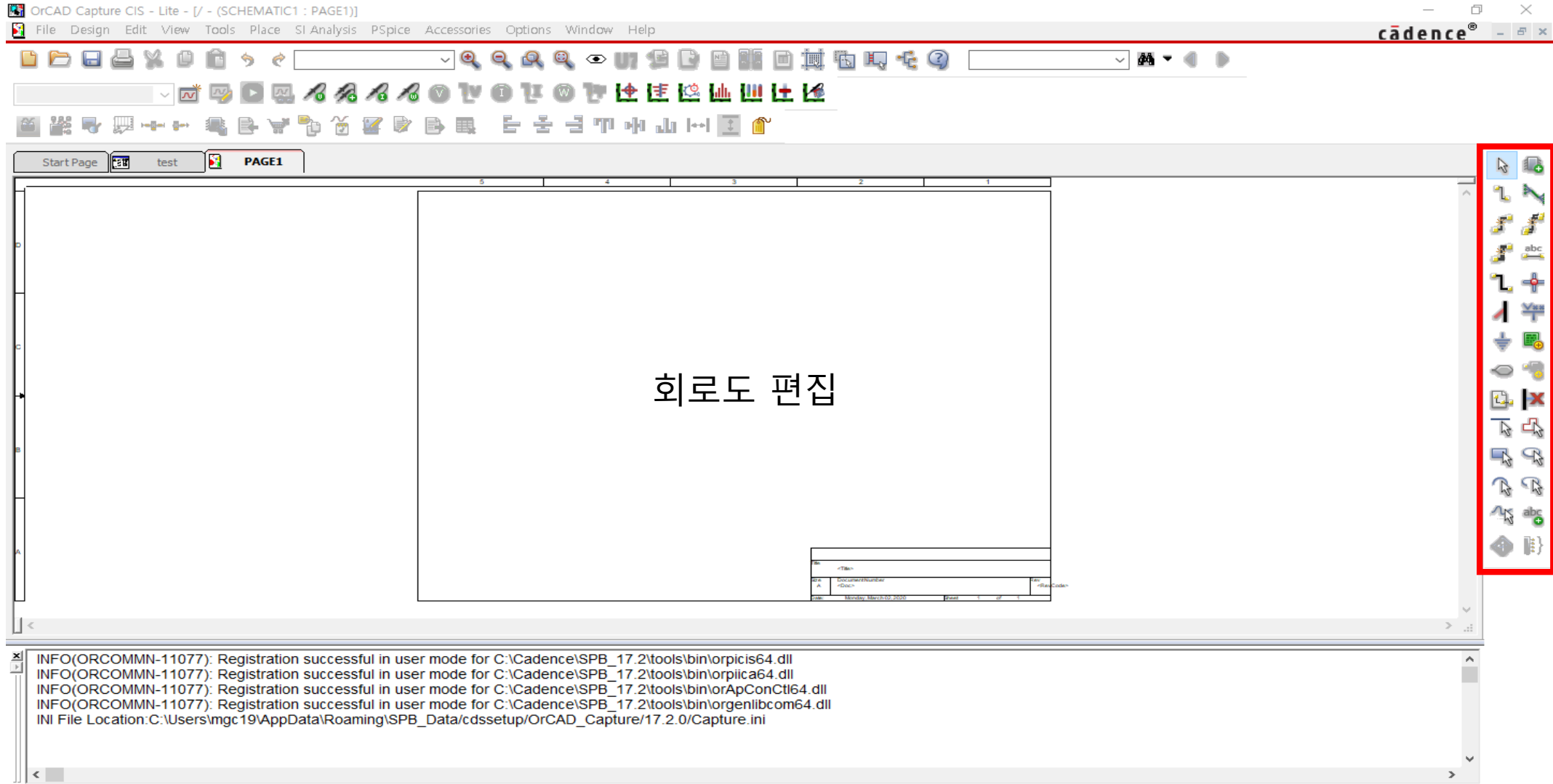


OrCAD Capture CIS Lite 실행 -> File -> New -> Project

# simulation



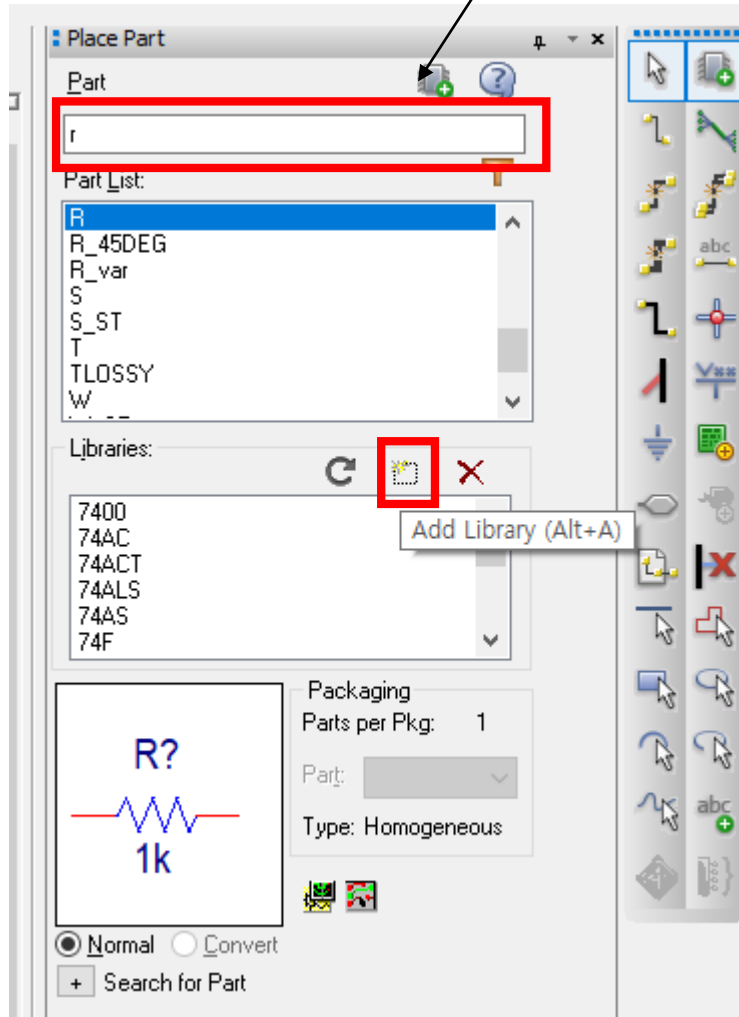
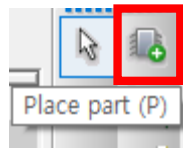
# simulation



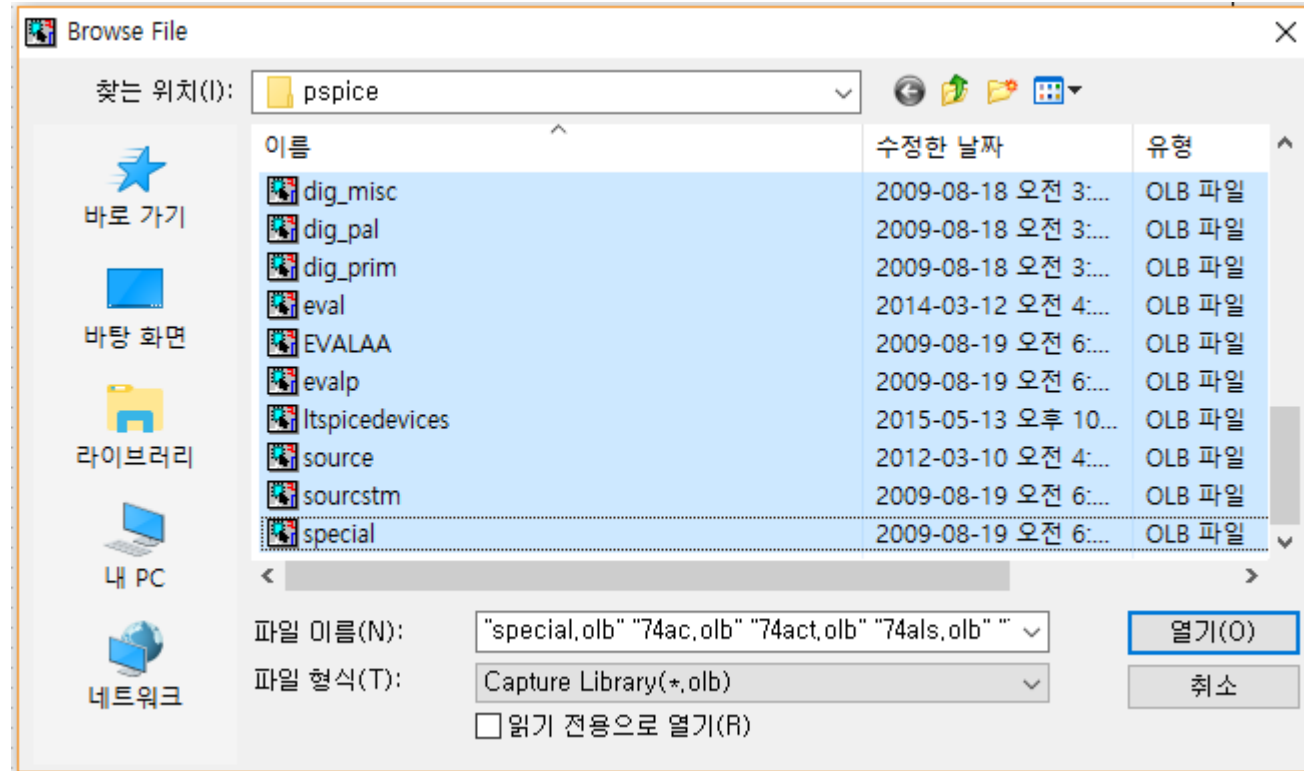
# simulation

원하는 부품 이름 입력

Place part  
선택

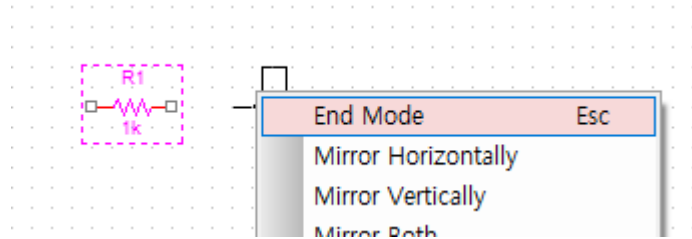


Add Library 선택

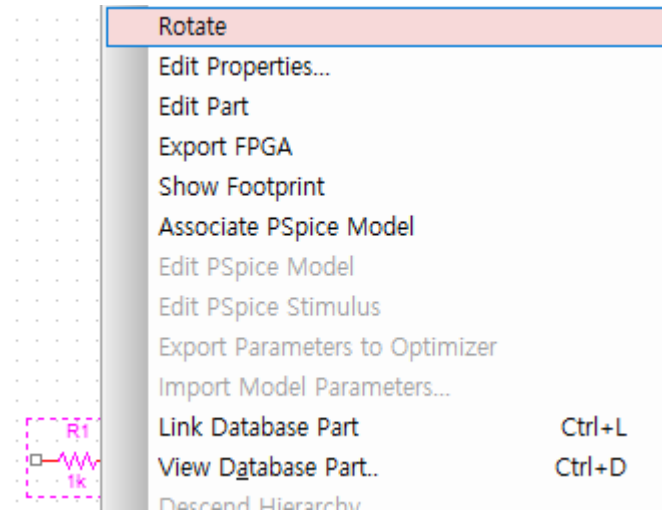


모든 라이브러리 불러오기

# simulation



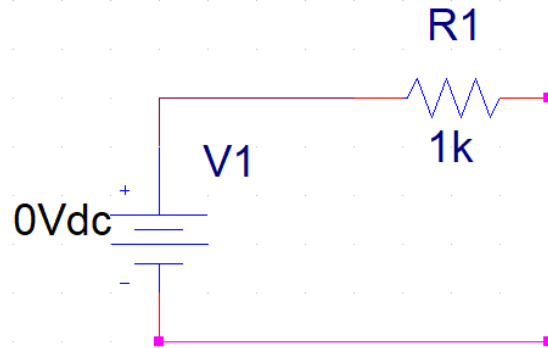
원하는 위치에 부품을 놓은 뒤, 마우스 오른쪽 버튼 클릭 후, End Mode 클릭하여 현재 작업 종료



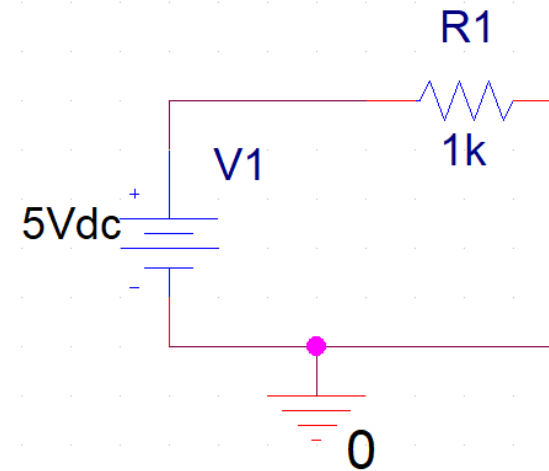
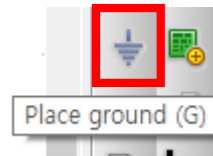
부품을 클릭한 다음 마우스 오른쪽 버튼 클릭 후, Rotate 선택 시 부품 회전 가능



Place wire 선택



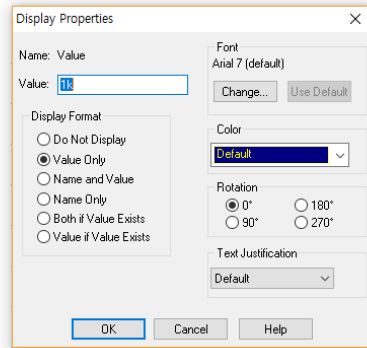
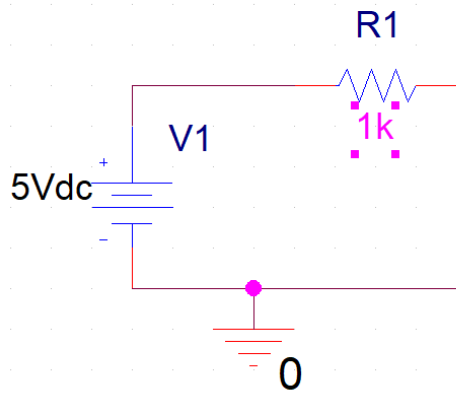
Place wire를 통해 선 연결



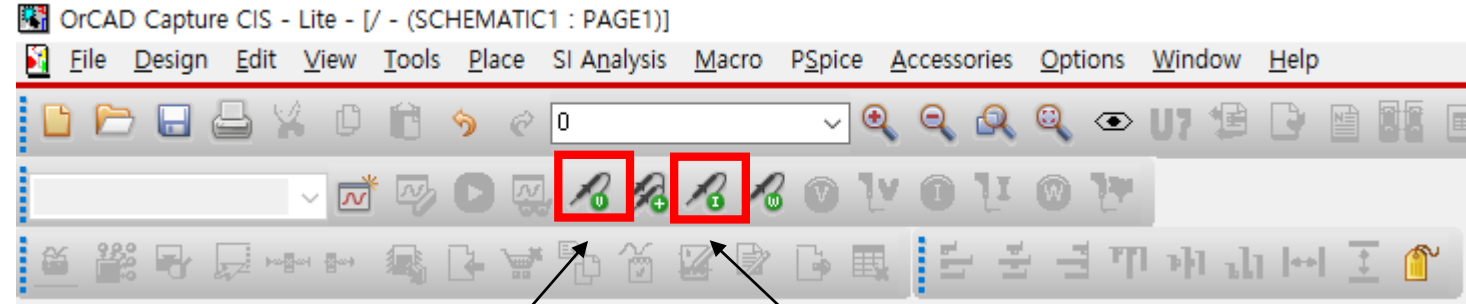
Place ground를 통해 접지



# simulation

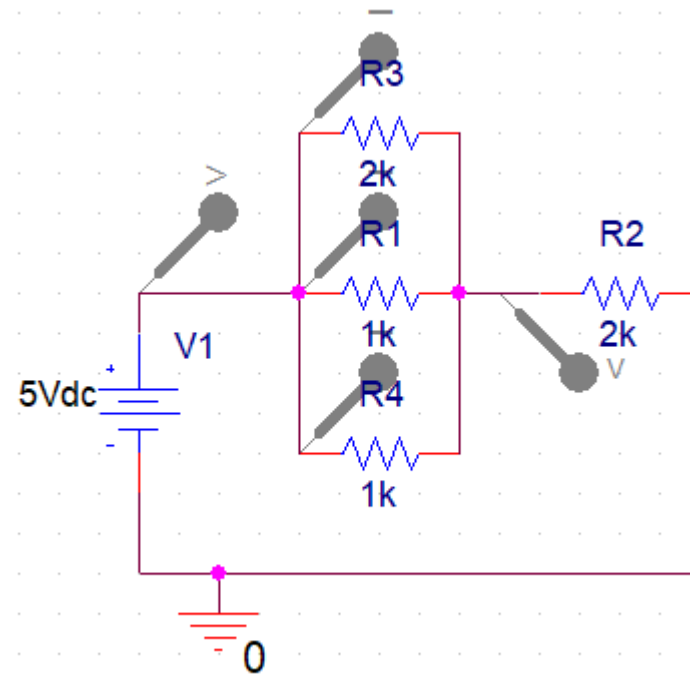


더블 클릭을 통해 값 수정



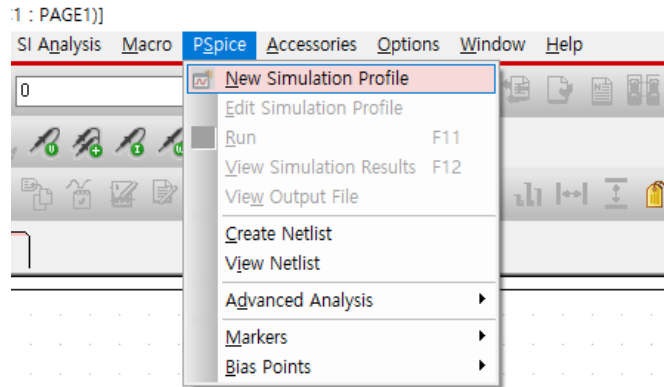
전압 측정

전류 측정

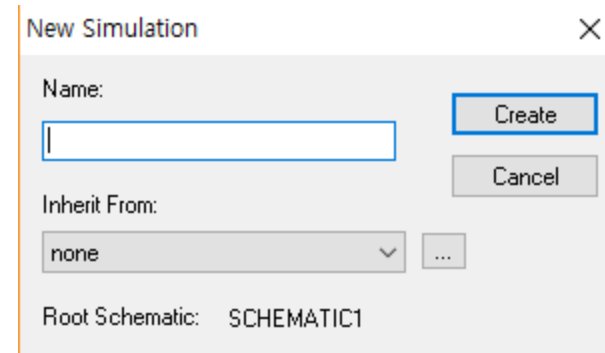


원하는 위치에 프로브

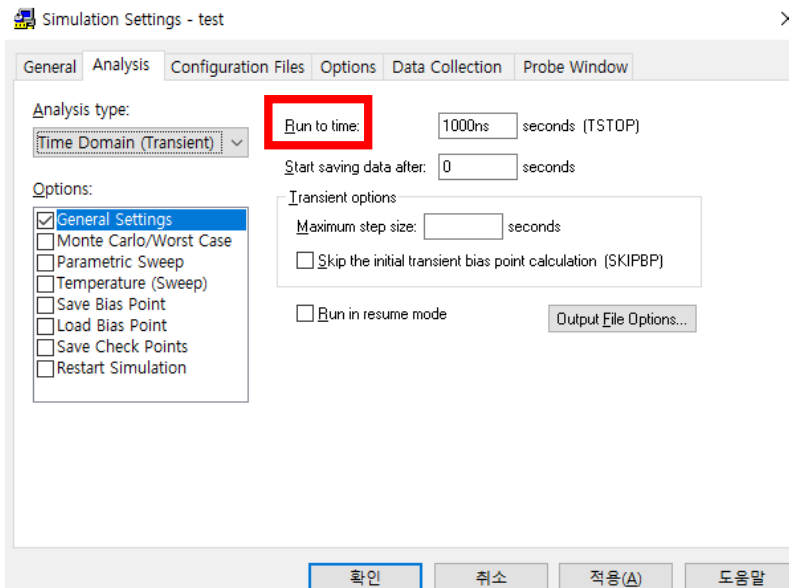
# simulation



Pspice->New Simulation Profile 선택

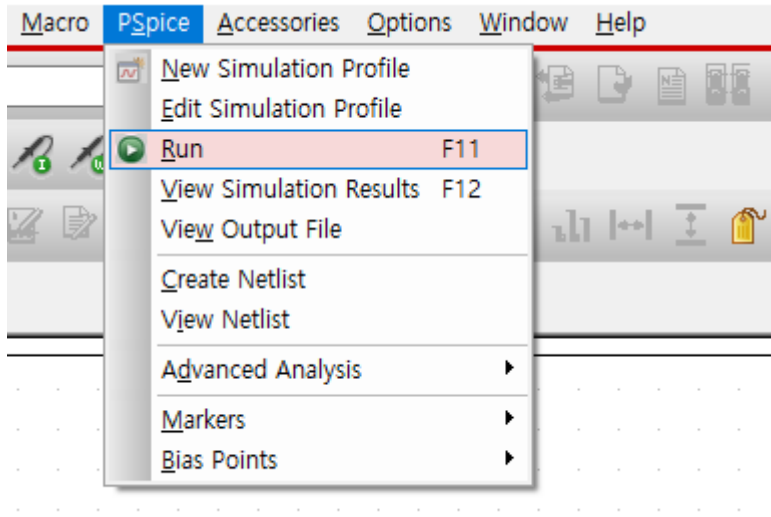


이름 입력후 Create 선택



Run to time을 통해 시뮬레이션 시간 설정

# simulation



Run을 클릭해 시뮬레이션 실행

