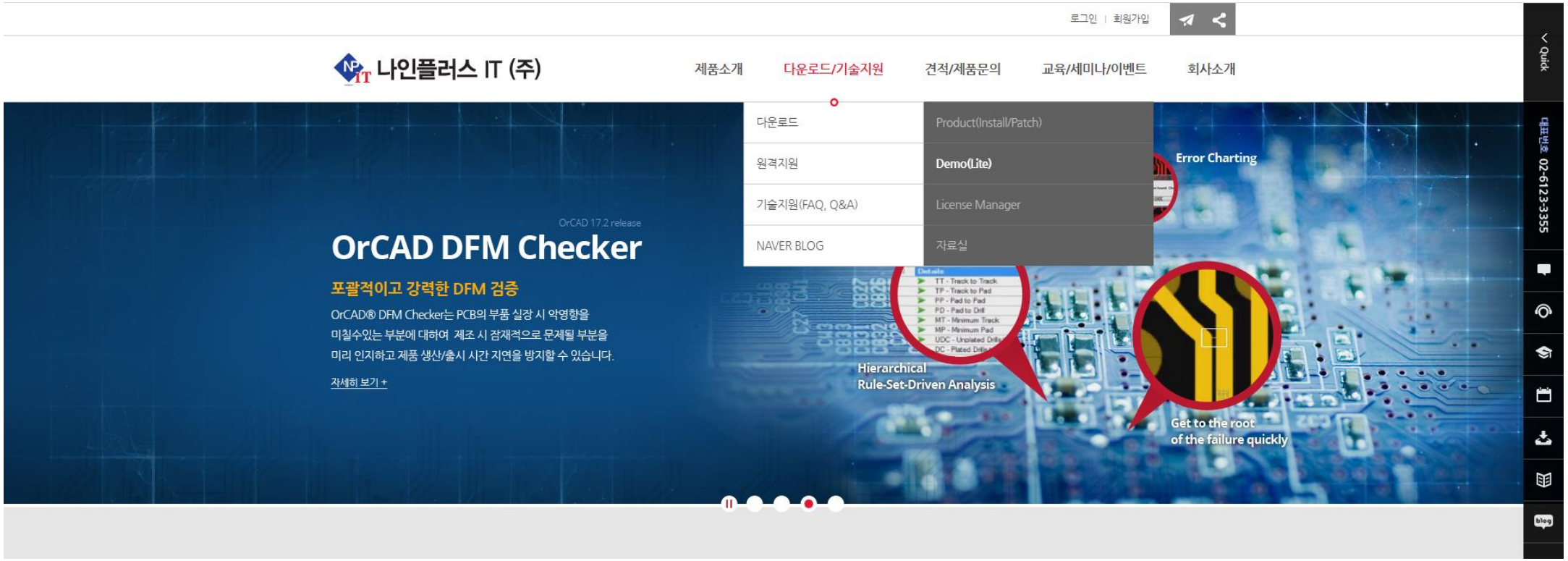
OrCAD 사용법

설치

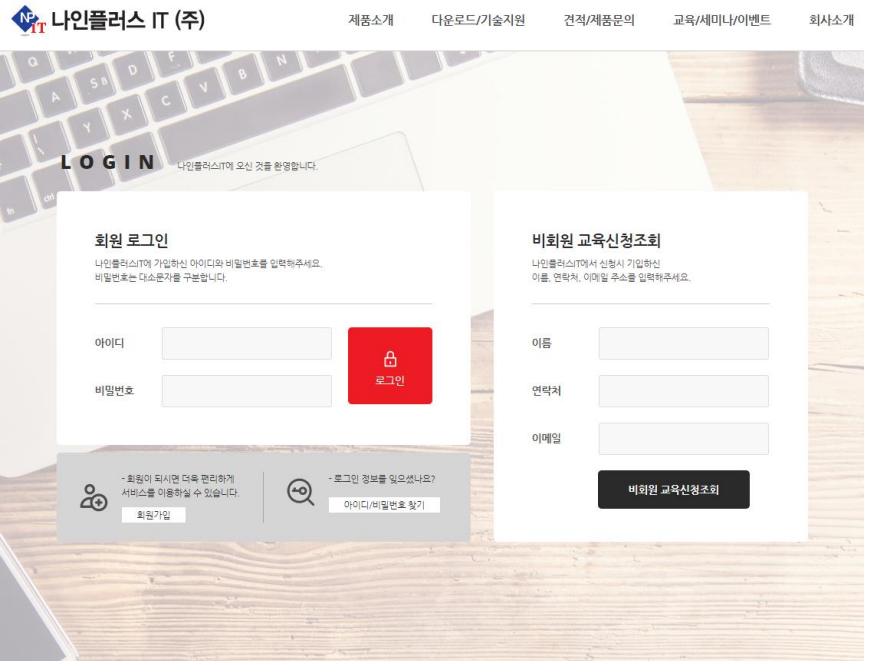


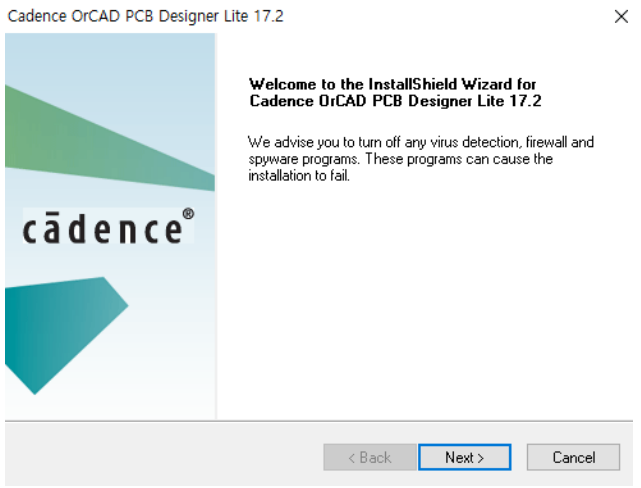
http://www.npit.co.kr/ 접속 -> 다운로드/기술지원 -> Demo(Lite) -> OrCAD Lite (All Product)

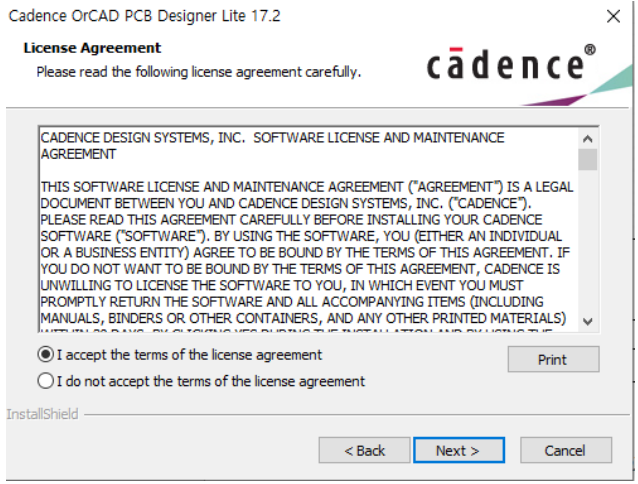
설치

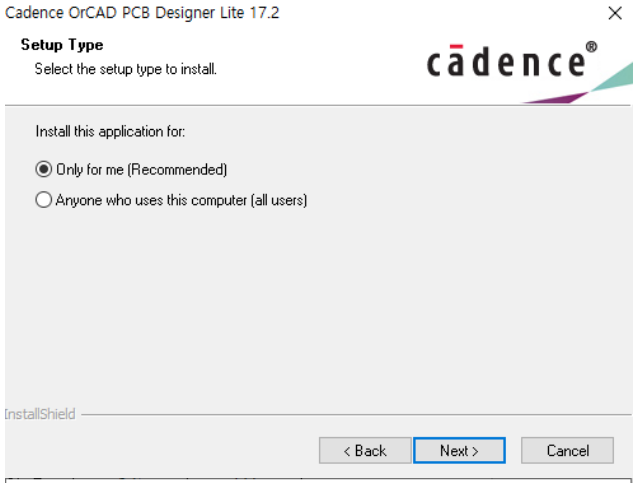
No.	프로그램 명	프로그램 다운로드	등록일	다운로드횟수
5	OrCAD 17.4-2019 Trial (설치방법)		2019-12-20	32
4	OrCAD 17.2-2016 Lite (Capture, PSpice)		2018-10-29	2,891
3	OrCAD 17.2-2016 Lite (All Product)		2018-10-29	1,180
2	OrCAD 16.6-2015 Lite (Capture, PSpice)		2017-03-10	5,547
1	OrCAD 16.6-2015 Lite (All Products)		2017-03-10	5,336

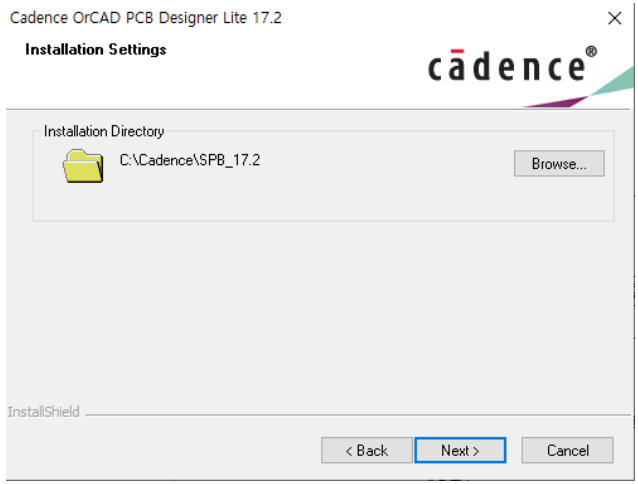
« ‹ 1 › »

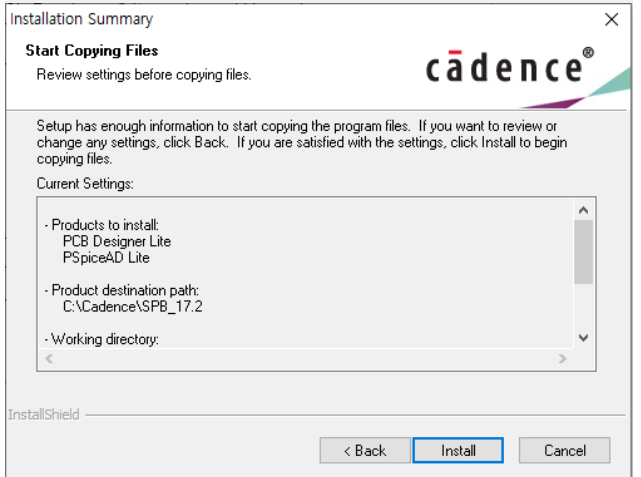




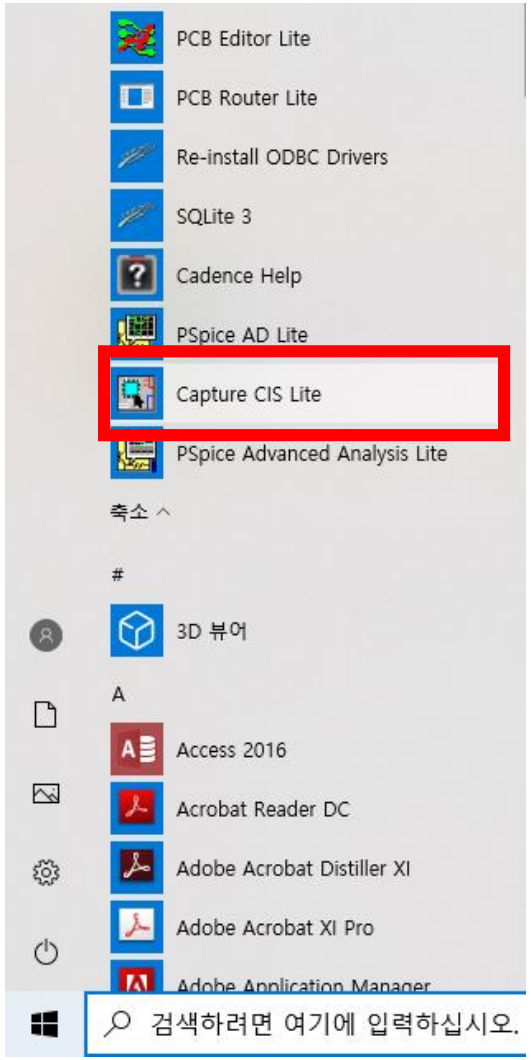


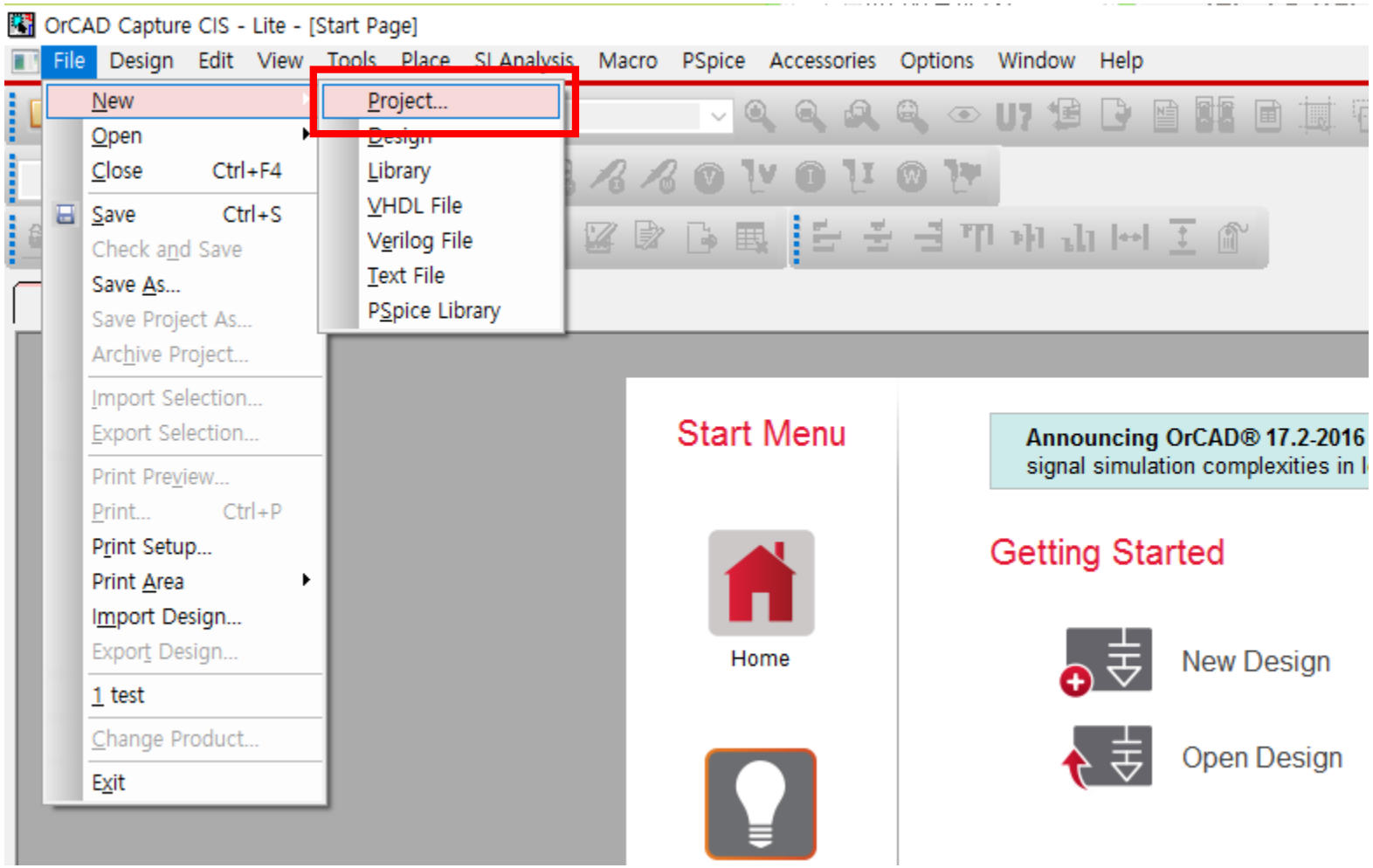






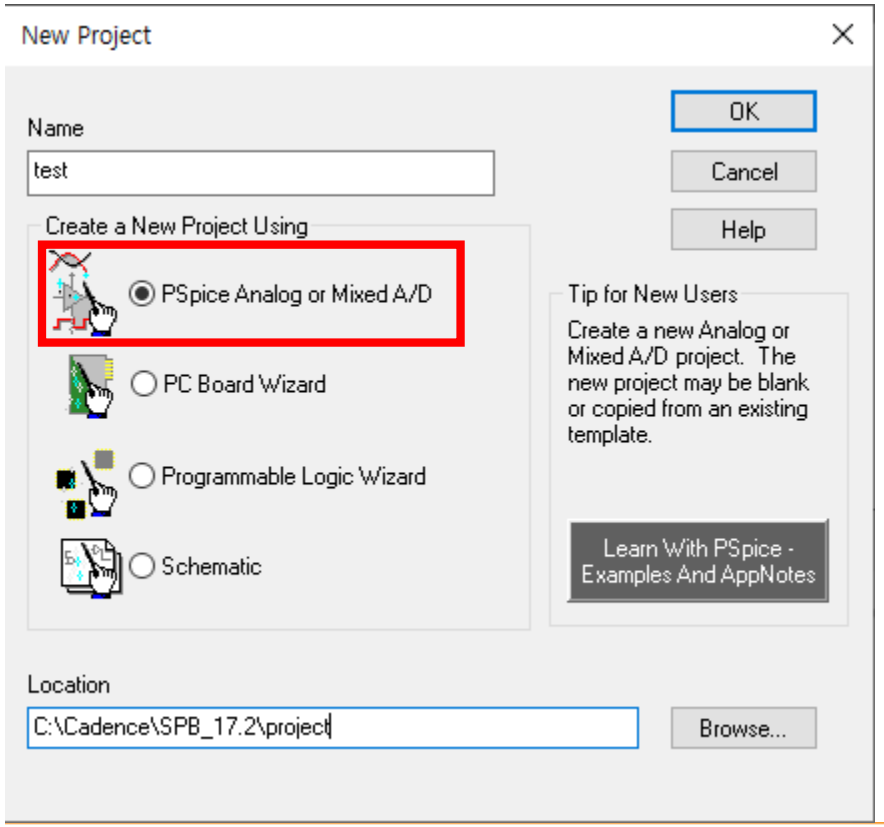
simulation

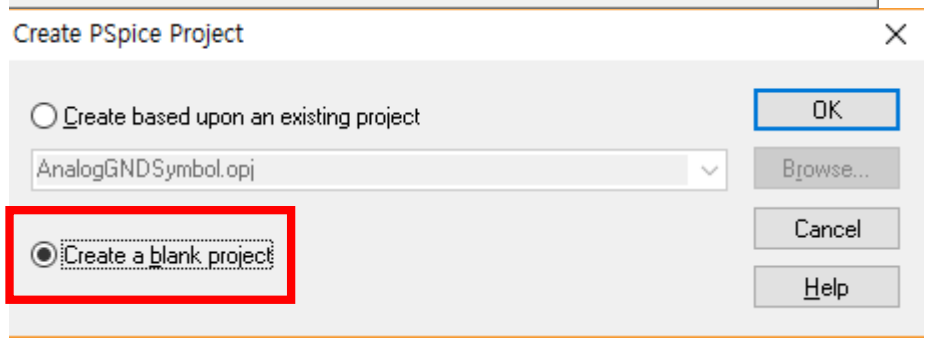


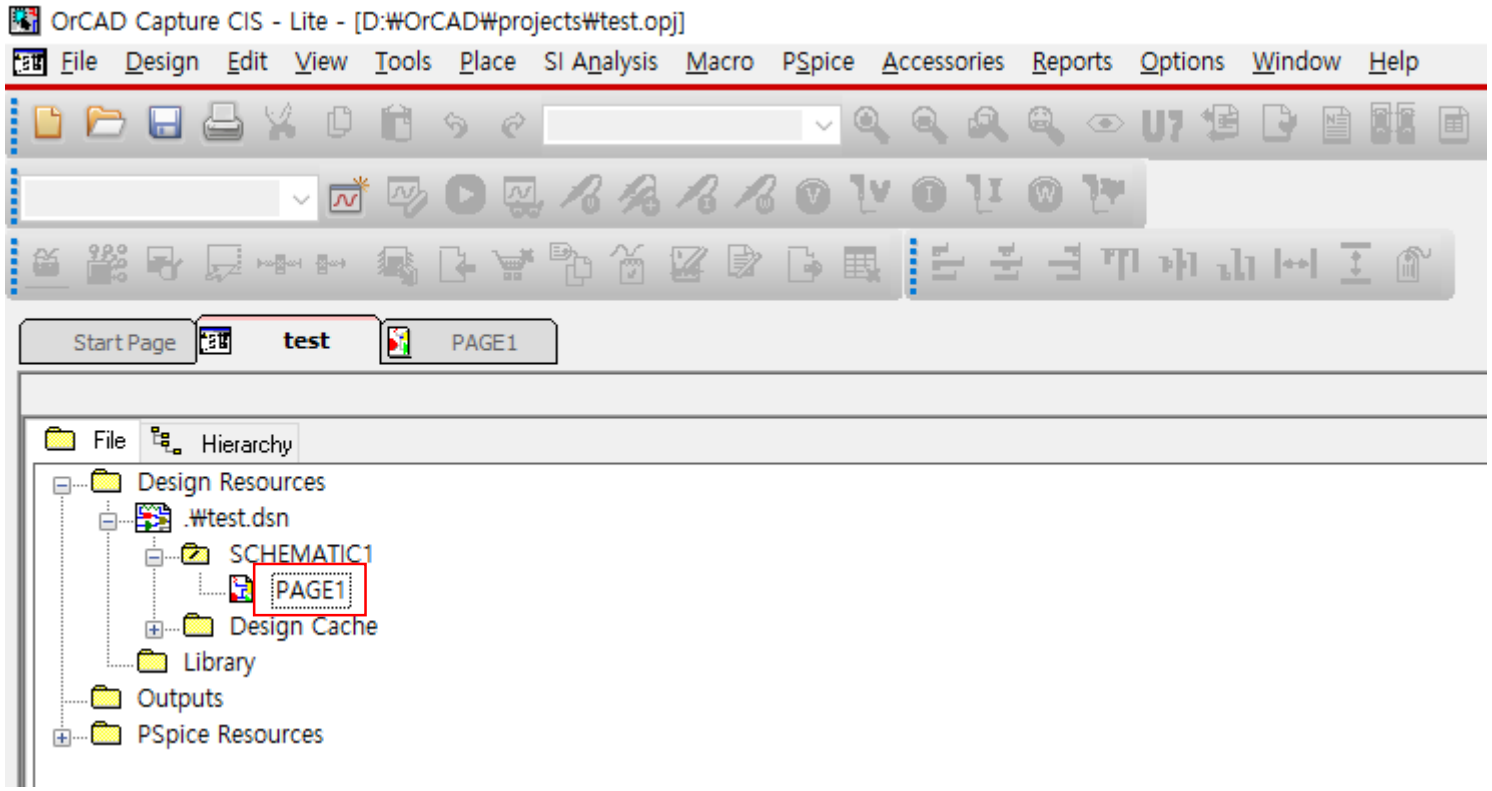


OrCAD Capture CIS Lite 실행 -> File -> New -> Project

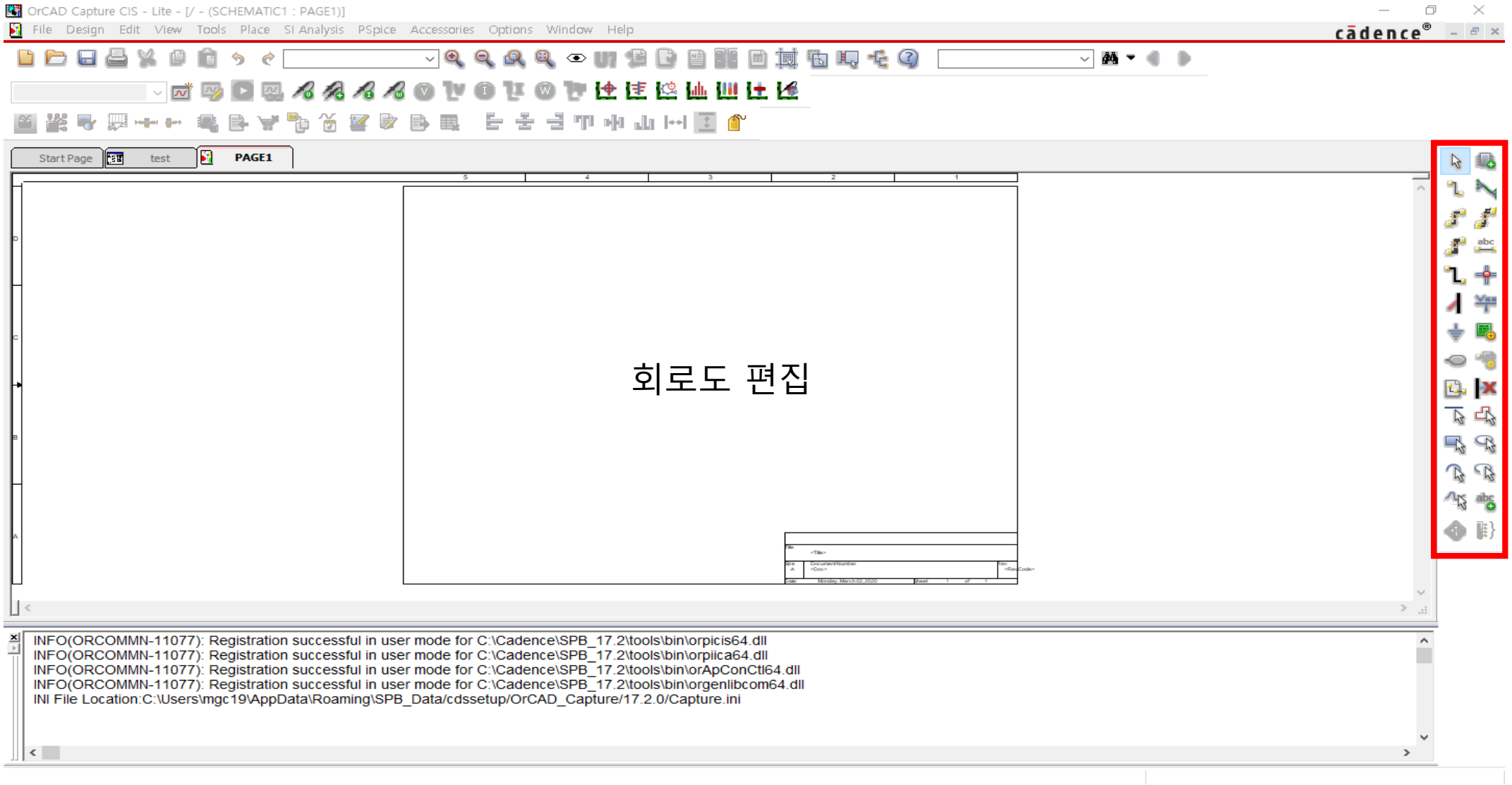
simulation


New Project
Name
test
OK
Cancel
Help
Create a New Project Using
PSpice Analog or Mixed A/D
PC Board Wizard
Programmable Logic Wizard
Schematic
Tip for New Users
Create a new Analog or Mixed A/D project. The new project may be blank or copied from an existing template.
Learn With PSpice - Examples And AppNotes
Location
C:\Cadence\SPB_17.2\project
Browse...

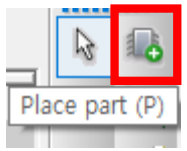

Create PSpice Project
Create based upon an existing project
AnalogGNDSymbol.opj
Browse...
Create a blank project
OK
Cancel
Help


OrCAD Capture CIS - Lite - [D:₩OrCAD₩projects₩test.opj]
File Design Edit View Tools Place SI Analysis Macro PSpice Accessories Reports Options Window Help
Start Page
test
PAGE1
File
Hierarchy
Design Resources
.₩test.dsn
SCHEMATIC1
PAGE1
Design Cache
Library
Outputs
PSpice Resources

simulation

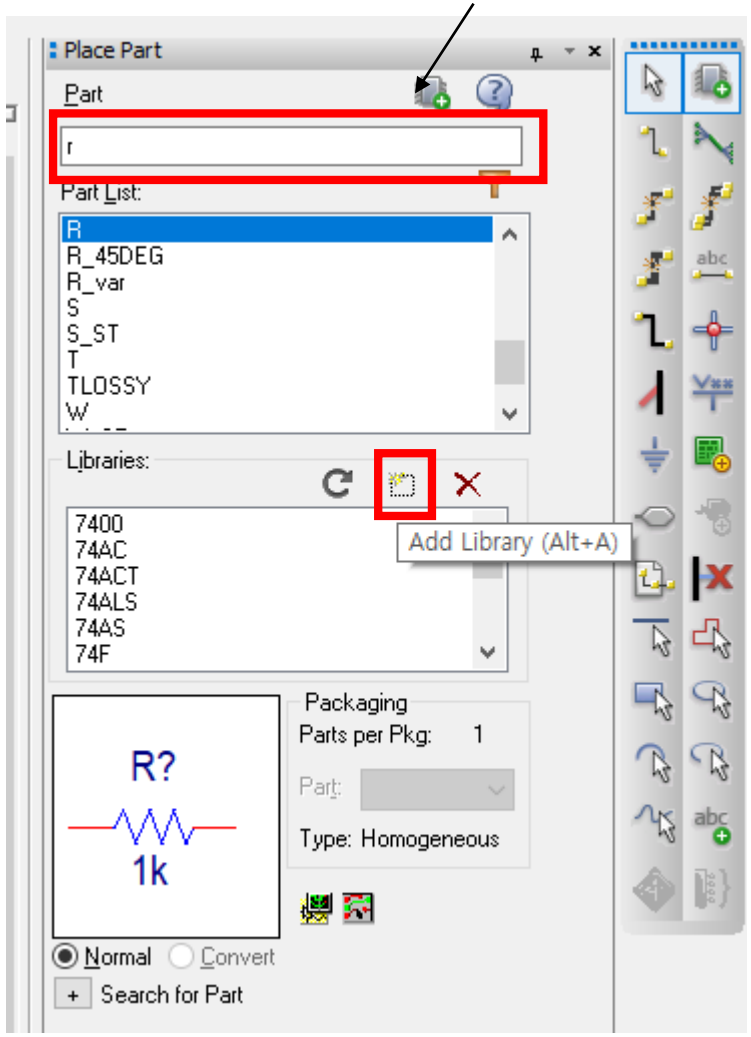

OrCAD Capture CIS - Lite - [/ - (SCHEMATIC1 : PAGE1)]
File Design Edit View Tools Place SI Analysis PSpice Accessories Options Window Help
cadence
Start Page
test
PAGE1
회로도 편집
INFO(ORCOMMN-11077): Registration successful in user mode for C:\Cadence\SPB_17.2\tools\bin\orpicis64.dll
INFO(ORCOMMN-11077): Registration successful in user mode for C:\Cadence\SPB_17.2\tools\bin\orpiica64.dll
INFO(ORCOMMN-11077): Registration successful in user mode for C:\Cadence\SPB_17.2\tools\bin\orApConCtl64.dll
INFO(ORCOMMN-11077): Registration successful in user mode for C:\Cadence\SPB_17.2\tools\bin\orgenlibcom64.dll
INI File Location:C:\Users\mgc19\AppData\Roaming\SPB_Data/cdssetup/OrCAD_Capture/17.2.0/Capture.ini

simulation

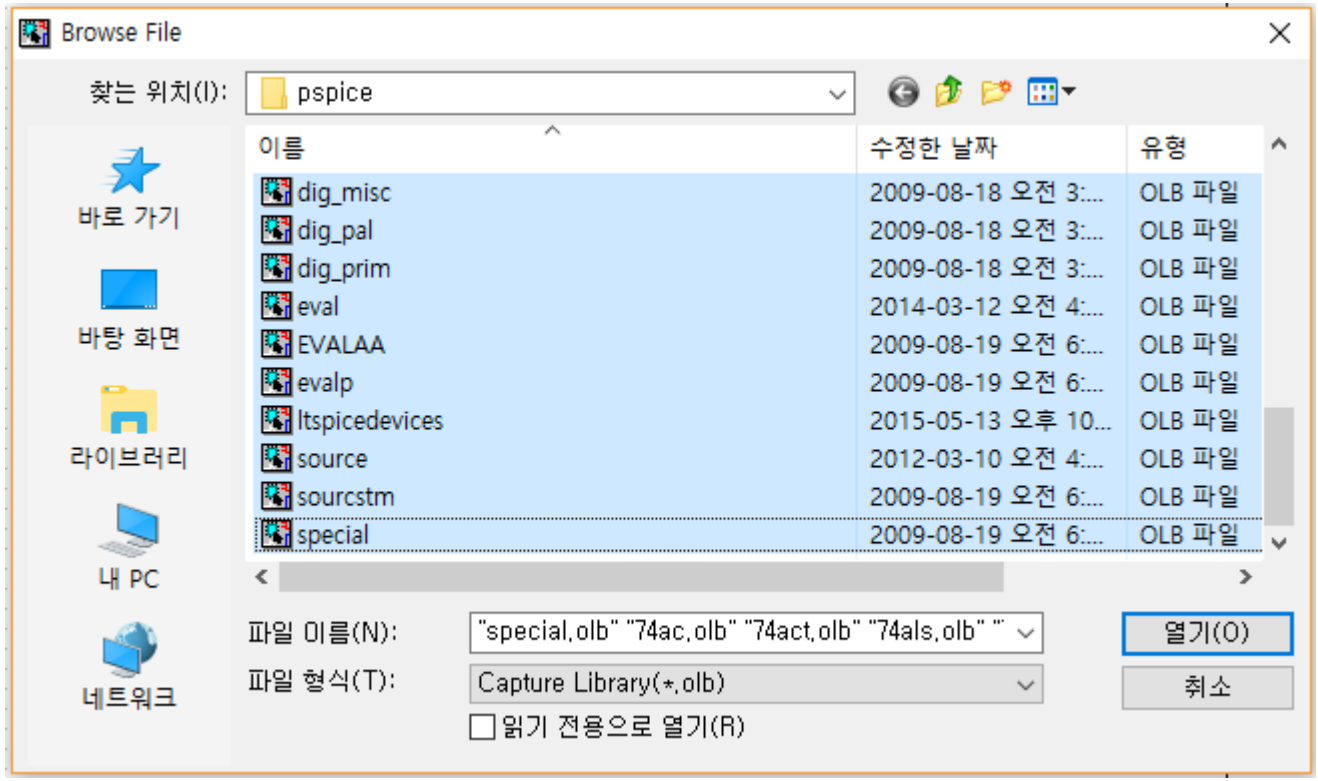


Place part
선택

원하는 부품 이름 입력

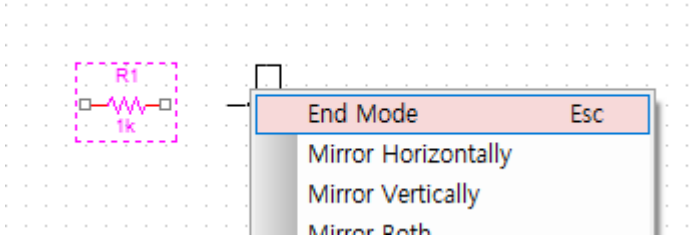


Add Library 선택

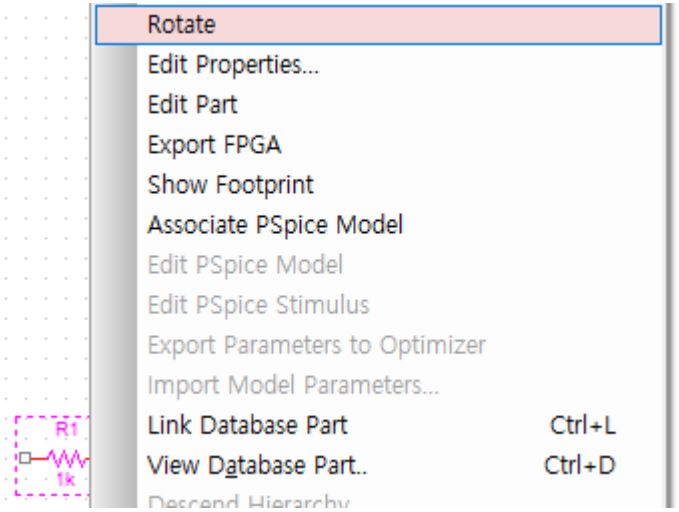


모든 라이브러리 불러오기

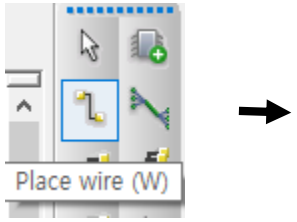
simulation



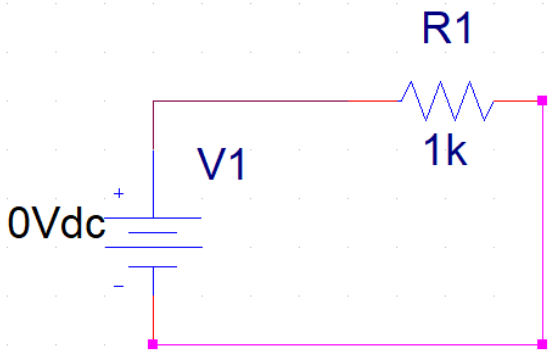
원하는 위치에 부품을 놓은 뒤, 마우스 오른쪽 버튼 클릭 후, End Mode 클릭하여 현재 작업 종료



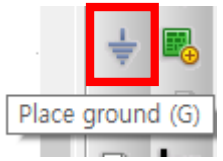
부품을 클릭한 다음 마우스 오른쪽 버튼 클릭 후, Rotate 선택 시 부품 회전 가능

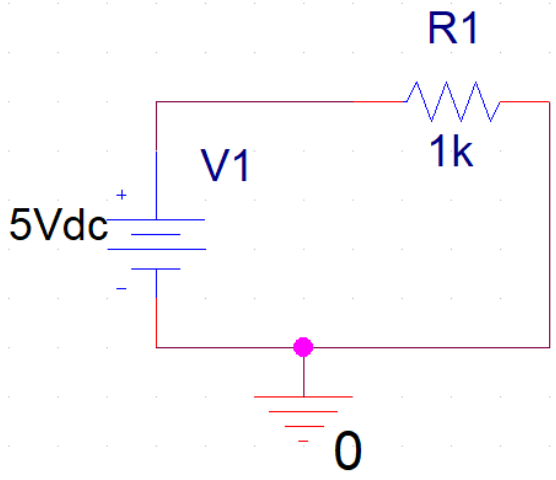


Place wire 선택



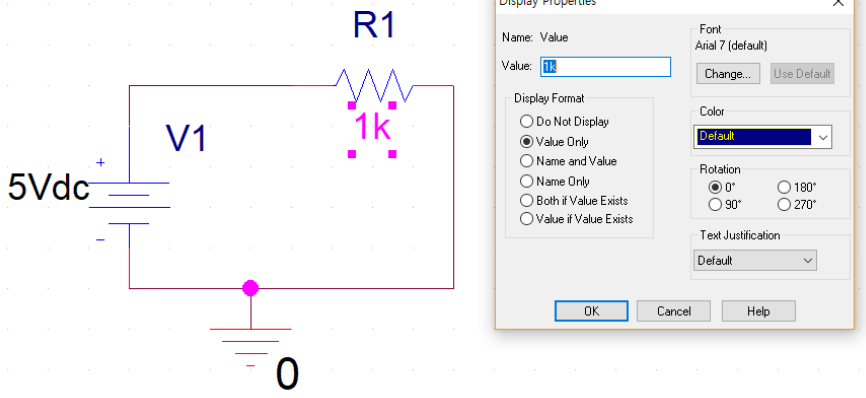
Place wire를 통해 선 연결



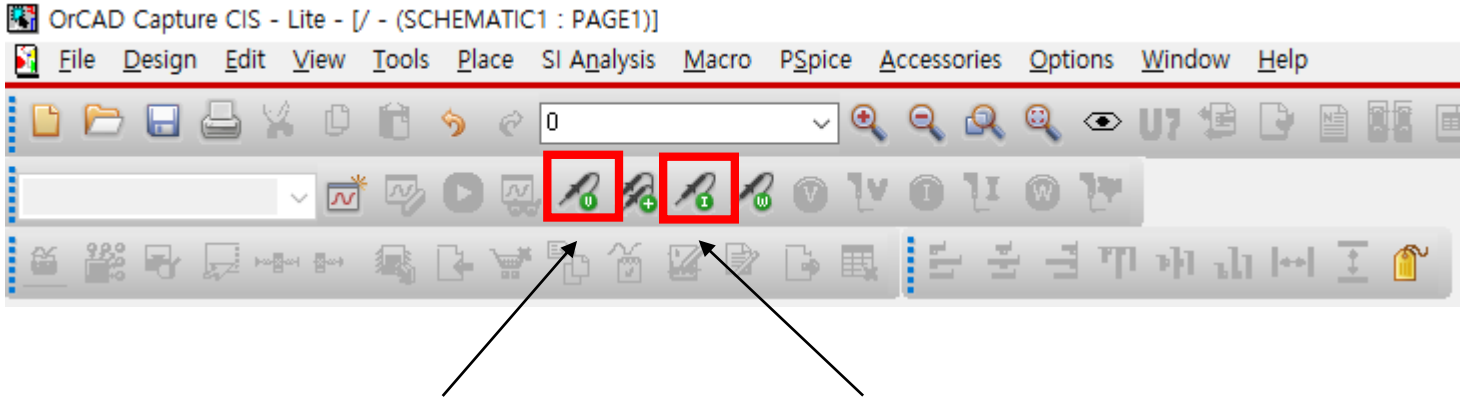


Place ground를 통해 접지

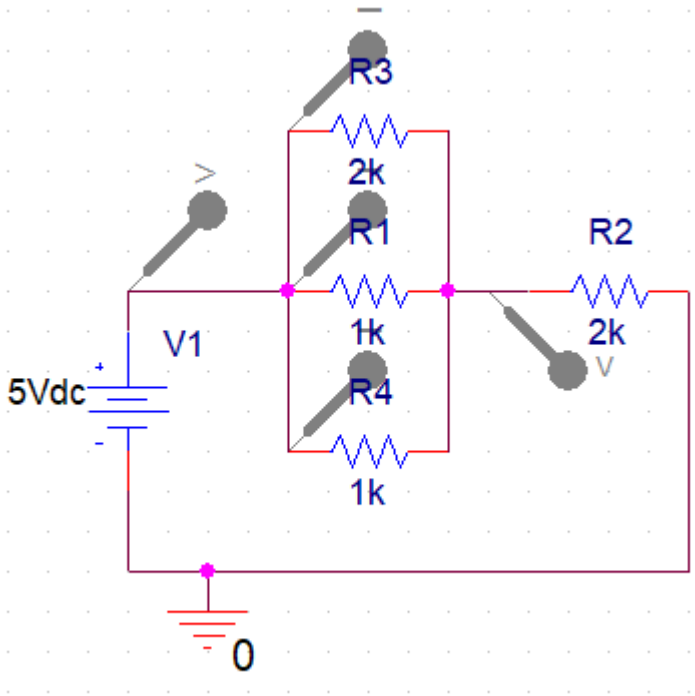
simulation



더블 클릭을 통해 값 수정

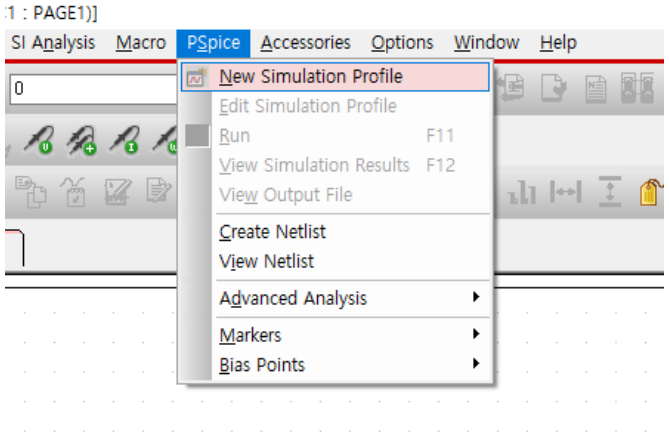


전압 측정 전류 측정

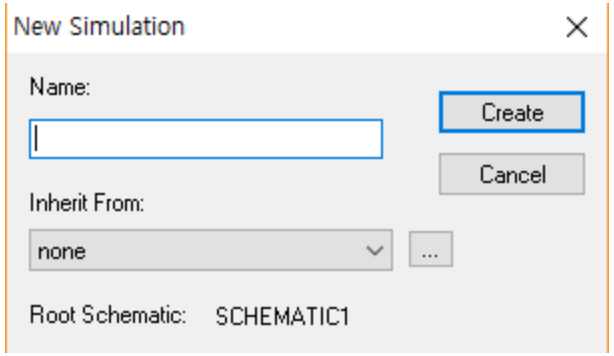


원하는 위치에 프로브

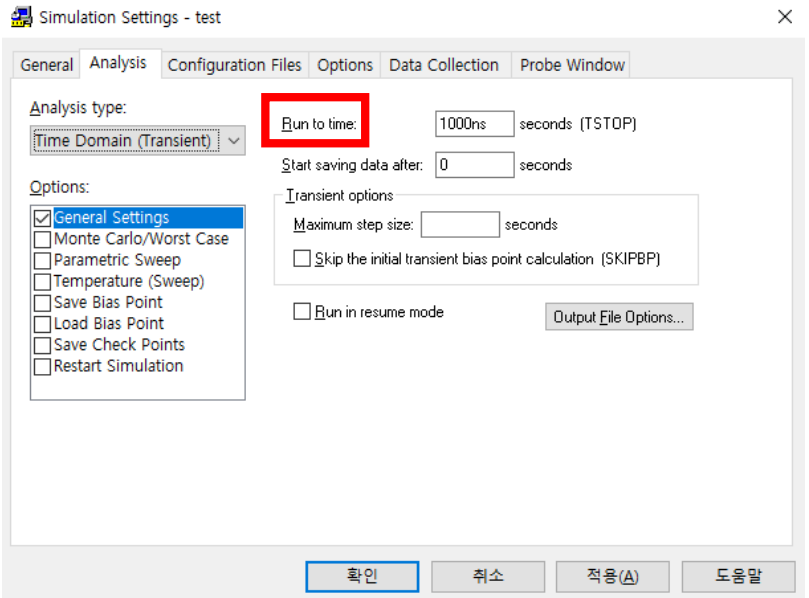
simulation



Pspice->New Simulation Profile 선택

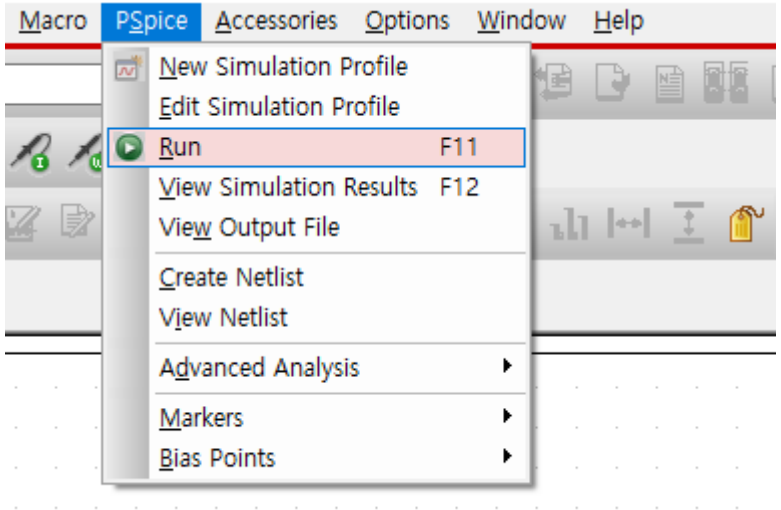


이름 입력후 Create 선택

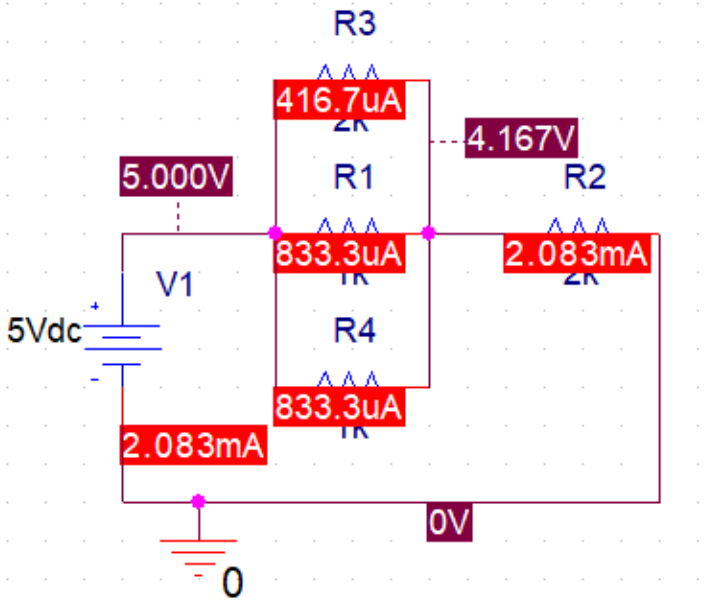


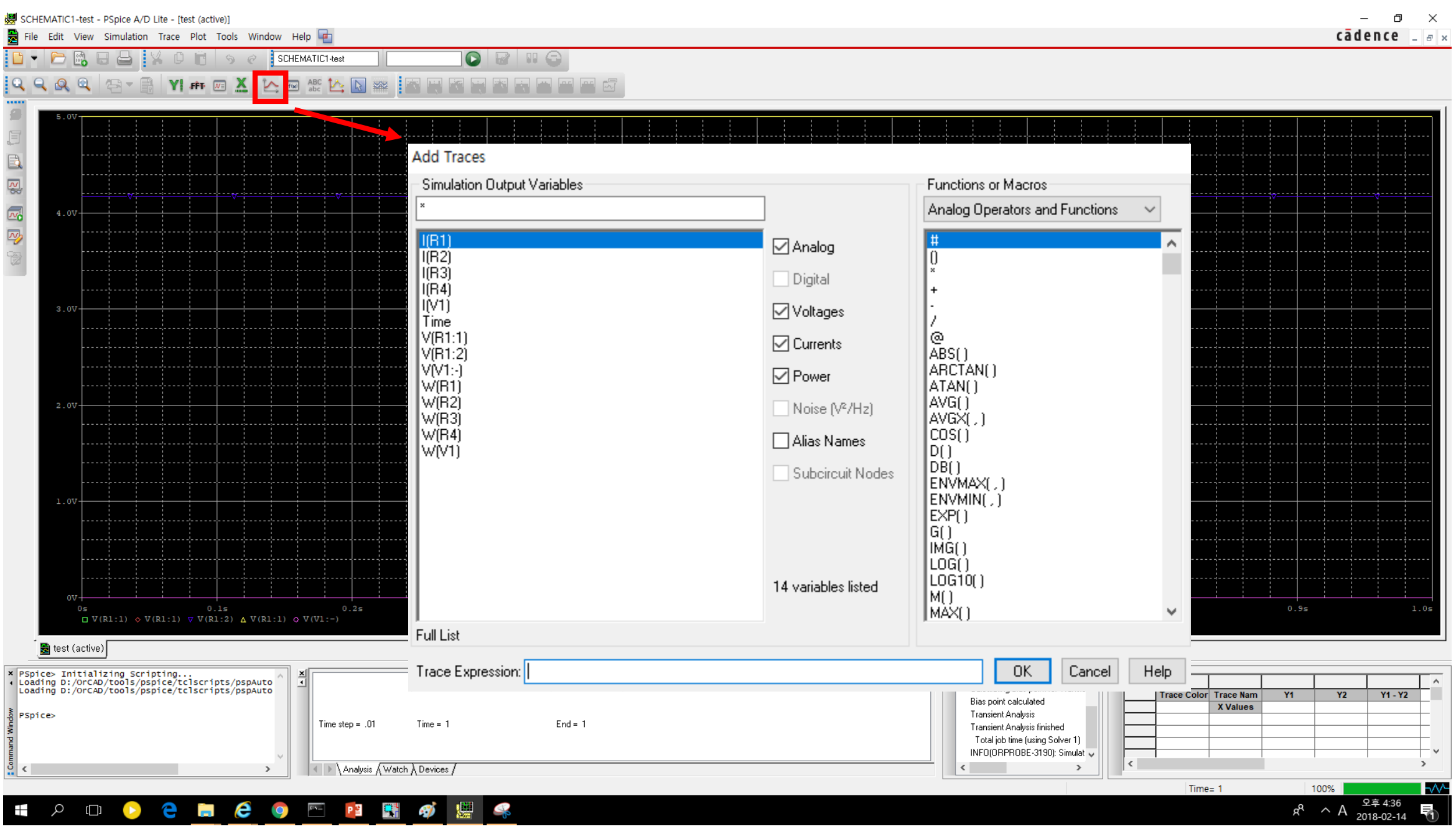
Run to time을 통해 시뮬레이션 시간 설정

simulation



Run을 클릭해 시뮬레이션 실행




SCHEMATIC1-test - PSpice A/D Lite - [test (active)]
File Edit View Simulation Trace Plot Tools Window Help
cadence
Add Traces
Simulation Output Variables
I(R1)
I(R2)
I(R3)
I(R4)
I(V1)
Time
V(R1:1)
V(R1:2)
V(V1:-)
W(R1)
W(R2)
W(R3)
W(R4)
W(V1)
Analog
Digital
Voltages
Currents
Power
Noise (V²/Hz)
Alias Names
Subcircuit Nodes
14 variables listed
Full List
Functions or Macros
Analog Operators and Functions
#
()
*
+
-
/
@
ABS( )
ARCTAN( )
ATAN( )
AVG( )
AVGX(,)
COS( )
D( )
DB( )
ENVMAX(,)
ENVMIN(,)
EXP( )
G( )
IMG( )
LOG( )
LOG10( )
M( )
MAX( )
Trace Expression:
OK
Cancel
Help
5.0V
4.0V
3.0V
2.0V
1.0V
0V
0s
0.1s
0.2s
0.9s
1.0s
V(R1:1) V(R1:1) V(R1:2) V(R1:1) V(V1:-)
test (active)
PSpice> Initializing Scripting...
Loading D:/OrCAD/tools/pspice/tclscripts/pspAuto
Loading D:/OrCAD/tools/pspice/tclscripts/pspAuto
PSpice>
Command Window
Time step = .01
Time = 1
End = 1
Analysis
Watch
Devices
Bias point calculated
Transient Analysis
Transient Analysis finished
Total job time (using Solver 1)
INFO(ORPROBE-3190): Simulat
Trace Color
Trace Nam
Y1
Y2
Y1 - Y2
X Values
Time= 1
100%
오후 4:36
2018-02-14