Department of Electronics & Telecommunication Engineering

PROJECT PROPOSAL

1	Title of the project: **Design, Simulation and Implementation of 4D QFSK Coherent Receiver** Area of project: Digital Communication
2	Abstract: 4 D QFSK is 4 Dimensional Quadrature Frequency Shift Keying Modulation. It is a combination of PSK and FSK to achieve better bandwidth efficiency and power efficiency. Coherent receiver will be used for reception. It requires carrier regeneration, multiplication, integration and decision making circuits.
3	Expected Outcome of the project: Realization of 4D QFSK Receiver
4	Number of interns required - 4
5	Project guide – Prof. Biju Balakrishnan
6	Preferred year of the intern - TE / BE
7	Duration of the project – 6 months

Prof. Pushkar Sathe   Prof Dr. Preeti Hemnani
Project Coordinator   (HOD-EXTC)

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