

VLSI Assignment 1 Questions

1. Illustrate the pass characteristics with a neat diagram.
2. Derive n-FET current voltage equations.
3. Discuss FET RC model.
4. Write VLSI Design Flow.
5. Explain CMOS Fabrication process.
6. Write short notes on:
 - a. Photolithography
 - b. T Gate: XOR and XNOR
 - c. Scaling theory
 - d. Stick diagram