1 Exercise 2

Having the function in maxterms

$$f_1(A, B, C, D) = \prod (M_0, M_1, M_5, M_7, M_8, M_{10}, M_{14}, M_{15})$$

equivalent to

$$f_2(A, B, C, D) = \sum (m_2, m_3, m_4, m_6, m_9, m_{11}, m_{12}, m_{13})$$

using minterms, can be simplify by different ways and represented using logic gates.

1.1 Simplify: Boolean Algebra

Using the Boolean algebra propertie

$$(A+B).(A+\overline{B}) = A (1)$$

or

$$(AB) + (A\overline{B}) = A (2)$$

the function could be simplify using (1):

$$f_1(A, B, C, D) = (A + B + C + D).(A + B + C + \overline{D}).(A + \overline{B} + C + \overline{D}).(A + \overline{B} + \overline{C} + \overline{D}).$$

$$(\overline{A} + B + C + D).(\overline{A} + B + \overline{C} + D).(\overline{A} + \overline{B} + \overline{C} + D).(\overline{A} + \overline{B} + \overline{C} + \overline{D})$$

$$= (A + B + C).(A + \overline{B} + \overline{D}).(\overline{A} + B + D).(\overline{A} + \overline{B} + \overline{C})$$

Which in minterms would be, using (2):

$$f_{2}(A, B, C, D) = (\overline{AB}C\overline{D}) + (\overline{AB}CD) + (\overline{AB}C\overline{D}) + (\overline{AB}C\overline{D}) + (\overline{AB}C\overline{D}) + (A\overline{B}CD) + (AB\overline{C}D) + (AB\overline{C}D) + (AB\overline{C}D)$$
$$= (A\overline{B}D) + (\overline{AB}D) + (\overline{AB}C) + (AB\overline{C}D)$$

1.2 Simplify: Karnaugh Map

Karnaugh map is a easier way to simplify logic experesion when the functions are too complex or too large to handle, cause Karnaugh map gives a more representative view for a faster analisis for it to simplify.

If the simplification is done with minterms, the groups should be of 1, adding each group in case there is more than 1, and in each group the independent variables would be multiplied.

CD A	B 00	01	11	10
00	0	1	1	0
01	0	0	1	1
11	1	0	0	1
10	1	1	0	0

Now grouping the colour groups we get that the function in minterms would be:

$$f_2(A, B, C, D) = (A\overline{B}D) (Red)$$

 $+(\overline{A}B\overline{D}) (Blue)$
 $+(\overline{AB}C) (Orange)$
 $+(AB\overline{C}) (Green)$

The same method could be done with maxterms; grouping O, multipling groups in case there is more than 1, and in each group the independent variables would be added.

1.3 Logic Circuit: AND, OR and NOT

Using the logic gates AND, OR and NOT the simplify version of the function could be represented in the figure below:

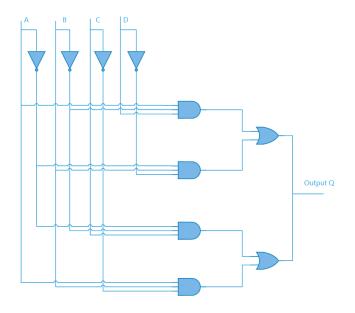


Figure 1: Logic circuit using AND, OR and NOT gates

1.4 Logic Circuit: NAND

All the gates could be equivalent to a combination of NAND or NOR gates. Therefore, the simplify function can be drawn as the next figure:

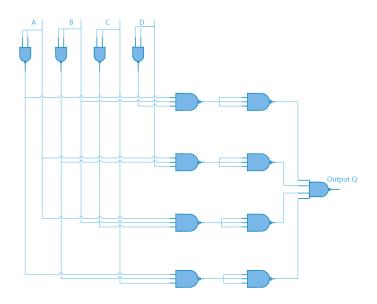


Figure 2: Logic circuit using NAND gates