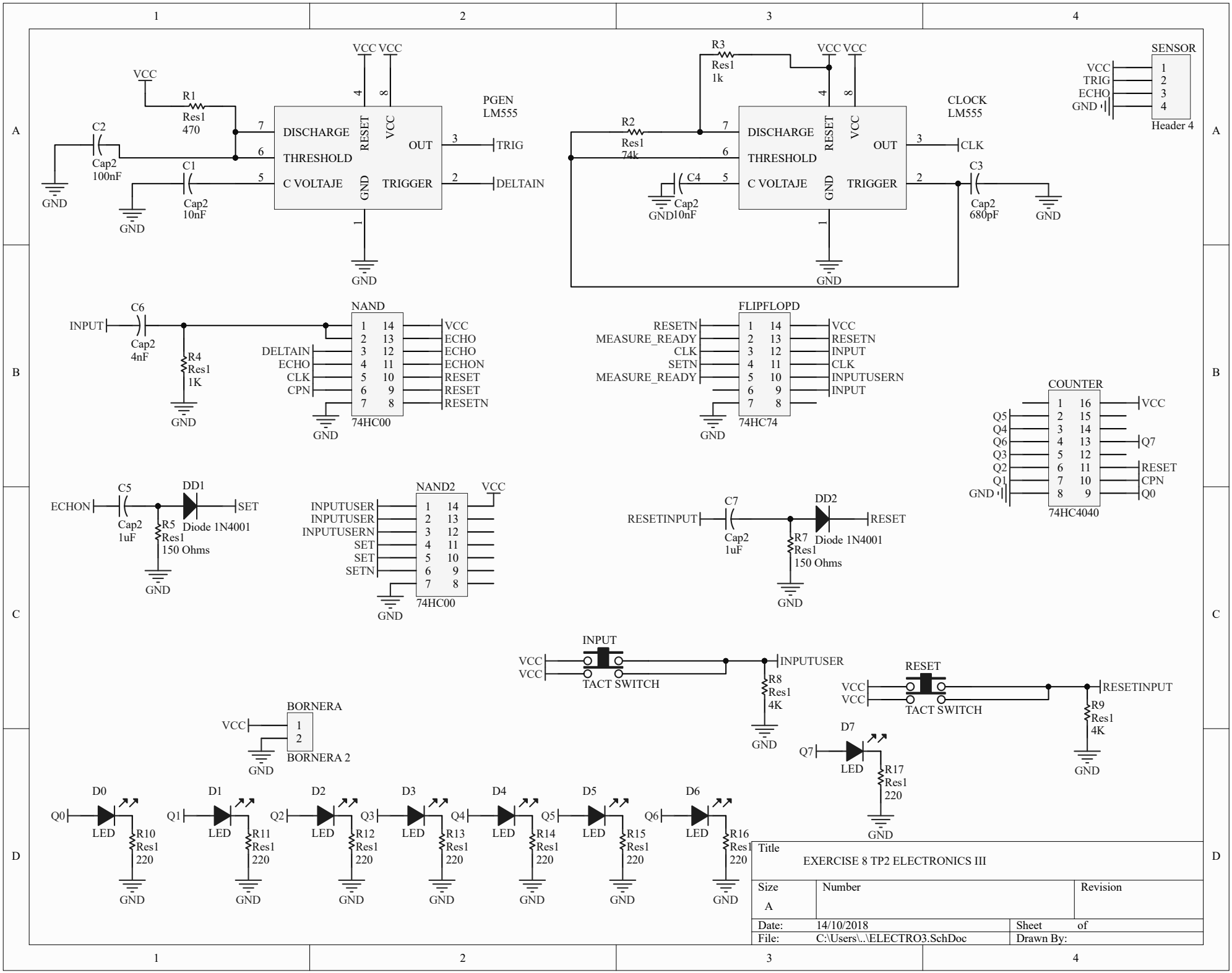
PGEN
LM555
DISCHARGE
THRESHOLD
C VOLTAJE
RESET
VCC
GND
OUT
TRIGGER
TRIG
DELTAIN
R1
Res1
470
C1
Cap2
10nF
C2
Cap2
100nF
CLOCK
LM555
CLK
R2
Res1
74k
R3
Res1
1k
C3
Cap2
680pF
C4
Cap2
10nF
SENSOR
VCC
TRIG
ECHO
GND
Header 4
NAND
74HC00
INPUT
C6
Cap2
4nF
R4
Res1
1K
DELTAIN
ECHO
CLK
CPN
VCC
ECHO
ECHON
RESET
RESETN
FLIPFLOPD
74HC74
RESETN
MEASURE_READY
CLK
SETN
INPUTUSERN
INPUT
COUNTER
74HC4040
Q5
Q4
Q6
Q3
Q2
Q1
Q7
Q0
ECHON
C5
Cap2
1uF
DD1
Diode 1N4001
SET
R5
Res1
150 Ohms
NAND2
INPUTUSER
SETN
RESETINPUT
C7
Cap2
1uF
DD2
R7
Res1
150 Ohms
INPUT
TACT SWITCH
R8
Res1
4K
RESET
R9
Res1
4K
BORNERA
BORNERA 2
D7
LED
R17
Res1
220
D0
D1
D2
D3
D4
D5
D6
R10
R11
R12
R13
R14
R15
R16
Title
EXERCISE 8 TP2 ELECTRONICS III
Size
A
Number
Revision
Date: 14/10/2018
Sheet of
File: C:\Users\..\ELECTRO3.SchDoc
Drawn By: