

# 1 Implementation of Flip-Flop D and SR Latch with discrete logic gates

Using the schematic on Figure 1 the logic gates were implemented on a PCB.

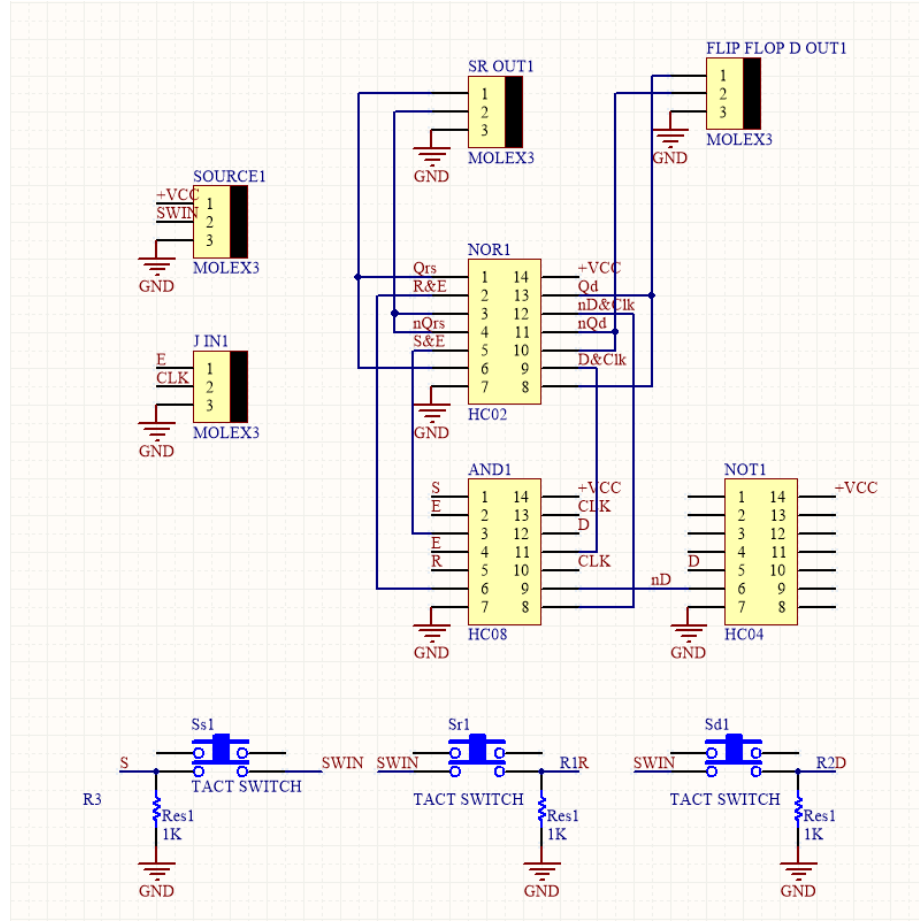


Figure 1: Schematic of the SR Latch (on the left) and Flip-Flop D (on the right)