

Instituto Tecnologico de Buenos Aires

Ingeniera Electronica

ELECTRONICA III

Implementación de circuitos logicos

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NotGATE

hola

 ${\bf Noise Margin}$

Tabla de Verdad

4HC02

TTL y CMOS

Flip Flop y Latch

Async-Sync-Counter

Ultrasonido

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Appendix

Bibliography

[1] Yoda. "A Brief History of Jedism" Journal of The New Jedi Order (Year): 93-98.