Started on	Friday, 18 February 2022, 1:09 PM
State	Finished
Completed on	Friday, 18 February 2022, 1:14 PM
Time taken	4 mins 53 secs
Marks	4.00/5.00
Grade	**1.43** out of 1.79 (**80**%)

Question **1**

Correct

Mark 1.00 out of 1.00

Choose the correct sequence of TCP connection establishment.

a. Client sends **SYN**, server replies with **SYN+ACK**, client replies with **ACK** ✔

b. Client sends **SYN**, server replies with **SYN**, client replies with **SYN+ACK**

c. Client sends **SYN**, server replies with **ACK**, client replies with **SYN+ACK**

Your answer is correct.

Client sends **SYN**, server replies with **SYN+ACK**, client replies with **ACK**

The correct answer is:
Client sends **SYN**, server replies with **SYN+ACK**, client replies with **ACK**

Question **2**

Correct

Mark 1.00 out of 1.00

What does **sequence number** field represent in TCP segment?

a. counting by segments

b. counting by bytes of data ✔

c. counting by retransmissions

Your answer is correct.

counting by bytes of data

The correct answer is:
counting by bytes of data

Question 3

Correct

Mark 1.00 out of 1.00

Select the items that help the TCP to provide reliable data transfer. Select all that apply.

- a. Timeouts ✔
- b. Retransmission ✔
- c. Acknowledgement ✔

Your answer is correct.

- Acknowledgement
- Timeouts
- Retransmission

The correct answers are:
Acknowledgement,

Timeouts,

Retransmission

Question 4

Incorrect

Mark 0.00 out of 1.00

Is the following statement TRUE about flow control of TCP?

- Receiver controls sender, so sender won't overflow receiver's buffer by transmitting too much, too fast

Select one:

- True
- False ✖

True

The correct answer is 'True'.

Question 5

Correct

Mark 1.00 out of 1.00

Match types of switching in router to their respective description.

An input port transfers a packet directly to the output port over a shared bus, without intervention by the routing processor.	Switching via a bus ✔
Switching via a crossbar switch consisting of 2N busses that connect N input ports to N output ports.	Switching via an interconnection network ✔
Switching between input and output ports is under direct control of the CPU.	Switching via a memory ✔

Your answer is correct.

The correct answer is:
An input port transfers a packet directly to the output port over a shared bus, without intervention by the routing processor. → Switching via a bus,

Switching via a crossbar switch consisting of 2N busses that connect N input ports to N output ports. → Switching via an interconnection network,

Switching between input and output ports is under direct control of the CPU. → Switching via a memory