



# System Mode Diagram

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All identified modes are valid

A mode denotes some kind of action / "doing"

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Every mode has at least one transition

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All allowable transitions are labelled

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A single mode is indicated as the default

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Encapsulating block is present

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The reference state is indicated

The reference is indicated by **REF:** <State Name>



## Instructions

For each of the items above, place a checkmark in the applicable box if your SMD complies.

If some elements of the SMD is not compliant correct it before submission

Only submit SMDs for which all elements contain 