FPGA-TRustZone Experiment – Data Acquisition
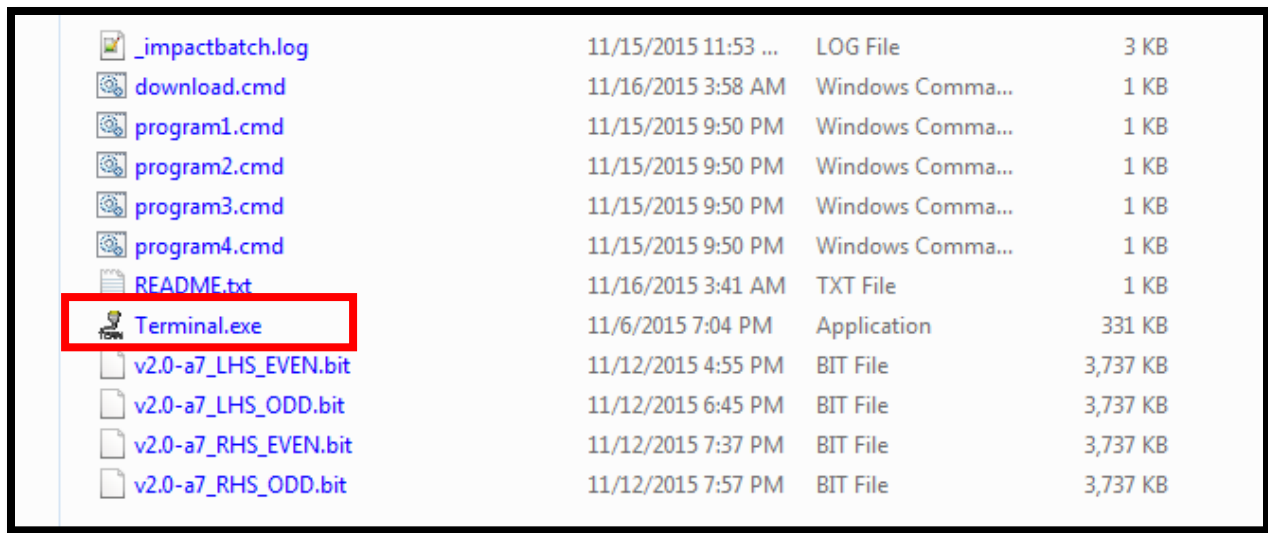
STEP 0: Download zip file from the following link:

http://isis.poly.edu/~vinayakj/documents/FPGA-TRustZone-FastSampling.zip

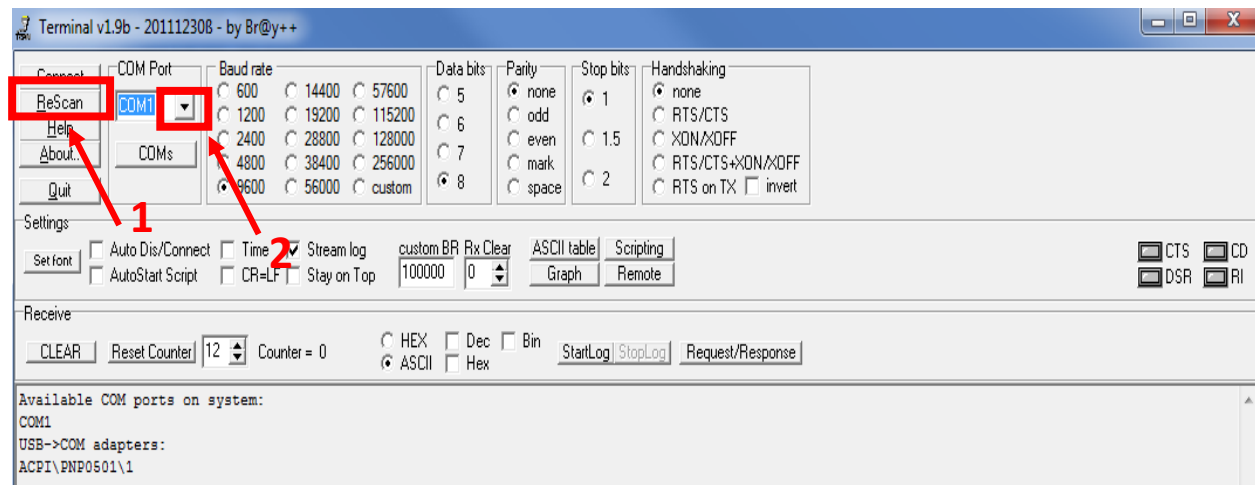STEP 1: Extract the zip folder to C:\ drive.

STEP 2: Connect your FPGA to PC and turn it on.

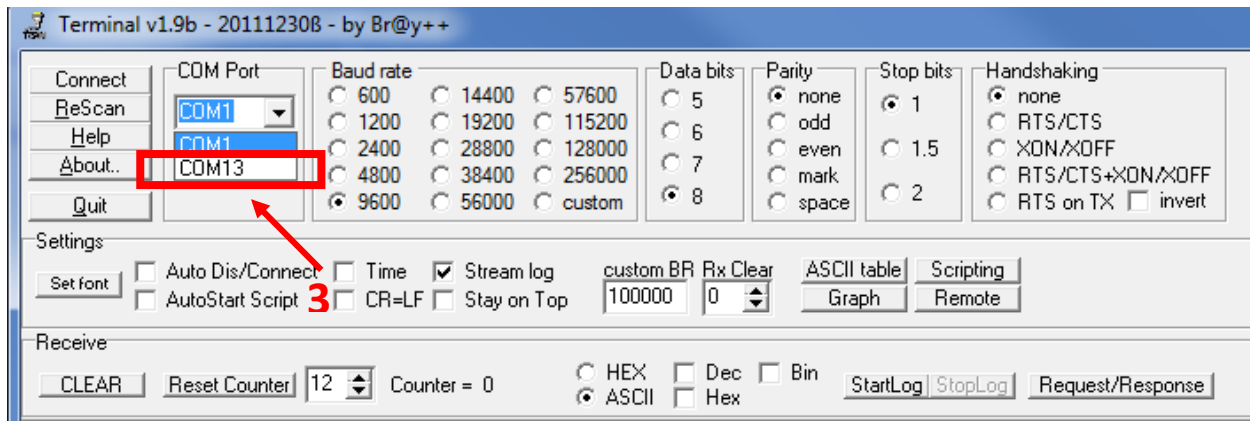STEP 3: Open the saved folder from STEP 1 and start Terminal.exe (Double click to start).

STEP 4: Setting up Terminal

A. Click "ReScan" → Click drop down arrow of "COM Port"

B. Select (Click) the com port **other than** COM1. Something like COM10 or whichever number shows up.

C. Setup other parameters.
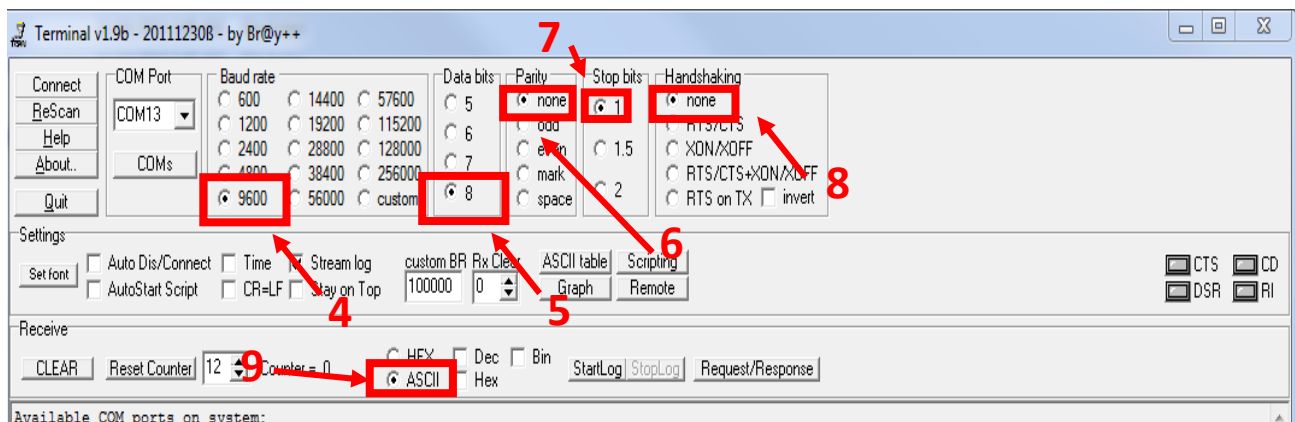
Select Baud Rate → 9600

Data Bits → 8

Parity → None
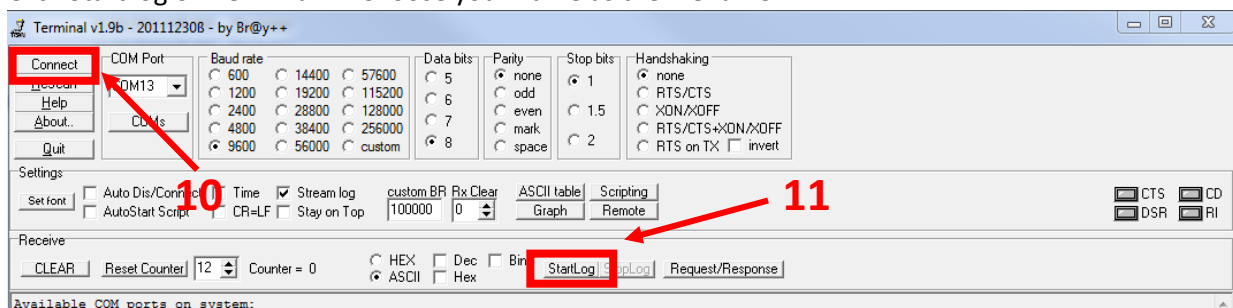
Stop bits → 1

Handshaking → None

Receive → ASCII

STEP 4: Click **"CONNECT"** and **START LOG TO CAPTURE OUTPUT TO FILE**

If everything is correct→ Connect will be successful and it should say "disconnect"

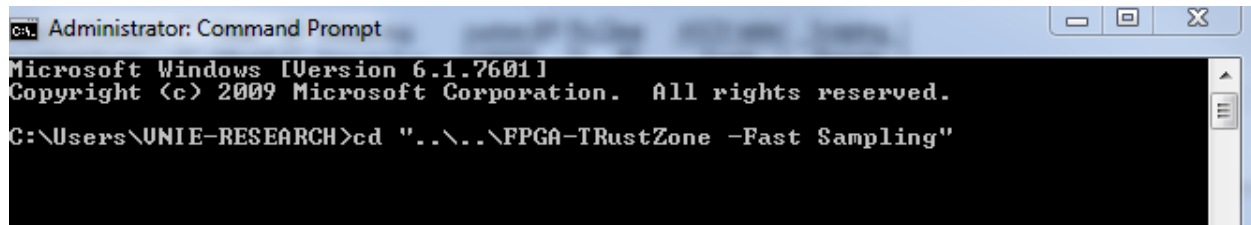Click StartLog on Terminal → Choose your name as the filename.

STEP 5: Start command prompt (Click windows button and type cmd)

STEP 6: cd to the saved directory (from STEP 1)

  If you saved the folder to C:\ drive, You can copy paste the following command :
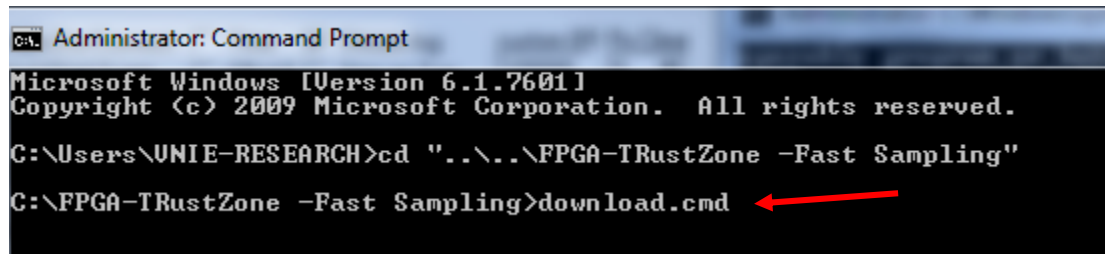
➔   cd "..\..\FPGA-TRustZone -Fast Sampling"

➔   Press enter and execute the command

STEP 7: Make sure the FPGA is connected and ON.

STEP 8: Type "download.cmd" ➔ Enter

STEP 9: SIT BACK for apprx 5 mins till you see DONE on the command prompt.

STEP 10: Stop the capture log in terminal.
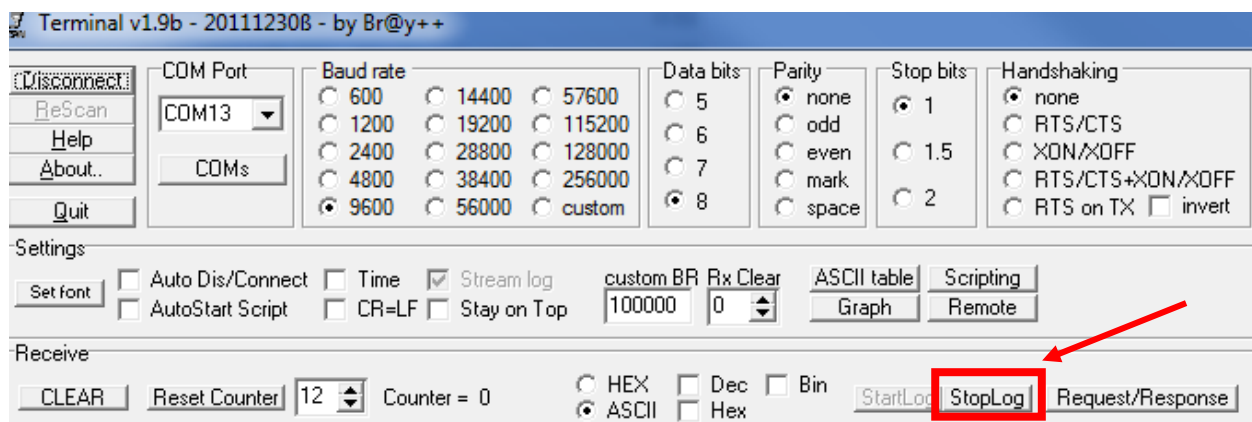
STEP 11: Upload the file to NYU classes the corresponding assignment.

 Thank you for the help!!!!