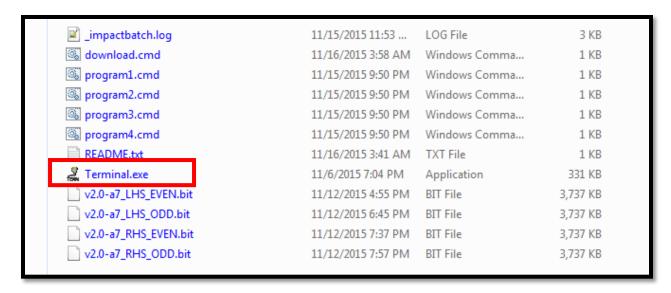
FPGA-TRustZone Experiment – Data Acquisition

STEP 0: Download zip file from the following link:

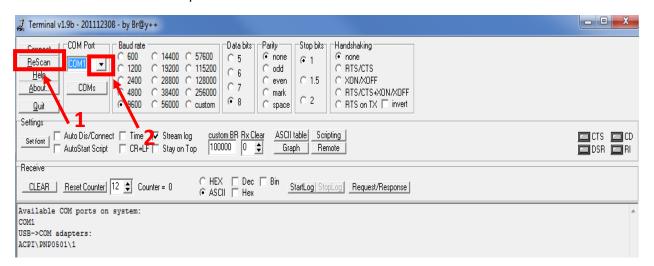
http://isis.poly.edu/~vinayakj/documents/FPGA-TRustZone-FastSampling.zip

- STEP 1: Extract the zip folder to C:\ drive.
- STEP 2: Connect your FPGA to PC and turn it on.
- STEP 3: Open the saved folder from STEP 1 and start Terminal.exe (Double click to start).



STEP 4: Setting up Terminal

A. Click "ReScan" → Click drop down arrow of "COM Port"



B. Select (Click) the com port other than COM1. Something like COM10 or whichever number shows up.

₹ Terminal v1.9b - 20111230ß - by Br@y++										
Connect ReScan Help About.	COM1 COM13	Baud rate 0 600	0 C 115200 C 6 0 C 128000 C 7	Parity Onnone Ondd Oneven Omark Onspace Stop bits On 1 1 1 1 1 1 1 1 1 1 1 1 1	Handshaking none RTS/CTS XON/XOFF RTS/CTS+XON/XOFF RTS on TX invert					
Settings Set font Auto Dis/Connect Time Stream log custom BR Rx Clear ASCII table Scripting AutoStart Script 3 CR=LF Stay on Top 100000 0 \$ Graph Remote										
Receive CLEAR	Reset Counter 12	Counter = 0	C HEX ☐ Dec	e	pLog Request/Response					

C. Setup other parameters.

Select Baud Rate → 9600

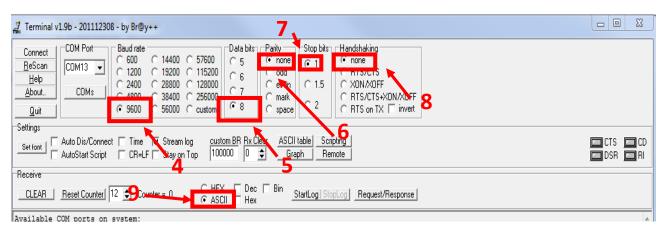
Data Bits → 8

Parity → None

Stop bits → 1

Handshaking → None

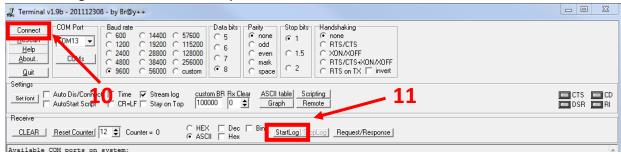
Receive → ASCII



STEP 4: Click "CONNECT" and START LOG TO CAPTURE OUTPUT TO FILE

If everything is correct → Connect will be successful and it should say "disconnect"

Click StartLog on Terminal \rightarrow Choose your name as the filename.

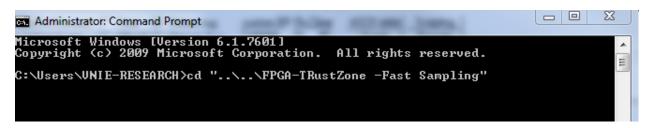


STEP 5: Start command prompt (Click windows button and type cmd)

STEP 6: cd to the saved directory (from STEP 1)

If you saved the folder to C:\ drive, You can copy paste the following command:

- → cd "..\..\FPGA-TRustZone -Fast Sampling"
- → Press enter and execute the command



STEP 7: Make sure the FPGA is connected and ON.

STEP 8: Type "download.cmd" → Enter

Administrator: Command Prompt
Microsoft Windows [Version 6.1.7601] Copyright (c) 2009 Microsoft Corporation. All rights reserved.
C:\Users\VNIE-RESEARCH>cd "\\FPGA-TRustZone -Fast Sampling"
C:\FPGA-TRustZone -Fast Sampling>download.cmd

STEP 9: SIT BACK for apprx 5 mins till you see DONE on the command prompt.

STEP 10: Stop the capture log in terminal.

☑ Terminal v1.9b - 20111230B - by Br@y++												
(Visconnect) ReScan Help About.	COM Port COM13 COMs	Baud rate 600 1200 2400 4800 9600	C 14400 C 19200 C 28800 C 38400 C 56000	C 57600 C 115200 C 128000 C 256000 C custom	Data bits	Parity none odd even mark space	Stop bits 1 1 15 2	Handshaking none RTS/CTS XON/XOFF RTS/CTS+XON/XOFF RTS on TX invert				
Settings Auto Dis/Connect Time Stream log custom BR Rx Clear ASCII table Scripting AutoStart Script CR=LF Stay on Top 100000 0 \$ Graph Remote												
Receive CLEAR	Reset Counter	12 💠 Cou	ınter = 0	C HE>		☐ Bin S	tartLog Stop	pLog Request/Response				

STEP 11: Upload the file to NYU classes the corresponding assignment.

Thank you for the help!!!!