

# CSE450 Exam Cheat Sheet

## Regular Expressions

\* Matches the previous element zero or more times.  
+ Matches the previous element one or more times.  
? Matches the previous element zero or one time.  
{n} Matches the previous element exactly n times.  
{n,} Matches the previous element at least n times.  
{n,m} Matches the previous element at least n times, but no more than m times.  
[character\_group] Matches any single character in character\_group. By default, the match is case-sensitive.  
[~character\_group] Negation: Matches any single character that is not in character\_group. By default, characters in character\_group are case-sensitive.  
[first-last] Character range: Matches any single character in the range from *first* to *last*.  
. Matches any single character in the Unicode general category or named block specified by name.  
^ The match must start at the beginning of the string or line.  
\$ The match must occur at the end of the string or before \n at the end of the line or string.

## Project 5 solution lex

VAL_LITERAL	r'((\d+)(\.\d+)?) (\.\d+)'
CHAR_LITERAL	r'"([^\']* \\n \\t \\\\' \\\\\\\\)'"
STRING_LITERAL	r'"([^\"]* \\n \\t \\\\" \\\\\\\\)*"'
ID	r'[a-zA-Z_][a-zA-Z_0-9]*'
ASSIGN_ADD	r'\+='
ASSIGN_MULT	r'\*='
COMP_EQU	r'=='
COMP_LTE	r'<='
COMP_LESS	r'<'
BOOL_AND	r'&&'
WHITESPACE	r'[\t]'
newline	r'\n'
ASSIGN_SUB	r'\-='
ASSIGN_DIV	r'/'
COMP_NEQU	r'!='
COMP_GTE	r'>='
COMP_GTR	r'>'
BOOL_OR	r'\\ '
COMMENT	r'\\#[^\n]*'

## Context Free Grammars

CFGs Consist of 4 components (Backus-Naur Form or BNF):

Terminal Symbols = token or $\epsilon$	$S \rightarrow aSa$
Non-terminal Symbols = syntactic variables	$S \rightarrow T$
Start Symbol S = special non-terminal	$T \rightarrow bSb$
Production Rules of the form LHS $\rightarrow$ RHS	$T \epsilon$

- LHS = A single non-terminal
- RHS = A string of terminals and non-terminals
- Specify how non-terminals may be expanded
- By default, the LHS of the first production rule is the Start Symbol

Shorthand - vertical bar '|' to combine multiple productions

$S \rightarrow aSa|T$

$T \rightarrow bTb|\epsilon$

## project 5 CFG

program : statements

statements :

statements : statements statement

statement : expression ';' | print\_statement ';' | declaration ';' | block | if\_statement | while\_statement

statement : ';' ,

statement : FLOW\_BREAK ';' ,

if\_statement : FLOW\_IF '(' expression ')' statement %prec IFX

if\_statement : FLOW\_IF '(' expression ')' statement FLOW\_ELSE statement

while\_statement : FLOW\_WHILE '(' expression ')' statement

block : '{' new\_scope statements '}' ,

"new\_scope" :

print\_statement : COMMAND\_PRINT '(' non\_empty\_comma\_sep\_expr ')' ,

non\_empty\_comma\_sep\_expr : expression

non\_empty\_comma\_sep\_expr : non\_empty\_comma\_sep\_expr ',' expression \usepackage{tikz} \usetikzlibrary{shapes}on

expression : var\_usage '=' expression

expression : expression '+' expression | expression '-' expression | expression '\*' expression | expression '/' expression

expression : '-' expression %prec UMINUS

expression : '!' expression

expression : var\_usage ASSIGN\_ADD expression | var\_usage ASSIGN\_SUB expression | var\_usage ASSIGN\_DIV expression | var\_usage ASSIGN\_MULT expression

expression : expression COMP\_EQU expression | expression COMP\_NEQU expression | expression COMP\_LTE expression | expression COMP\_LESS expression | expression COMP\_GTR expression | expression COMP\_GTE expression

expression : expression BOOL\_AND expression | expression BOOL\_OR expression

simple\_declaration : type ID

assign\_declaration : simple\_declaration '=' expression

expression : ID '.' ID '(' ')' ,

statement : ID '.' ID '(' expression ')' ,

declaration : simple\_declaration | assign\_declaration

var\_usage : ID

expression : var\_usage

expression : STRING\_LITERAL

expression : CHAR\_LITERAL

expression : '(' expression ')' ,

type : ARRAY\_KEYWORD '(' TYPE ')' ,

var\_usage : ID '[' expression ']' ,

type : STRING\_KEYWORD

expression : COMMAND\_RANDOM '(' expression ')' ,

## Tube IC

Scaler ones:

<code>val_copy s1 s2</code>	<code>s2 = s1</code>
<code>add s1 s2 s3</code>	<code>s3 = s1 + s2</code>
<code>sub s1 s2 s3</code>	<code>s3 = s1 - s2</code>
<code>mult s1 s2 s3</code>	<code>s3 = s1 * s2</code>
<code>div s1 s2 s3</code>	<code>s3 = s1 / s2</code>
<code>test_less s1 s2 s3</code>	If (s1 < s2) set s3 to 1, else set s3 to 0.
<code>test_gtr s1 s2 s3</code>	If (s1 > s2) set s3 to 1, else set s3 to 0.
<code>test_equ s1 s2 s3</code>	If (s1 == s2) set s3 to 1, else set s3 to 0.
<code>test_nequ s1 s2 s3</code>	If (s1 != s2) set s3 to 1, else set s3 to 0.
<code>test_gte s1 s2 s3</code>	If (s1 >= s2) set s3 to 1, else set s3 to 0.
<code>test_lte s1 s2 s3</code>	If (s1 <= s2) set s3 to 1, else set s3 to 0.
<code>jump Lable</code>	jump to the lable
<code>jump_if_0 s1 Lable</code>	If s1 == 0, jump to Lable.
<code>jump_if_n0 s1 Lable</code>	If s1 != 0, jump to Lable.
<code>random s1 s2</code>	s2 = a random integer x, where 0 <= x < s1.
<code>out_val s1</code>	Write a floating-point value of s1 to standard out.
<code>out_char s1</code>	Write s1 as char to standard out.

**array ones:**

<code>ar_get_idx a1 s2 s3</code>	In a1, find value at index s2, and put into s1.
<code>ar_set_idx a1 s2 s3</code>	In a1, set value at index s2 to the value s3
<code>ar_get_size a1 s2</code>	Calculate the size of a1 and put into s2.
<code>ar_set_size a1 s2</code>	Resize a1 to have s2 entries.
<code>ar_copy a1 a2</code>	Duplicate all values within a1 into a2.

## Tube AC

- There are no scalar or array variables.
- There are eight registers called regA, regB, regC, regD, regE, regF, regG, and regH. These are identical to scalar variables, but you have a limited number of them.
- There are no array-based instructions so you must find replacements for the array instructions.

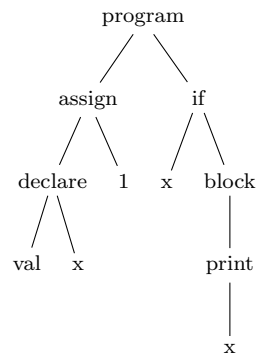
## Flow Control exampls

using them jumps

### IF example

```
val x = 1;
if(x) {
    print(x);
}
```

**IC:**  
`val_copy 1 s2`  
`val_copy s2 s1`  
`jump_if_0 s1 if_1`  
`out_val s1`  
`out_char '\n'`  
`if_1:`



## WHILE

```
val x = 6;
val y = 0;
while(y < x) {
    y += 1;
}
```

### IC:

```
val_copy 6 s2
val_copy s2 s1
val_copy 0 s4
val_copy s4 s3
start_1:
test_less s3 s1 s5
jump_if_0 s5 end_2
val_copy 1 s6
add s3 s6 s7
val_copy s7 s3
jump start_1
end_2:
```

### Assembly inst

L	0	1	2	3	4	5	...	100	101	102	103	104	105
U	106	4	100	105	3	103	...	2	'a'	'b'	1	4	0

