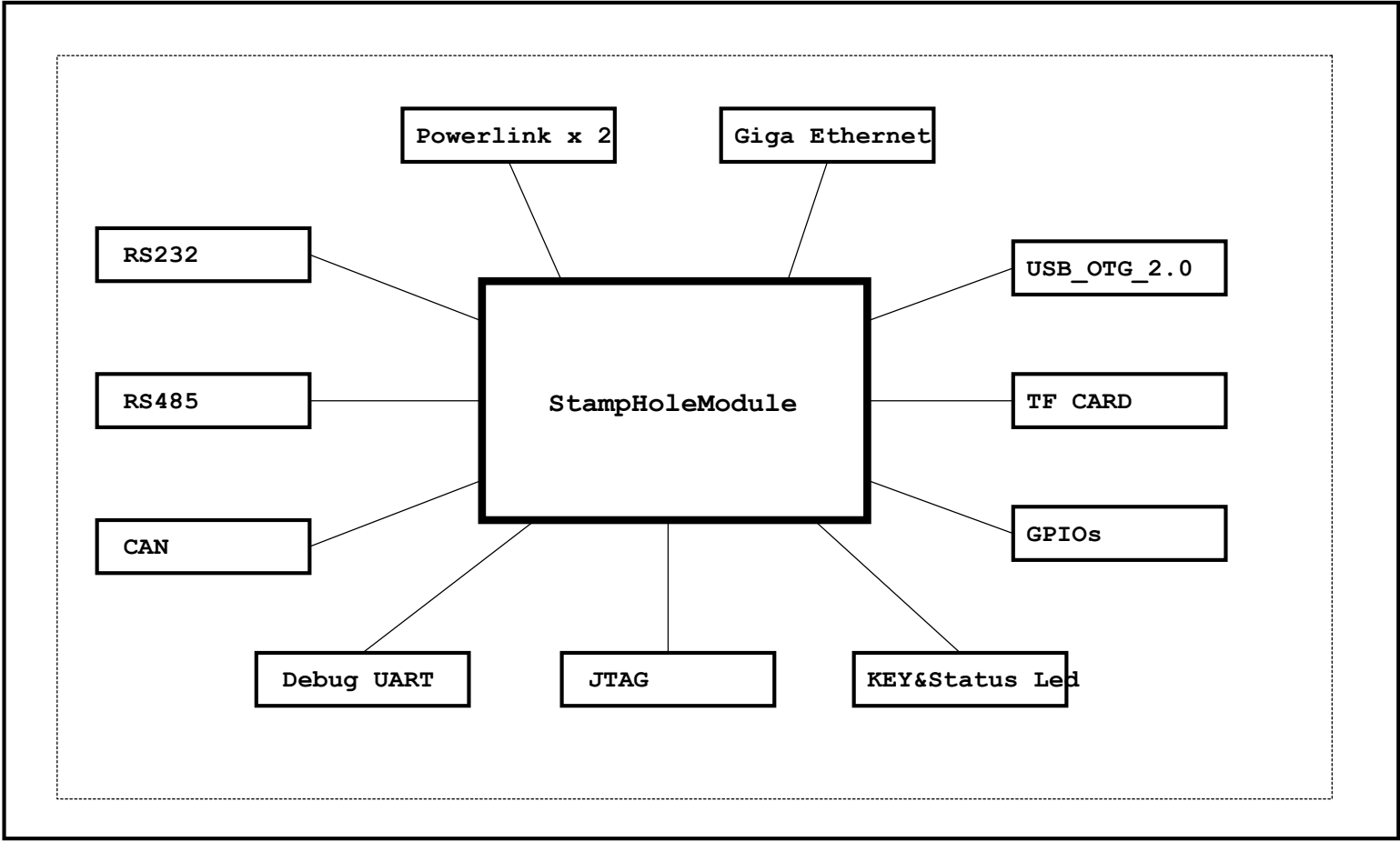


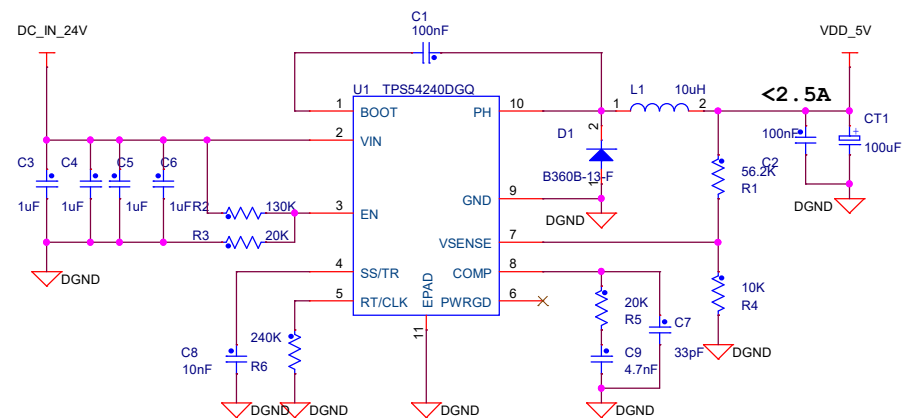
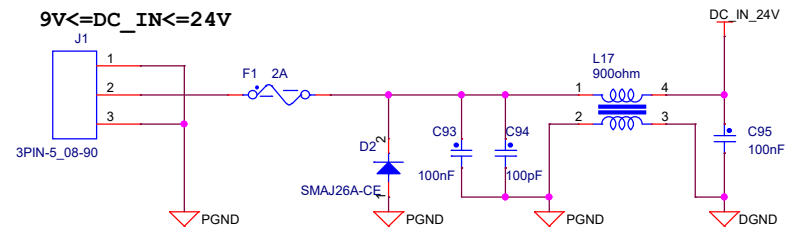
REV	Description	DATE	BY
Ver 1	Initial Release.		X
Ver 1.1	Changed Pin connection of KEY S1.		

PAGE NO.	SCHEMATIC PAGE
1	Title Page
2	BLOCK
3	PowerSystem
4	StampHoleModule
5	PowerLink x 2
6	GigaEthernet &RS232/485/CAN
7	DebugPort &LEDs &Key &GPIOs
8	USB OTG & TF CARD

Title Z7020_10_CLG400		
Size B	Document Number ZQ7020_10	Rev V4.0
Date: Monday, November 06, 2017		Sheet 1 of 8



Title			
Z7020_10_CLG400			
Size	Document Number		Rev
B	ZQ7020_10		V4.0
Date:		Monday, November 06, 2017	Sheet 2 of 8



Title	Z7020_10_CLG400	Rev	V4.0
Size	Document Number		
B	ZQ7020_10		
Date:	Monday, November 06, 2017	Sheet	3 of 8

