

MYC-YZ70XX PinMapTable

Version: V1.0 20170707

Function	SoC Pin Name	Bank	Ra11	Net Name SideA SideC	Not Name	Rall I	Rank	SoC Pin Name	Function
Power-supply pins for Bank 34(HR Bank) / Input				VD0I0 34 PL A1 C1					
BANK 34 Select IO	IO L10P T1 34	Bank 24	MIC	IO B34 LP10 A2 C2	PGMTTA TRDA	_			Ethernet Data 0 Positive
BANK 34 Select 10	TO LIBN TI 34			TO 834 INTO A3 C3	DOMETO TONO	_			Frhernet Data 8 Negative
BANK 35 SELECT TO	TO 123P T3 35			IO_B35_LP23_A4 C4					Ethernet bata 6 negative
				10 833 LP23 A4 04	GND	_			
BANK 35 Select IO	IO L23N T3 35	Bank 35	M15		RGMII0 TRP1				Ethernet Data 1 Positive
				GND A6 C6	RGMII0 TRN1				Ethernet Data 1 Negative
BANK 34 Select IO	IO_L6P_T0_34				GND				
BANK 34 Select IO	IO L6N TO VREF 34			IO 834 LN6 A8 C8 IO 834 LP5 A9 C9	RGMII0 TRP2				Ethernet Data 2 Positive
BANK 34 Select IO	IO_L5P_T0_34								Ethernet Data 2 Negative
BANK 34 Select IO	IO L5N TO 34	Bank 34	T15	IO B34 LN5 A10 C10	GND				
				GND A11 C11	RGMII0 TRP3				Ethernet Data 3 Positive
BANK 34 Select ID	IO L11P T1 SRCC 34	Rank 34	1014	IO B34 LP11 A12 C12	RGMTTR TRN3				Ethernet Data 3 Negative
BANK 34 Select TO	IO L11N T1 SRCC 34	Bank 24	Inc		GND				
RANK 34 Select TO	IO L9P T1 DQS 34			IO_B34_LP9_A14 C14		_			Ethernet Link LED / Output / 1.8V IO Standard.
BANK 34 Select IO	IO L9N T1 DQS 34			IO B34 LN9 A15 C15	DOMETO LED ACT	_			Ethernet Status LED / Output / 1.8V IO Standard.
BARK 34 SELECT ID	10 Faw 11 DG2 34	Dallik 34	017	GND A16 C16					Ethernet PHY Management data clock output / 1.8V IO Standard
			V17	IO B34 LP21 A17 C17	MDIO CK	C10	Bank S01	PS MI052 501	Ethernet PHY Management data Clock output / 1.8V IO Standard Ethernet PHY Management data input/output / 1.8V IO Standard
BANK 34 Select IO	IO_L21P_T3_DQS_34							PS_MI053_501	
BANK 34 Select IO	IO L21N T3 DQS 34			IO B34 LN21 A18 C18	FPGA CONFIG DONE	R11	Bank 0	DONE_0	Active High, DONE indicates successful completion of configuration. / Output / 3.3V IO Standard
BANK 34 Select IO	IO_L19P_T3_34			IO_B34_LP19_A19 C19					Boot Mode setting / See Note1
BANK 34 Select ID	IO L19N T3 VREF 34	Bank 34	R17	IO 834 LN19 A20 C20	BOOT_JP2				Boot Mode setting / See Note1
				GND A21 C21	VDDIO 500 PS				Power-supply for Peripherals of Bank 500(3.3V) / Output
Output signal used to feed watchdog				WDT FEED OUT A22 C22	VCCBATT				Decryptor key memory backup supply / Input / If not used, should be tied to GND or 1.8V.
Tie this signal to WDT FEED OUT to enable watchdog; leave open to disable watchdog.				WDT FEED IN A23 C23	PS MTD39(USR1HS D7)	C18	Rank 581	PS MI039 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select TO	IO L18P T2 34	Rank 34	V16	IO 834 LP18 A24 C24	PS MTO31 (USB1HS NXT)			PS MI031 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select ID	IO L18N T2 34			IO B34 LN18 A25 C25				PS MI028 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
MINIST STRANGE AV	10 L164 12 34	watte 34	MTD	10 004 1010 8420 125	DE MADOS (NEUSTRE CAD)	C10	Deals 501	PS MI028 501 PS MI030 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO L16P T2 34	Deals 77	100	GND A26 C26 IO B34 LP16 A27 C27	L2 LITO36(RESTRE 20)	C15	Dank 501	PS MI030 501 PS MI032 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO L16N T2 34			IO B34 LN16 A28 C28	PS M1U33(USB1HS D1)			PS MI033 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO L15P T2 DQS 34	Bank 34	T20	IO B34 LP15 A29 C29	PS MIO29(USB1HS DIR)	C13	Bank 501	PS MI029 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO_L15N_T2_DQS_34	Bank 34	U20	IO_B34_LN15_A30 C30	PS_MIO34(USB1HS_D2)	A12	Bank 501	PS_MI034_501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
				GND A31 C31	PS MIO38(USB1HS D6)	E13	Bank 501	PS MI038 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO_L7P_T1_34	Bank 34	Y16	IO_B34_LP7_A32 C32	PS_MI036(USB1HS_CLK)	A11	Bank 501	PS_MI036_501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select IO	IO L7N T1 34	Bank 34	Y17	IO B34 LN7 A33 C33				_	
BANK 34 Select IO	IO L17P T2 34			IO B34 LP17 A34 C34		F12	Bank 581	PS MI035 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
BANK 34 Select ID	IO L17N T2 34			IO_B34_LN17_A35 C35	PS MTD37(USB1HS DS)	A10	Bank 581	PS MI037 501	Bank 501 MIO / USB1 Controller Signal by default / 1.8V IO Standard
				GND A36 C36	COTOO CHO			PS MI041 501	SDIO Command / 1.8V IO Standard
BANK 34 Select ID	IO L24P T3 34	Deels 24	P15	IO B34 LP24 A37 C37	COTOO CO			PS MI041 581	SDIO Card Detection / 1.8V IO Standard
BANK 34 Select IO	IO L24N T3 34			IO B34 LN24 A38 C38	SD100 D4	B15	Bank 501	PS MIO45 501	SDIO Data 4 / 1.8V IO Standard
BANK 34 Select IO	IO L22P T3 34			IO B34 LP22 A39 C39		D14	Bank 501	PS MI040 501	SDIO Clock / 1.8V IO Standard
BANK 34 Select IO	IO_L22N_T3_34	Bank 34	W19	IO_B34_LN22_A40 C48					
				GND A41 C41	SDIO0 D3			PS MI044 501	SDIO Data 3 / 1.8V IO Standard
BANK 34 Select IO	IO_L12P_T1_MRCC_34	Bank 34	U18	IO_B34_LP12_A42 C42	SDIO0_D1	E12	Bank 501	PS_MI042_501	SDIO Data 1 / 1.8V IO Standard
BANK 34 Select IO	IO L12N T1 MRCC 34	Bank 34	U19	IO B34 LN12 A43 C43		A9	Bank 501	PS MIO43 501	SDIO Data 2 / 1.8V IO Standard
BANK 34 Select ID	IO L20P T3 34	Bank 34	T17	IO B34 LP28 A44 C44	UART1 TX	B12	Bank 501	PS MI048 501	Debug UART TX / 1.8V IO Standard
BANK 34 Select ID	IO L20N T3 34	Rank 34	818	IO_B34_LN28_A45 C45	HART1 RY	C12	Bank 581	PS_MI049_501	Debug UART RX / 1.8V IO Standard
				GND A46 C46	EVT DET TM				Active-low signal Used for system reset / Input / 3.3V IO Standard
BANK 34 Select IO	IO L23P T3 34	Bank 24	N17	IO B34 LP23 A47 C47	DC COO DECET OUTS	ne	2 Jan 2	PS MI07 500	Set low to reset peripherals / Output / 3.3V IO Standard
BANK 34 Select 10	TO 123W T3 34			IO B34 LN23 A48 C48				PROGRAM B 0	Active Low, asynchronous reset to configuration logic. / Input / 3.3V IO Standard
						LD	Dallk 0	PRUGRAM B 6	Active tow, asymptomous reset to contiguration logic. / Imput / 5.50 to Standard
BANK 34 Select IO	IO L14P T2 SRCC 34	Bank 34	N20	IO B34 LP14 A49 C49	XADC VCC				XADC analog supply voltage(1.8V), / Output for Peripherals.
BANK 34 Select IO	IO_L14N_T2_SRCC_34	Bank 34	P20	IO_B34_LN14 A50 C50					XADC analog ground
				GND A51 C51	Sys_PG				Open-Drain output signal, high voltage indicates SOM Power Good.
BANK 35 Select IO	IO_L8P_T1_AD10P_35			IO_B35_LP8_A52 C52	VDDIO_501_PS				Power-supply for Peripherals of Bank 501(1.8V) / Output
BANK 35 Select IO	IO L8N T1 AD10N 35			IO B35 LNB A53 C53	GND				
BANK 34 Select IO	IO L13P T2 MRCC 34	Bank 34	N18	IO B34 LP13 A54 C54	VDD SV				DCSV PowerSupply / Input
BANK 34 Select ID	IO L13N T2 MRCC 34	Bank 34	P19	IO B34 LN13 A55 C55	VDD 5V				DCSV PowerSupply / Input
Function	SoC Pin Name	Deet.	0-11	Net Name SideB SideD	Not None	n-11 I	Don't	SoC Pin Name	Function
	SOC Pin Name	Bank	8911			Rall	Bank	SOC Pin Name	Function
Power-supply pins for Bank 35(HR Bank) / Input			-	VDDIO 35 PL B1 D1	GNU				
BANK 35 Select IO	IO L7P T1 AD2P 35	Bank 35	M19				Bank 0		JTAG Test Clock / 3.3V IO Standard
BANK 35 Select IO	IO_L7N_T1_AD2N_35	Bank 35	M20				Bank 0		DTAG Test Data Input / 3.3V IO Standard
BANK 35 Select IO	IO L21P T3 DQS AD14P 35	Bank 35	N15	IO B35 LP21 B4 D4 IO B35 LN21 B5 D5	JTAG TDO	F6	Bank 0	TDO 8	JTAG Test Data Output / 3.3V IO Standard
BANK 35 Select IO	IO_L21N_T3_DQS_AD14N_35	Bank 35	N16	IO_B35_LN21_B5 D5	JTAG_TMS	36	Bank 0	TMS_0	JTAG Test Mode Select / 3.3V IO Standard
				GND 86 D6					[
BANK 35 Select IO	IO L12P T1 MRCC 35	Bank 35	K17	TO 925 1912 97 D7	IO B35 LP6	F16	Bank 35	IO L6P T0 35	BANK 35 Select IO
BANK 35 Select IO	IO_L12N_T1_MRCC_35			IO_B35_LN12_B8	IO B35 LN6	F17	Bank 35	IO_L6N_T0_VREF_35	BANK 35 Select IO
BANK 35 Select IO	IO L9P T1 DQS AD3P 35	Rank 35	L19	IO B35 LP9 B9 D9		F18	Rank 35	IO LSP TO ADSP 35	BANK 35 Select IO
BANK 35 Select ID	IO L9N T1 DQS ADSN 35	Rank 20	L20	IO 835 LN9 B10 D10	TO 835 INS	F19	Rank 35	IO LSN TO ADSN 3	BANK 35 Select IO
	_0_CJM_12_0QJ_AD3N_33	_win 33		GND B11 D11		-4-			
BANK 35 Select IO	TO 117P T2 ADSP 35		120	Creo Diri UII	unu			L	BANK 35 Select IO
BANK 35 Select IO BANK 35 Select IO				IO 835 LP17 B12 D12	10 B35 LP4	D20	Dank 35		BANK 35 Select IO BANK 35 Select IO
	IO_L17N_T2_AD5N_35			IO_B35_LN17 B13 D13	10_835_LN4	D28	pank 35	IO_L4N_T0_35	
BANK 35 Select IO	IO L14P T2 AD4P SRCC 35	Bank 35	318	IO B35 LP14 B14 D14	10 835 LP1	C20	Bank 35	IO L1P TO ADOP 35	BANK 35 Select IO
BANK 35 Select IO	IO_L14N_T2_AD4N_SRCC_35	Bank 35	H18	IO_B35_LN14 B15 D15		B20	Bank 35	IO_L1N_T0_AD0N_35	BANK 35 Select IO
				GND B16 D16					
BANK 35 Select IO	IO L22P T3 AD7P 35	Bank 35	L14	IO B35 LP22 B17 D17	IO B35 LP3	E17	Bank 35		BANK 35 Select IO
BANK 35 Select IO	IO_L22N_T3_AD7N_35			IO B35 LN22 B18 D18	IO B35 LN3	D18	Bank 35	IO_L3N_T0_DQS_AD1N_35	BANK 35 Select IO
BANK 35 Select IO	IO L24P T3 AD15P 35	Bank 35	K16	IO B35 LP24 B19 D19	IO B35 LP2	B19	Bank 35	IO L2P TO ADSP 35	BANK 35 Select IO
BANK 35 Select IO	IO L24N T3 AD15N 35			IO B35 LN24 B28 D28	IO B35 LN2	A20	Bank 35	IO L2N TO ADBN 35	BANK 35 Select IO
				GND B21 D21		_			1
BANK 35 Select IO	IO L10P T1 AD11P 35	Dank 25	K19	IO B35 LP10 B22 D22	TO 825 1 828	V14	Dank 25	TO 1200 T3 ADED 35	BANK 35 Select IO
BANK 35 Select IO	IO LIGH TI ADIIN 35	Dank 35	710	IO 835 LP16 822 D22	TO 925 1 N20	214	Dank 25		BANK 35 Select IO
BANK 35 Select IO BANK 35 Select IO	IO L18P T2 AD13P 35			10 B35 LN18 B23 D23 10 B35 LP18 B24 D24	TO 035 1011				BANK 35 Select IO BANK 35 Select IO
BANK 35 Select IO BANK 35 Select IO	IO L18P T2 AD13P 35 IO L18N T2 AD13N 35	pank 35	G19 G20	IO B35 LP18 B24 D24 IO B35 LN18 B25 D25	10 B35 LP11	L16	Dank 35	IO L11P T1 SRCC 35 IO L11N T1 SRCC 35	BANK 35 Select IO BANK 35 Select IO
DHMW 23 SETECT TO	10_L18N_12_AD13N_35	pank 35	628			L1/	Delfix 35	TO_LIIN_II_SKUL_55	DWW 22 26TECT TO
				GND B26 D26	GND				
BANK 35 Select IO	IO L15P T2 DQS AD12P 35	Bank 35	F19	IO B35 LP15 B27 D27	IO B34 LP1	T11	Bank 34	IO L1P T0 34	BANK 34 Select IO
BANK 35 Select IO	IO_L15N_T2_DQS_AD12N_35			IO B35 LN15 B28 D28	IO B34 LN1			IO_L1N_T0_34	BANK 34 Select IO
BANK 35 Select IO	IO L13P T2 MRCC 35			IO B35 LP13 B29 D29				IO L2P TO 34	BANK 34 Select IO
BANK 35 Select ID	IO L13N T2 MRCC 35			IO B35 LN13 B30 D30					BANK 34 Select IO
				GND B31 D31	GND				
BANK 35 Select IO	IO L16P T2 35	Bank 35	G17	IO B35 LP16 B32 D32	TO R34 I P4	V12	Rank 34	IO L4P T0 34	BANK 34 Select IO
BANK 35 Select IO	IO L16N T2 35	Rank 25	G18	IO B35 LN16 B33 D33	IO B34 LN4	W13	Bank 34	IO L4N TO 34	BANK 34 Select IO
BANK 35 Select IO	IO L19P T3 35			IO B35 LN10 B35 D33	TO 034 103				BANK 34 Select IO
									BANK 34 Select IU RANK 34 Select IO
BANK 35 Select IO	IO L19N T3 VREF 35			IO B35 LN19 B35 D35				IO L3N TO DQS 34	

Note 1:Boot mode setting



