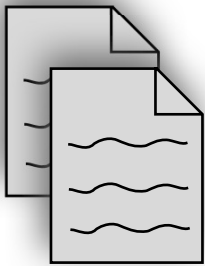
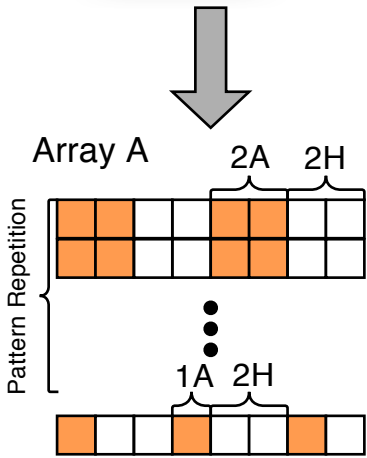


Step 1:



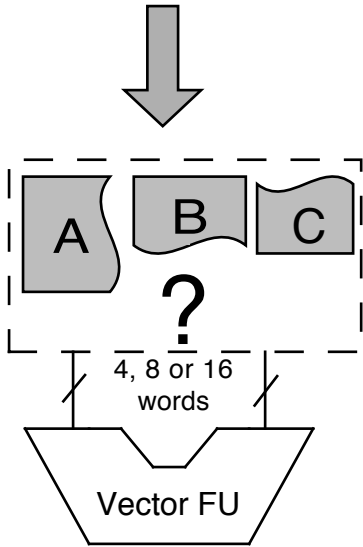
Analysis of the application code with irregular memory accesses

Step 2:



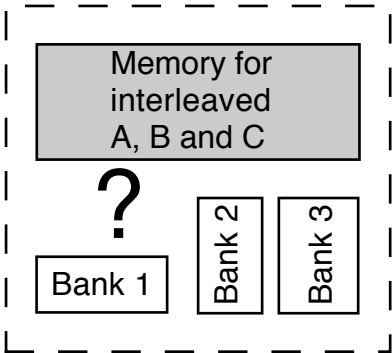
Extraction of the memory access pattern for the application data

Step 3:



Exploration of interleaving options for varying SIMD architectures

Step 4:



Exploration of data-to-memory mapping options for a clustered SPM architecture