

Technology scaling impact on the interconnect of clustered scratchpad memory architectures

Iason Filippopoulos Francky Catthoor
Per Gunnar Kjeldsberg

July 31, 2015

Abstract

This work

- 1 Introduction**
- 2 Related Work**
- 3 Technology Scaling**
 - 3.1 Memory Banks**
 - 3.2 Interconnection**
- 4 Simulation**
- 5 Model Construction and Projection Results**
- 6 Conclusion**