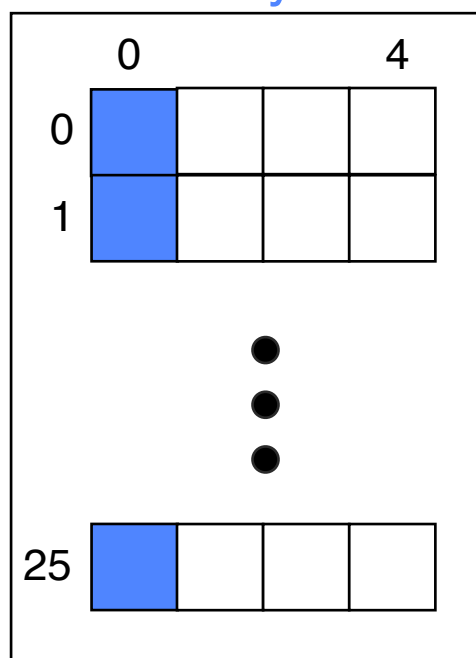
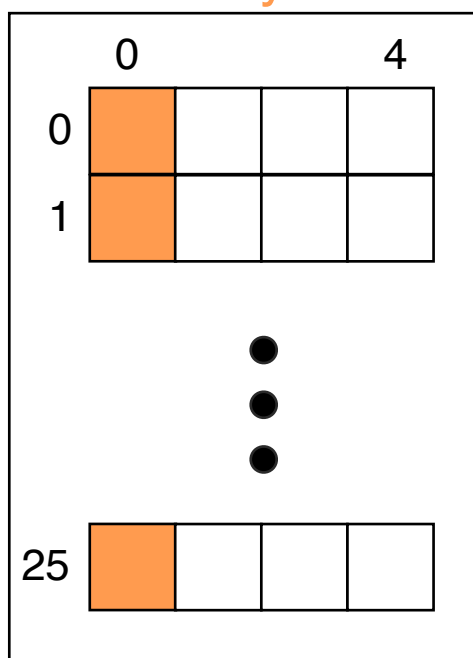


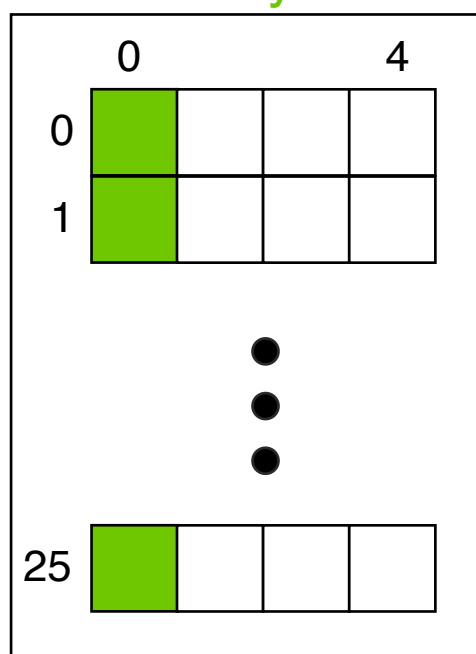
Array A



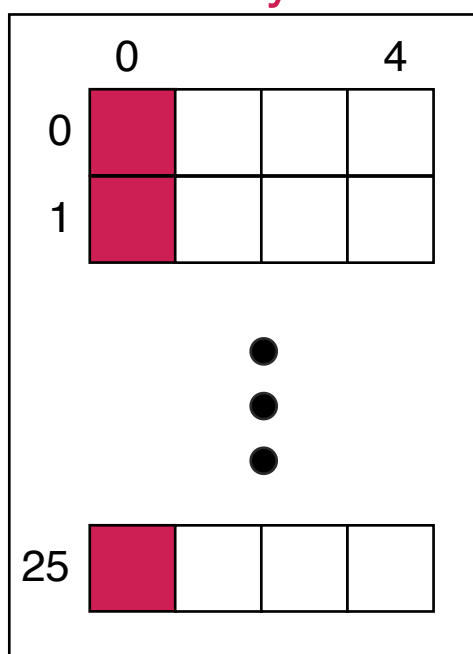
Array B



Array C

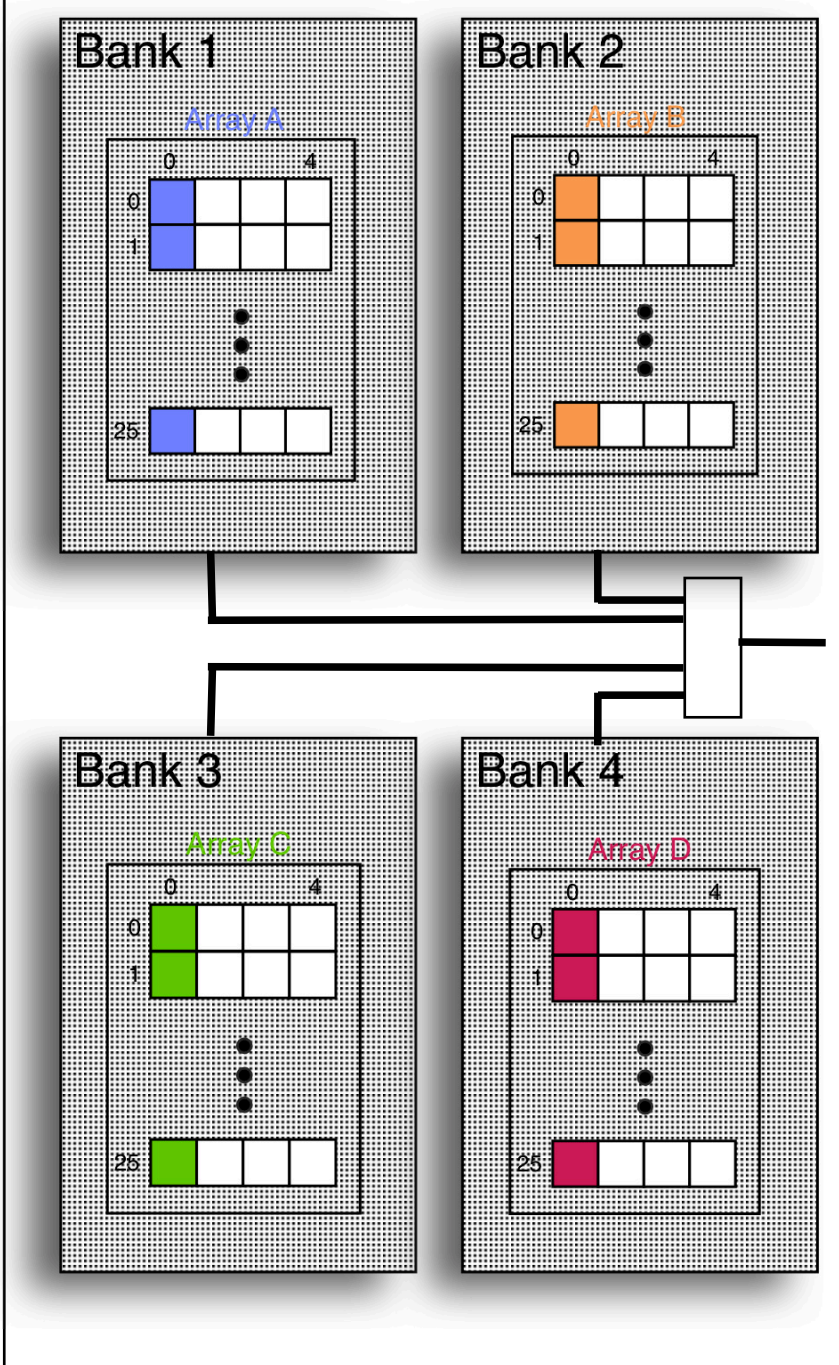


Array D



(a)

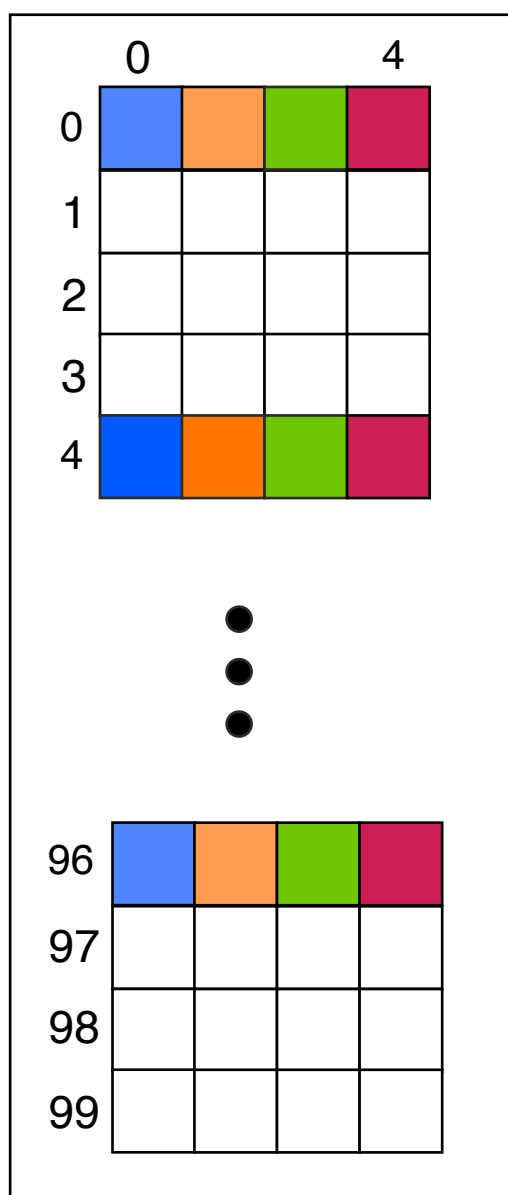
Clustered Memory Architecture



(c)

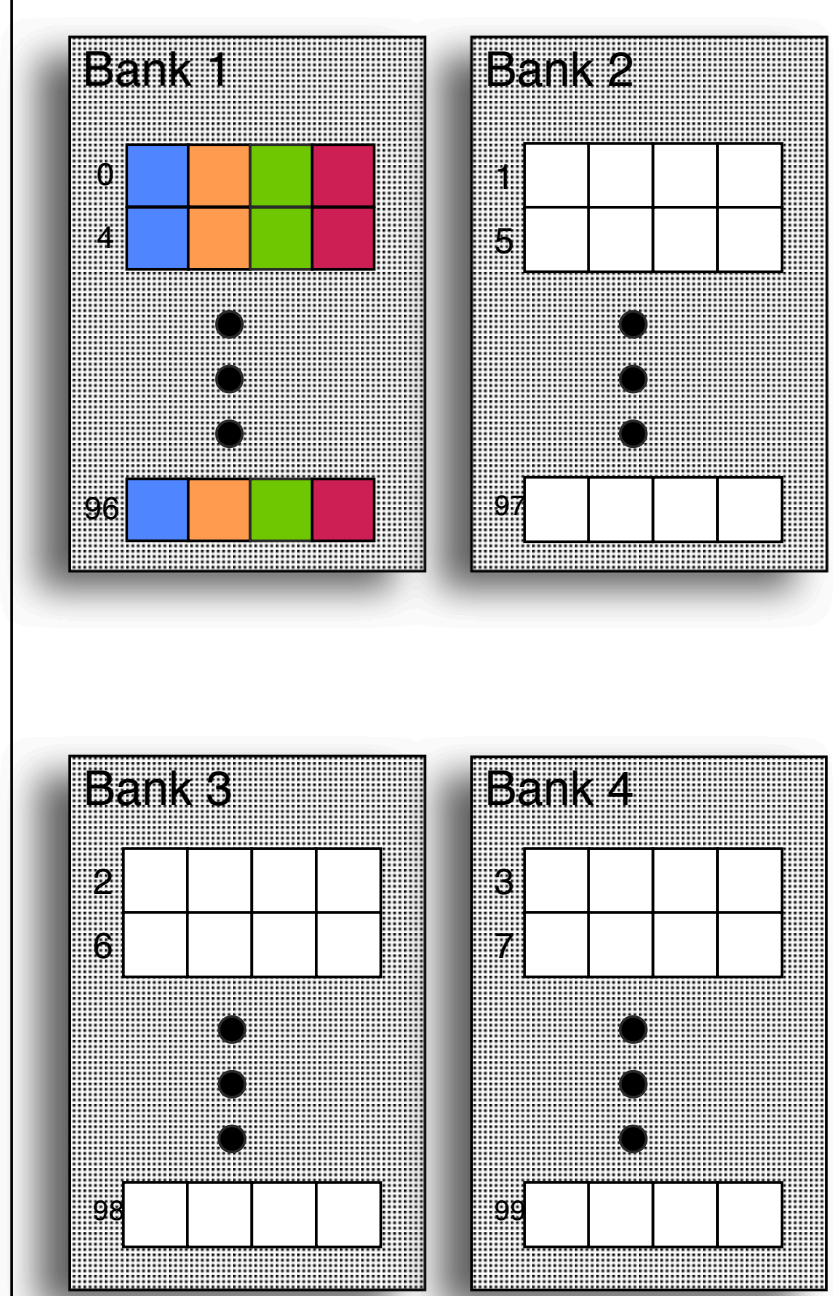
Interleaved

A/B/C/D



(b)

Clustered Memory Architecture



(d)