Veri İletişimi ve Bilgisayar Ağları BLM3051



Dr. Öğr. Üyesi Furkan ÇAKMAK

Ders Bilgilendirme Formu - Haftalık Konular

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

Week #	Date	Subjects
1	20,02,2025	Veri İletişimine Giriş, Mimari Modeller
2	27.02.2025	OSI Referans Modeli, Katmanları, Fonksiyonları
3	06.03.2025	Fiziksel Katman, Sinyalleşme
4	13.03.2025	Paralel ve Seri İletişim, Haberleşme Ortamları ve Teknik Özellikleri, Multiplexing (TDM, FDM)
	20.03.2025	Hata Tespiti ve Düzeltme Yöntemleri
6	27.03.2025	Veri Bağı Kontrol Teknikleri ve Akış Kontrolü
7	03.04.2025	Senkron ve Asenkron Veri Bağı Protokolleri (BSC, HDLC)
	10.04.2025	Ara Sınav
9	17.04.2025	LAN Teknolojileri, IEEE 802.3, IEEE 802.4, 802.5, 802.11
10	24.04.2025	Geniş Alan Ağlarında Kullanılan Teknolojiler (X.25, ISDN, FR, ATM, xDSL.)
11	01.05.2025	Emek ve Dayanışma Günü
12	08.05.2025	Ağ Katmanı, Anahtarlama, Bağlantılı ve Bağlantısız Servisler, Statik ve Dinamik Routing
13	15.05.2025	Ağ Katmanında Sıkışıklık, Sebepleri ve Çözümleri, IP (Internetworking Protocol)
14	22.05.2025	ICMP, BOOTP, DHCP, Taşıma Katmanı - UDP (User Datagram Protocol), TCP (Transmisson Control Protocol)
15	29.05.2025	Öğrenci Proje Sunumları

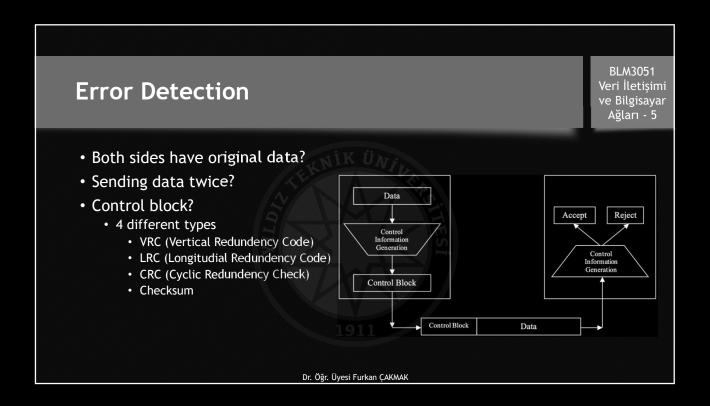
Error Detection and Correction Techniques

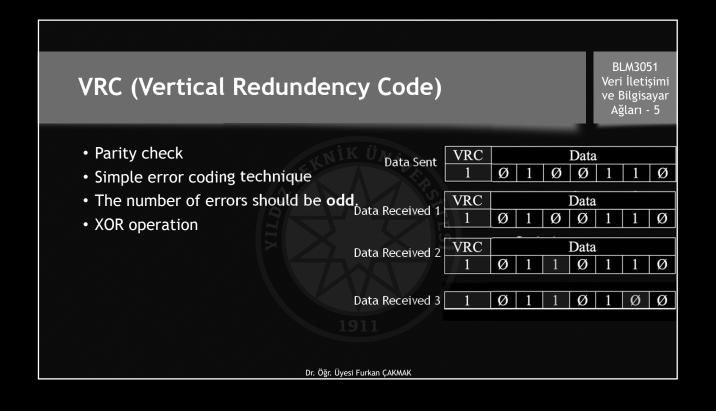
BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

- Data Link Layer (in OSI model)
- Error reasons
 - Attenuation
 - Delay Distortion
 - Video + Voice
 - Problem in time sensitive conditions
 - · Noise in the communication environment
 - Thermal noise
 - Random electron motion
 - Intermodulation noise
 - CrossTalk
 - Impulse Noise

Dr. Öğr. Üyesi Furkan ÇAKMAK

BLM3051 Veri İletişimi **Error Types** ve Bilgisayar Ağları - 5 Data Sent Data Received • Single bit error Ø ØØ ØØ · Multi bit error Ø Ø Ø Ø Ø Error bursts 1 Ø Ø Ø Ø Dr. Öğr. Üyesi Furkan ÇAKMAK





LRC (Longitudial Redundency Code)

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

• LRC is 2D-VRC

	Byte 1		Byte 2		Byte 3	Byte 4	LRC
	1	Chry	Ø	12	1	1	1
	Ø		Ø	(6)	1	1	Ø
	Ø	N I	1	118	Ø	1	Ø
	1	11	1 /		Ø	1	1
	3/1		Ø		1	Ø	Ø
	Ø		1		1	Ø	Ø
	1		Ø		Ø	Ø	1
VRC	Ø		1		Ø	Ø	1
	1Ø1 Ø	0011	▼	1100	11Ø Ø		10010011

11001100

1Ø11ØØ*1*Ø

Ø/11Ø1/1

Dr. Öğr. Üyesi Furkan ÇAKMAK

CRC (Cyclic Redundency Check)

10011010

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

10010011

- The data to be sent is divided into a predetermined prime polynomial.
- The remainder value is added to the data to be sent as an error control code.
- The remainder zero in receiver side means that error-free transmission.
- Common polynomials used for CRC: 13-bits, 17-bits, 33-bits
 - The number of undetectable errors is almost zero
- Commonly used polynomials in CRC technique:

 - CRC-ITU $x^{16}+x^{12}+x^{5}+1$
 - CRC-32 $x^{32}+x^{26}+x^{23}+x^{22}+x^{16}+x^{12}+x^{11}+x^{10}+x^{8}+x^{7}+x^{5}+x^{4}+x^{2}+x+1$

CRC (Cyclic Redundency Check) - Example

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

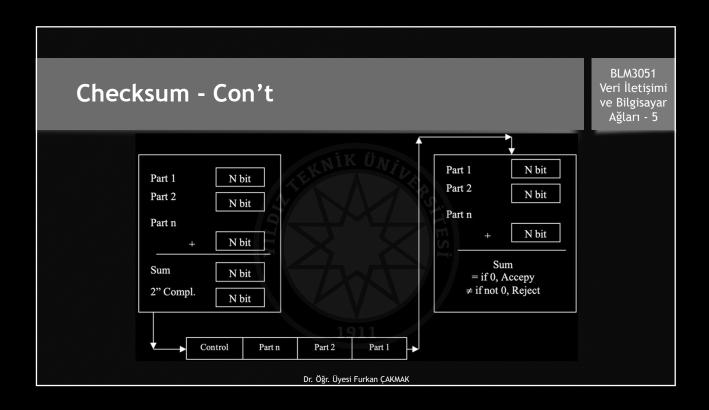
Example: Data Sent: 100100, polynom: $x^3 + x^2 + 1$, CRC = ?

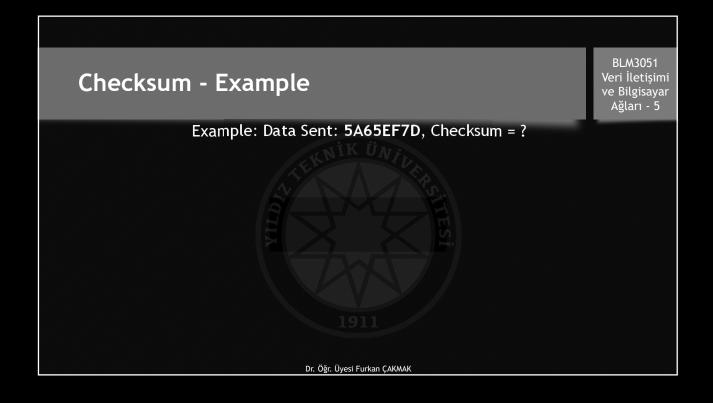
Dr. Öğr. Üyesi Furkan ÇAKMAK

Checksum

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

- The sender divides the data into N-bits parts (usually 16 bits are used).
- The parts are collected using the first complementary arithmetic.
 - In this way, a total value of only N bits is obtained.
- Calculate two's complement using summed value
 - The calculated value is added to the end of the information to be sent.
- The checksum detects all of the odd errors and most of the even numbers.
 - However, if one or more bits in a part are 0 when they are 1, but there is a 0 when 1 in another part, the error will not be understood because there will be no difference in this column sum.





Error Correction

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

- 2 methods
 - Send data again
 - · If one bit error
 - Hamming Code / Distance

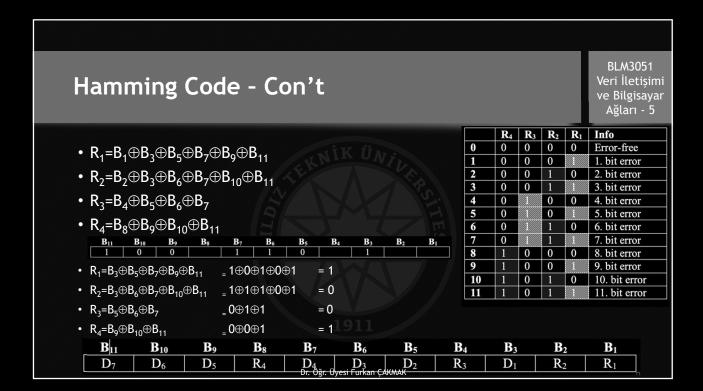
Dr. Öğr. Üyesi Furkan ÇAKMAK

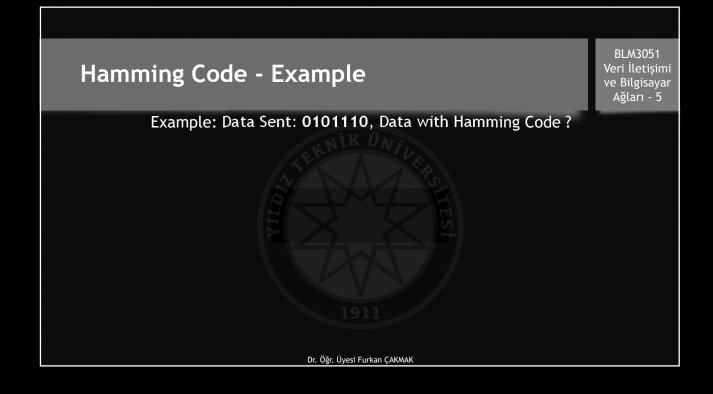
Hamming Code

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

- If we sent m bit data, the error occurs in 1,2,...,m bit
- Adding error-free state, the data length will be m+1
- Control block length must be $log_2(m+1) \le r$
- m + r bit must be sent error-free
- So, control block length must be $log_2(m+r+1) \le r$
- (1, 2, 4, 8, 16. bits)

\mathbf{B}_{11}	\mathbf{B}_{10}	\mathbf{B}_9	$\mathbf{B_8}$	\mathbf{B}_7	\mathbf{B}_{6}	\mathbf{B}_{5}	\mathbf{B}_4	\mathbf{B}_3	\mathbf{B}_2	$\mathbf{B_1}$
D_7	D_6	D_5	R ₄	D_4	D_3	D_2	R_3	D_1	R_2	R_1





Thank you for listening...

BLM3051 Veri İletişimi ve Bilgisayar Ağları - 5

