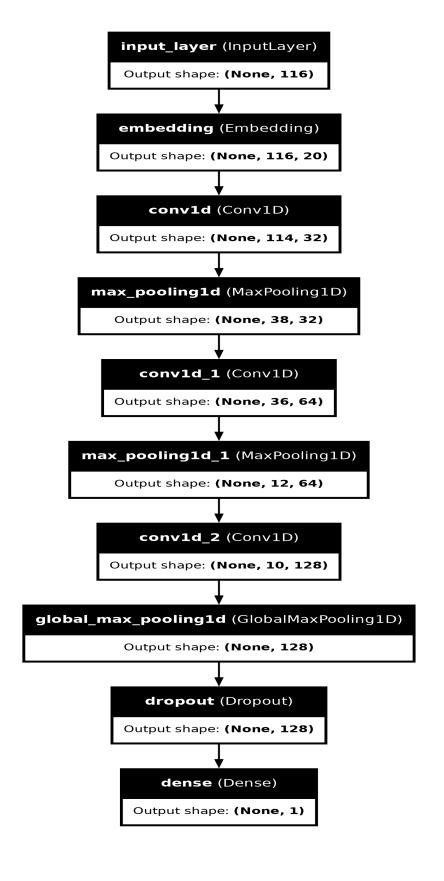
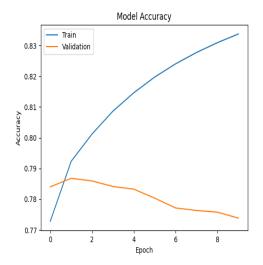
Model 1:

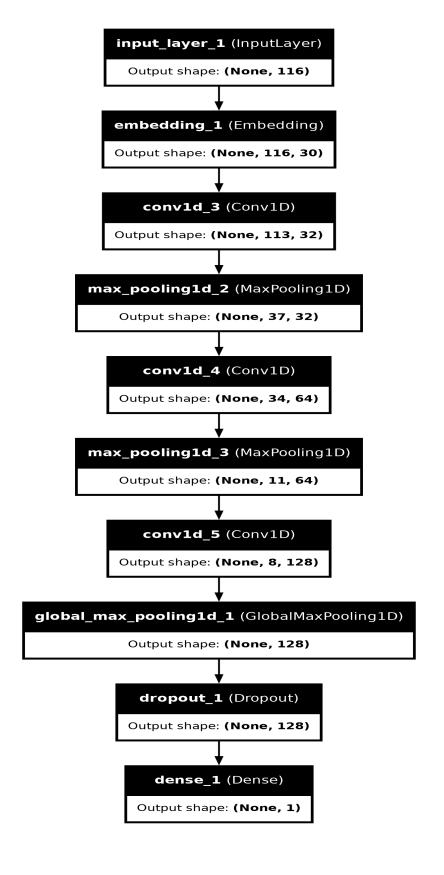
Parameters: Embedding dimension = 20, Window = 3, 1- Conv1D no. filters = 32, 2- Conv1D no. filters = 64 3- Conv1D no. filters = 128

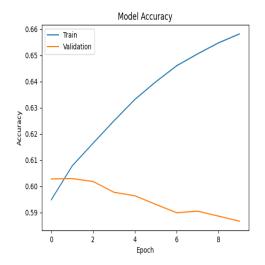




Model 2:

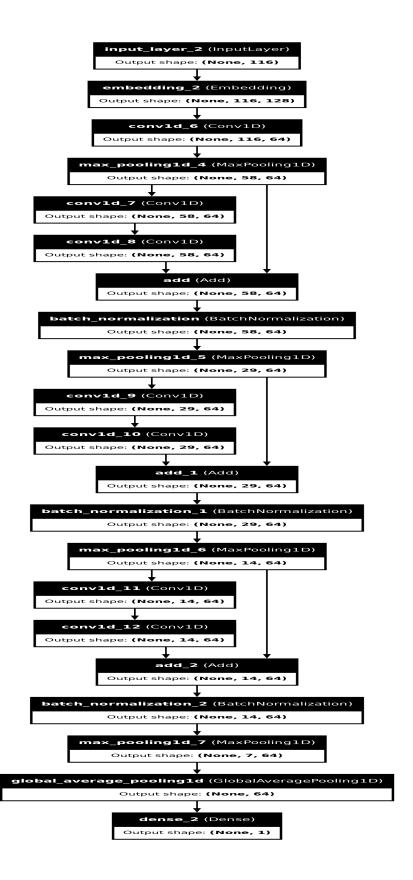
Parameters: Embedding dimension = 30, Window = 4, 1- Conv1D no. filters = 32, 2- Conv1D no. filters = 64 3- Conv1D no. filters = 128

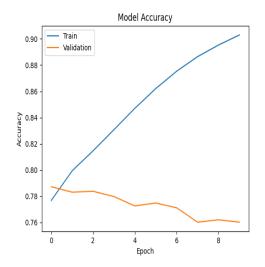




Model 3:

Architecture: {'filters': 64, 'kernel_size': 3, 'num_blocks': 3}

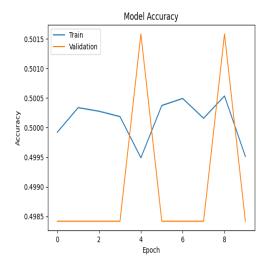




Model 4:

Architecture: VGG for text

```
input_layer_3 (InputLayer)
     Output shape: (None, 116)
    embedding_3 (Embedding)
   Output shape: (None, 116, 128)
      convld_13 (ConvlD)
   Output shape: (None, 116, 64)
       convld_14 (ConvlD)
   Output shape: (None, 116, 64)
max_pooling1d_8 (MaxPooling1D)
    Output shape: (None, 58, 64)
      convld_15 (ConvlD)
   Output shape: (None, 58, 128)
      convld_16 (Conv1D)
   Output shape: (None, 58, 128)
max_pooling1d_9 (MaxPooling1D)
    Output shape: (None, 29, 128)
      convld_17 (Conv1D)
   Output shape: (None, 29, 256)
      convld_18 (Conv1D)
   Output shape: (None, 29, 256)
      conv1d_19 (Conv1D)
   Output shape: (None, 29, 256)
max_pooling1d_10 (MaxPooling1D)
    Output shape: (None, 14, 256)
       conv1d_20 (Conv1D)
   Output shape: (None, 14, 512)
      convld_21 (ConvlD)
    Output shape: (None, 14, 512)
      conv1d_22 (Conv1D)
   Output shape: (None, 14, 512)
max_pooling1d_11 (MaxPooling1D)
    Output shape: (None, 7, 512)
    flatten (Flatten)
Output shape: (None, 3584)
        dense_3 (Dense)
    Output shape: (None, 4096)
        dense_4 (Dense)
    Output shape: (None, 4096)
         dense_5 (Dense)
      Output shape: (None, 1)
```



Conclusion:

According to the accuracies of each architecture, it appears that the first one one outperforms the others.