



**Multiplexor
KoSoAoI**

0: K (11 bits)
1: STACK
2: ADDER
3: INTERRUPT

Data from ALU
 Source Data
 Instruction's Address
 K (8 bits Data)
 Register's Address (Data)
 K (11 bits Address)
 Control Signals

**Multiplexor
WoKoSoFoLoR**

0: W Register
1: K (8 bits)
2: SWAP
3: FILE
4: ROTATE LEFT
5: ROTATE RIGHT