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RFACE					
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FREEDOM K64F

Revisions & Change Log				
Rev	Description	Date	Approved	
X1	Initial Draft	Oct 18	Chung	
X2	Feedbacks Implemented	Oct 22	Chung	
хз	Feedbacks from Chung Implemented	Oct 23	Chung	
X4	Components changed as per the feedbacks	Oct 24	Chung	
X5	RF and Bluetooth headers added	Oct 28	Chung	
X6	Arduino connections added	Nov 1	Chung	
Х7	Net review and fix	Nov 3	Chung	
A	Release to production, prototype build	Nov 8	Chung	
A1	openSDA reset capacitor fix	Nov 22	del Rey	
В	Release to production, production build	Jan 20	Chung	
С	Release to production, production build	Feb 06	Chung	
D	Fixing I2C swap to headers	Feb 26	Chung	
D1	Enhancing USB PWR input filter for robustness	Mar 18	Chung	
Е	Swapping J2.2 connection to PTC12 Fix Eth Link status when 2 FRDMs are connecter to each other	Jul 11	Chung	
E1	Updating J13 Ethernet connector part	Sep 03	Chung	
E2	Depopulating J14	Sep 10	Chung	
E3	Depopulating C55, populating R75	Oct 14	Chung	





ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt. 1 uF, some are 10% or 20%

All polarized capacitors are tantalum

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



























