

**Main FPGA**

U1A EF2M45LG48

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JTAGEN 5

TMS 11  
TCK 12  
TDI 13  
TDO 14

ADC\_0 3  
LVDSRX\_N 6  
LVDSRX\_P 7  
LVDSRX\_N 9  
LVDSRX\_P 10

U1B EF2M45LG48

UD+ 15  
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U1C EF2M45LG48

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U1D EF2M45LG48

GPI04/I0\_T1P\_3  
GPI03/I0\_T1N\_3  
GPI013/I0\_T2P\_3  
GPI012/I0\_T2N\_3  
GPI011/I0\_T3P\_3  
GPI010/I0\_T3N\_3

GPI06/DPCLKI0\_8/ADC1\_CH0/I0\_T\_3  
GPI07/ADC1\_CH1/I0\_T\_3  
GPI08/USRCLK/ADC1\_CH2/I0\_T4N\_3  
GPI09/DPCLKI0\_7/ADC1\_CH3/I0\_T4P\_3  
GPI00/ADC1\_CH4/I0\_T5N\_3  
GPI01/ADC1\_VREF/I0\_T5P\_3  
GPI05/ADC0\_VREF/I0\_T\_3

**LEDs**

LED\_R  
LED\_G  
LED\_B

**USB**

J1 USB\_B

**POWER 3.3V**

U2 AP1117-33

**Program FPGA**

J2 USB\_Blaster

J3 JTAGEN

SW1 RESET

**Connectors**

J7 SWD

J4 PMOD\_J2C

J8 ADC\_LVDS

**QSPI Flash /PSRAM**

U3 AT25SF081-SSHD-X

**U1E EF2M45LG48**

VCCAUX  
VCCCAUX  
VCCCAUX  
VCC\_VDDA  
VBAT

XTAL\_I  
XTAL\_O

**ICenowy**

Sheet: /  
File: elf2.sch  
**Title: ELF2 board**  
Size: A4 Date: 2019-03-10  
KiCad E.D.A. kicad 5.0.2  
**Rev: v1**  
Id: 1/1