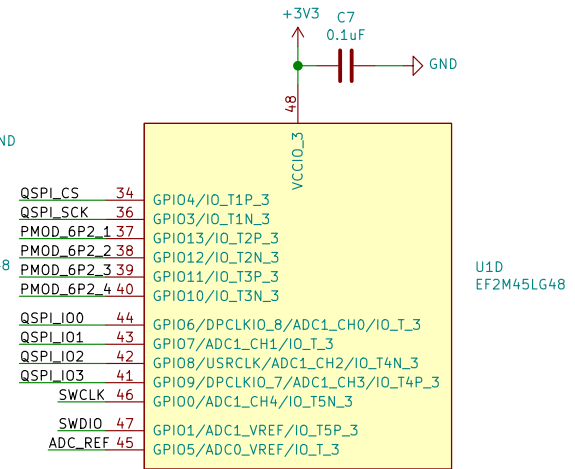
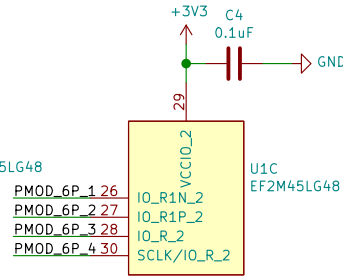
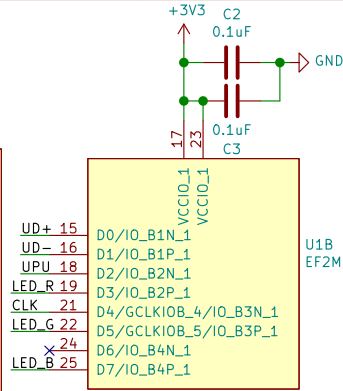
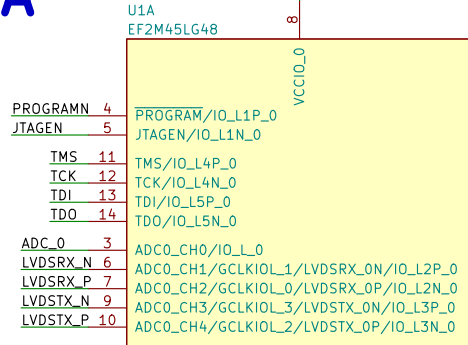
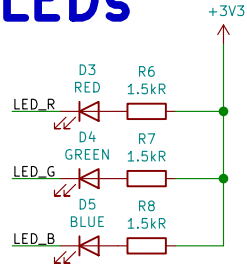


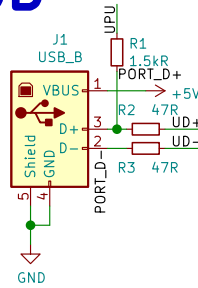
Main FPGA



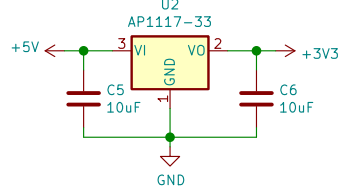
LEDs



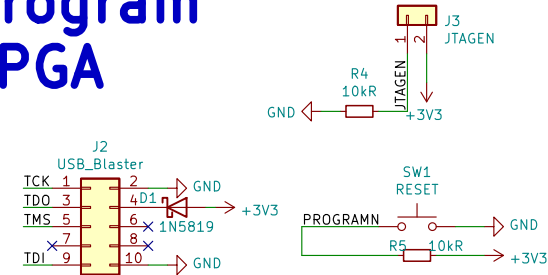
USB



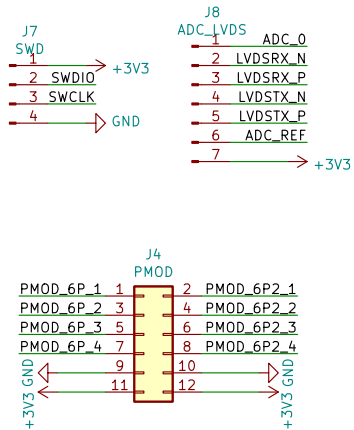
POWER 3.3V



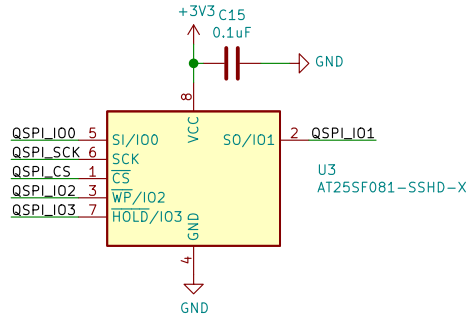
Program FPGA



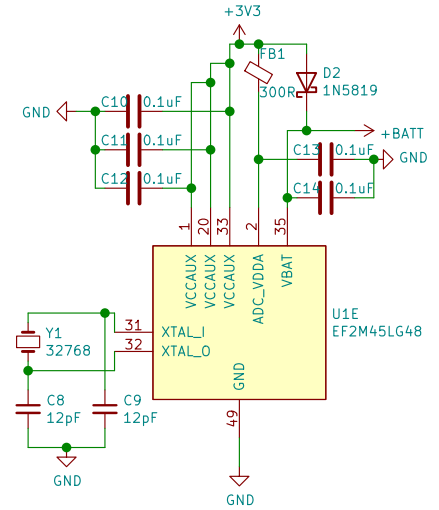
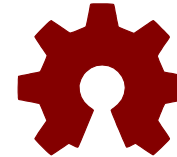
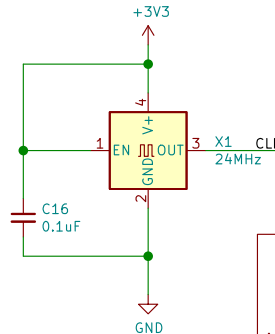
Connectors



QSPI Flash /PSRAM



Clock



Icenowy

Sheet: /
File: elf2.sch

Title: ELF2 board

Size: A4 Date: 2019-03-10
KiCad E.D.A. eeschema 5.1.12

Rev: v1
Id: 1/1