APPLICATION INFORMATION

Port P1 Pin Schematic: P1.0 to P1.3, Input/Output With Schmitt Trigger – MSP430G2x21
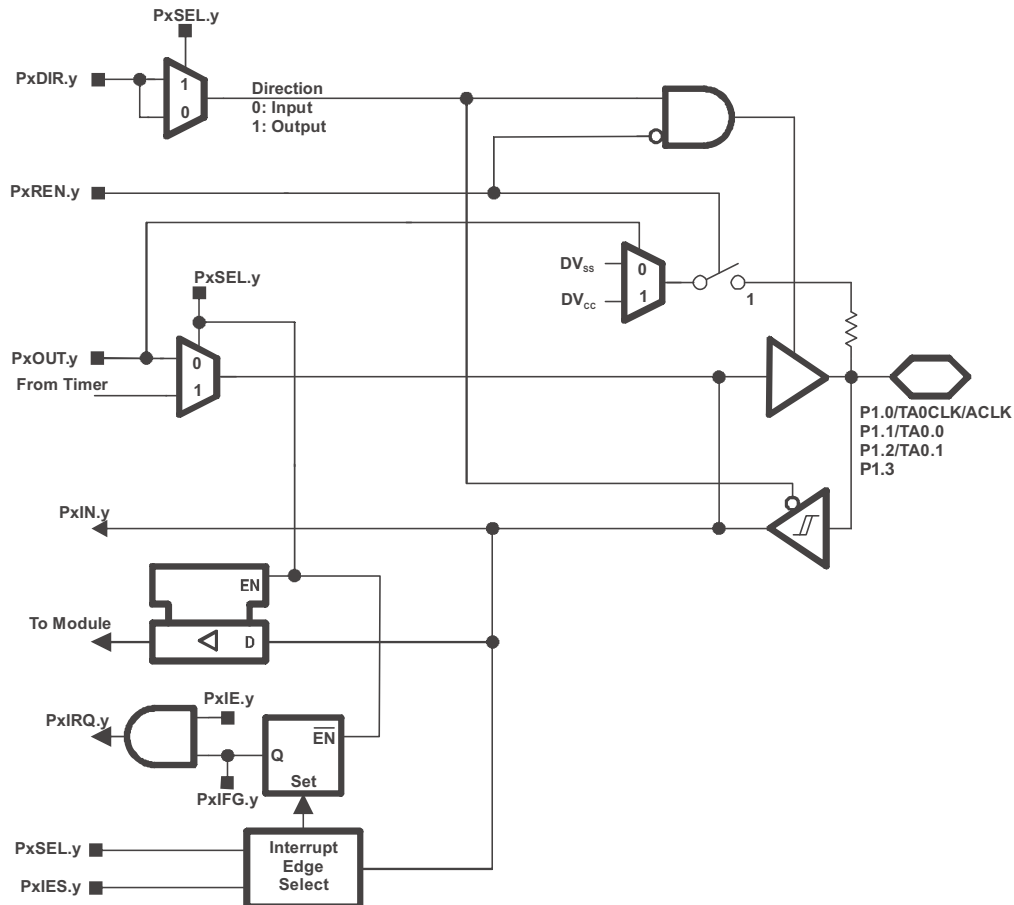

Table 13. Port P1 (P1.0 to P1.3) Pin Functions – MSP430G2x21

PIN NAME (P1.x)	x	FUNCTION	CONTROL BITS/SIGNALS	
			P1DIR.x	P1SEL.x
P1.0/		P1.0 (I/O)	I: 0; O: 1	0
TA0CLK/	0	TA0CLK	0	1
ACLK		ACLK	1	1
P1.1/		P1.1 (I/O)	I: 0; O: 1	0
TA0.0	1	TA0.CCI0A	1	1
		TA0.0	0	1
P1.2/		P1.2 (I/O)	I: 0; O: 1	0
TA0.1	2	TA0.CCI1A	1	1
		TA0.1	0	1
P1.3	3	P1.3 (I/O)	I: 0; O: 1	0