

Iddo Tzameret

Introduction to Concrete Complexity

Lecture Notes

Imperial College London
Dept. of Computing

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0.1 Summary of Module

We are interested in approaches to the fundamental hardness questions in computational complexity.

Computational complexity: the study of which problems can be efficiently computed and which cannot.

Efficiency: we understand efficiency as Polynomial Time computability. A Boolean function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ is said to be efficiently computable if there is a polynomial-time Turing Machine M that, on input x in $\{0, 1\}^n$ outputs $f(x)$ and runs in time n^c for some constant c . Note that n^c is a polynomial in the input length n (the exponent c does not depend on the input length n).

The arguably main question of the theory of computation, the one that subsumes in some sense many problems in other parts of computing, once is the P vs NP question: Can we separate P from NP, namely, is there a language in NP that is not in P? In other words, can we prove that SAT (Boolean satisfiability problem) cannot be solved in polynomial time? And yet again, roughly, can problems whose solutions once given can be verified efficiently, can be solved efficiently?

We are interested in *concrete* approaches, namely, considering a simple, usually combinatorial-looking, model of computation, such as a Boolean circuit, and establishing lower bounds against the size of circuits required to prove certain specific functions that are given to us concretely (usually, these functions also possess some straightforward combinatorial properties; e.g., they represented specific graph problems). In this sense, the question is concrete because the result is unconditional (namely, it does not depend on unproved assumptions, such as $P \neq NP$), and the model itself is concrete: it is a (primarily combinatorial) object of which its size we lower bound in precise terms (e.g., circuit C computing function $f(x)$ must have size $2^{|x|}$, where $|x|$ is the bit-size of the input x).

Three main concrete approaches to the fundamental hardness questions are the following:

1. Circuit Complexity
2. Proof Complexity
3. Algebraic Complexity

We shall see a bit from each, mainly circuit complexity and some basic proof complexity while commenting briefly on algebraic complexity.

Other approaches to the fundamental hardness questions are usually more intrinsic to complexity theory. In that respect, the whole of computational complexity theory could be viewed as “approaching” the fundamental hardness questions through complexity class, reductions, concrete lower bounds and the relation between these notions and results. One intriguing approach that makes this attempt in particular is the “Meta Complexity” approach. We are not going to touch on this in this course.

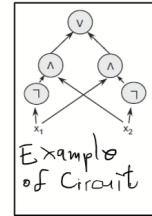
Chapter 1

Introduction to Circuit Complexity

1.1 Basic Circuit Complexity

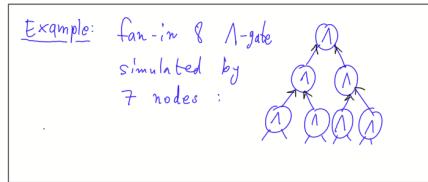
Definition 1.1 (Boolean Circuit) Let $n \in \mathbb{N}$ and x_1, \dots, x_n be n variables. A Boolean Circuit C with n inputs is a directed acyclic graph. It contains n nodes with no incoming edges, called the *input nodes* and a single node with no outgoing edges, called the *output node*. All other nodes are *internal nodes* or *gates*, and are labelled by the logical gates \vee , \wedge , \neg (i.e., logical OR, AND, NOT, resp.). The \vee , \wedge nodes have fan-in (i.e., number of incoming nodes) 2, and \neg has fan-in 1. The *size* of C , denoted $|C|$, is the number of nodes in the underlying graph. C is called a *formula* if each node has at most one outgoing edge (i.e., the underlying graph is a tree),

Fig. 1.1 Example of a simple Boolean circuit.



Comment

Fan-in $d > 2$ can be simulated by a tree of $d - 1$ nodes:



? What is the function computed by the circuit below?

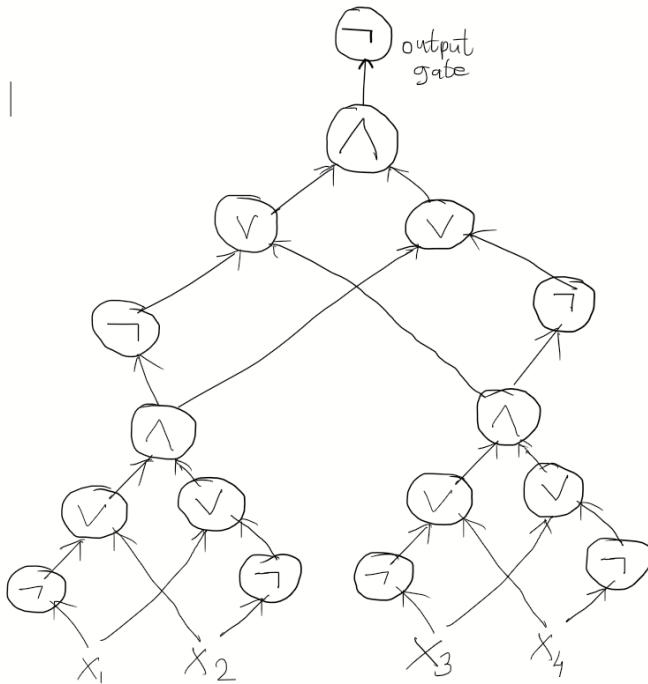


Fig. 1.2 Question

Answer

PARITY on 4 input bits. It outputs 1 if the number of 1's in the input is odd. I.e., it computes the XOR of x_1, \dots, x_4 . In other words, it computes the function $x_1 + x_2 + x_3 + x_4 \bmod 2$. (Understand why that is. Hint: recall that $\neg x_1 \vee x_2$ is

logically equivalent to $x_1 \rightarrow x_2$. And notice that this structure repeats throughout the circuit.)

1.2 Circuit families, language recognition, and function computation

Let \mathbb{N} denote the set of natural numbers starting from 1.

A *language* is a set of (finite) strings. Note that the language can contain infinite many strings, only that each string is finite. A *string* is an ordered sequence of symbols from a fixed constant size alphabet. We shall use mainly strings over the alphabet consisting of two symbols $\{0, 1\}$. Hence, a language is simply a set $L \subseteq \{0, 1\}^*$ (recall, that $\{0, 1\}^*$ is the set of all finite 0-1 strings, including the empty string).

Definition 1.2 Let $T : \mathbb{N} \rightarrow \mathbb{N}$ be a function. A *$T(n)$ -sized circuit family* is a sequence $\{C_n\}_{n \in \mathbb{N}}$ of Boolean circuits, where C_n has n input-gates (i.e., n inputs-bits) and a single output-bit such that $|C_n| \leq T(n), \forall n \in \mathbb{N}$.¹

A language $L \subseteq \{0, 1\}^*$ is said to be *in* $\text{SIZE}(T(n))$, namely,

$$L \in \text{SIZE}(T(n)),$$

if there is a $T(n)$ -size circuit family $\{C_n\}_{n \in \mathbb{N}}$ s.t. (such that)

$$\forall n \in \mathbb{N} \ \forall x \in \{0, 1\}^n : x \in L \Leftrightarrow C_n(x) = 1.$$

In this case, we say that the family $\{C_n\}_{n \in \mathbb{N}}$ **decides** the language L .

Similarly, for a *function* $f : \{0, 1\}^* \rightarrow \{0, 1\}$, $f \in \text{SIZE}(T(n))$ if there exists $T(n)$ -size circuit family $\{C_n\}_{n \in \mathbb{N}}$ such that $\forall n \in \mathbb{N} \ \forall x \in \{0, 1\}^n, f(x) = 1 \Leftrightarrow C_n(x) = 1$. In this case, we say that the family $\{C_n\}_{n \in \mathbb{N}}$ **computes** the function f .

Slice of function. Let $f : \{0, 1\}^* \rightarrow \{0, 1\}$ be a Boolean function. We can consider the *slice of size n* of f to be the function $f_n : \{0, 1\}^n \rightarrow \{0, 1\}$, which is f restricted to inputs of length precisely n .

Similarly, we can consider $\{f_n\}_{n \in \mathbb{N}}$ to be the *family* of Boolean functions $f_n : \{0, 1\}^n \rightarrow \{0, 1\}$, for all n at least 1 (so, this is a family of all slices of f). In other words, the family $\{f_n\}_{n \in \mathbb{N}}$ is the same as $f : \{0, 1\}^* \rightarrow \{0, 1\}$.

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1.3 Quick recap: growth functions, O -notation and run-times

Be sure to recall the following basic definitions of (though we are not going to use them concretely in this course).

- Time bounds for Turing Machines (TMs)
- The class P (aka PTIME)
- The class NP, verifiers, short certificates
- SAT is NP complete: Cook-Levin theorem

Time Complexity

We mainly consider the worst-case time for a problem to be solved by a given TM.

Definition 1.3 Let M be a TM that halts on every input. Let $f : \mathbb{N} \rightarrow \mathbb{N}$ be a function, s.t., $f(n)$ is the max number of steps it takes M to halt on inputs of length n . We say f is the running time of M . And that M runs in time $f(n)$.

O -Notation

We usually use n for the length of inputs

Recall:

1. $f(n) = O(g(n))$ if \exists constant c and $n_0 \in \mathbb{N}$ st. $\forall n \geq n_0 \quad f(n) \leq cg(n)$ (for two functions $f, g : \mathbb{N} \rightarrow \mathbb{R}^+$).
 2. $f(n) = o(g(n))$ if $\lim_{n \rightarrow \infty} \frac{f(n)}{g(n)} = 0$
-

Basic Growth Rates

1. When $f = O(\log n)$ the base b of $\log_b n$ is irrelevant.
 2. $2^{O(n)}$ means $\leq 2^{cn}$ for some $c, \text{const.}$
 3. *Polynomial growth rate* means $n^{O(1)} = 2^{O(\log n)} = n^c$, for some constant c , namely c is independent of n (while n grows, c stays fixed).
 4. *Exponential growth rate* (similarly, *exponential bound*) means for us 2^{n^δ} , for a real constant $\delta > 0$. (Some texts insist that “exponential growth” should only refer to 2^{cn} , for a constant $c > 0$. Note the difference between this and our definition!)
-

Definition 1.4 (P/poly; polynomial-size circuits) The class P/poly is the class of languages that are decidable by polynomial-size circuit families. That is, $\text{P/poly} = \bigcup_{c \in \mathbb{N}} \text{SIZE}(n^c)$.

Theorem 1.1 $P \subseteq P/\text{poly}$.

Proof Assignment/tutorial. [See for a sketch in Arora-Barak'10, Sec. 6.1.1, page 110; [1]]. \square

1.4 Our main focus: lower bounds

Lower bounds, hard functions. We say that we have a *super-polynomial lower bound against P/poly* , namely a super-polynomial lower bound against (polynomial-size) Boolean circuits, if the following occurs: there exists a Boolean function $f : \{0, 1\}^* \rightarrow \{0, 1\}$ such that no polynomial-size circuit family $\{C_n\}_n$ computes f . In other words, f is not in P/poly . If we show that $f \notin \text{SIZE}(T(n))$, we say that we have *proven a $T(n)$ -lower bound for f* (against Boolean circuits). We say that this f is *hard* for $\text{SIZE}(T(n))$.

Motivation for Circuit Complexity

- 1) Non-uniformity: maybe for every $l \in \mathbb{N}$ there's a small circuit for SAT that can solve it in quadratic time? (Karp-Lipton: PH doesn't collapse $\Rightarrow NP \subseteq P/\text{poly}$).
- 2) Mathematically *cleaner* than Turing Machines; so might have better hope for lower bounds.

1.4.1 Most functions are hard: Shannon lower bound

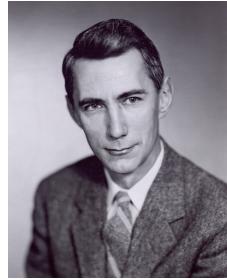


Fig. 1.3 C.E. Shannon. Source: By Unknown author - Tekniska Museet, CC BY 2.0, <https://commons.wikimedia.org/w/index.php?curid=144716444>

Open: $\text{NEXP} \stackrel{?}{\subseteq} P/\text{poly}$ (note that by the time hierarchy theorem $\text{EXP} \subsetneq P$; but in “ $\text{NEXP} \stackrel{?}{\subseteq} P/\text{poly}$ ” we consider NEXP which is *uniform*, and P/poly which is *non-uniform*).

Theorem 1.2 (Shannon lower bound) *For every $n > 1$ exists a function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ that cannot be computed by a circuit of size $\leq 2^n/10n$.*

Proof (by counting method.)

Claim We can encode a boolean circuit of size S with $9 \cdot S \log S$ bits. \square

Proof (Claim) Using adjacency List:

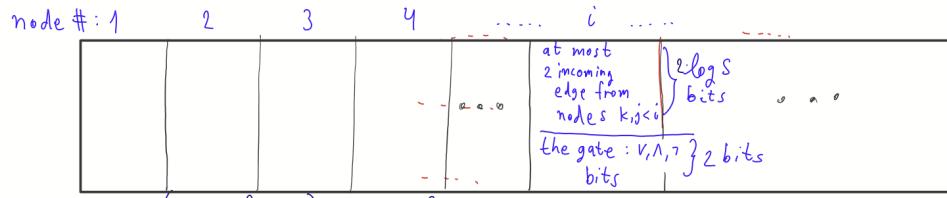


Fig. 1.4 Enter Caption

We have:

$$S \cdot (2 + 2 \log S) \leq 9S \log S.$$

(Actually, we have: $2S(\log S + 1) = 2S(2 \log S) \leq 4S \log S$). \square

We now show that the number of circuits of size $\leq 2^n/10n$ is smaller than that of possible Boolean functions w/ n inputs. Thus, by the Pigeonhole Principle, there exists a Boolean function that cannot be computed by a circuit of size $\leq 2^n/10n$.

The number of circuits of size S is bounded from above by the number of distinct codes of circuits of size S , which is

$$\begin{aligned} &\leq 2^{9 \cdot S \log S} \\ &= 2^{(9 \cdot 2^{n/10n} \cdot \log(2^n/10n))} = 2^{(9 \cdot 2^n/10n \cdot (n - \log 10n))} \\ &\leq 2^{\frac{9n}{10n} \cdot 2^n} = 2^{\frac{9}{10} \cdot 2^n}. \end{aligned}$$

But this is less than 2^{2^n} the number of distinct Boolean functions $f : \{0, 1\}^n \rightarrow \{0, 1\}$. \square

Note: the reason we need to lower bound $2^n/10n$ circuits (and not $2^n/c$), is that encoding a circuit is super linear (i.e., $\Omega(\log n)$).

Chapter 2

Monotone Circuit Lower Bounds

We've seen that proving that SAT is not in P/poly, i.e., can't be solved by polynomial-size circuits, implies that P \neq NP. Due to the notorious difficulty of these questions, we are interested in proving *weaker* lower bounds, namely, some lower bounds against restricted classes of circuits. Here, we study such a restricted circuit class: A boolean circuit without negation gates, i.e., monotone circuits.

Definition 2.1 (Monotone circuit) A Monotone circuit is a Boolean circuit that contains fan-in two gates AND and OR, but has *no* NOT gates.

This means in particular that monotone circuits can compute only monotone functions: a Boolean function is said to be monotone if increasing the number of ones in the input cannot flip the value of the function from 1 to 0.

More precisely, for $\bar{x}, \bar{y} \in \{0, 1\}^n$, write $\bar{x} \geq \bar{y}$ iff $\forall i \in [n], x_i \geq y_i$, where $[n]$ denotes $\{1, \dots, n\}$. (Here, $x_i \geq y_i$ for Boolean x_i, y_i means simply that $1 \geq 0$ and $0 \geq 0, 1 \geq 1$, while $0 \not\geq 1$.)

Definition 2.2 (Monotone function) A Boolean function $f : \{0, 1\}^n \rightarrow \{0, 1\}$ is said to be *monotone* if $\forall \bar{x} \geq \bar{y}, f(\bar{x}) \geq f(\bar{y})$.

Many NP problems are monotone, like CLIQUE:

Given an undirected graph $G = (V, E)$ with n nodes, a k -clique in G is a set $U \subseteq V$ of size k , st. every pair of nodes $u_1, u_2 \in U$ is connected by an edge (in E):

$$\forall u_1 \in U \forall u_2 \in U (u_1 \neq u_2 \Rightarrow (u_1, u_2) \in E).$$

Recall that a computational (decision) problem is a *language*, namely an infinite set of finite strings over a finite alphabet (usually the alphabet $\{0, 1\}$). Here, our language consists of all the strings that encode (in some natural way) an accepted graph, i.e., a k -clique with n nodes. The natural way to encode a graph in our case is this: a graph $G = (V, E)$ with n nodes, is encoded by $\binom{n}{2}$ input variables x_{ij} , where the semantic of the encoding is: $x_{ij} = 1$ iff $(i, j) \in E$. In other words, if the input variable $x_{ij} = 1$, our input graph contains the edge (i, j) , and otherwise it does not.

We are interested in CLIQUE(k, n) for a fixed k , as the following Boolean function:

The computational problem **CLIQUE**(k, n):

Input: Undirected graph $G = (V, E)$ with n nodes, and a number k (given in unary, i.e., 1^k).

Accept: if the graph G contains a k -clique.

Reject: otherwise.

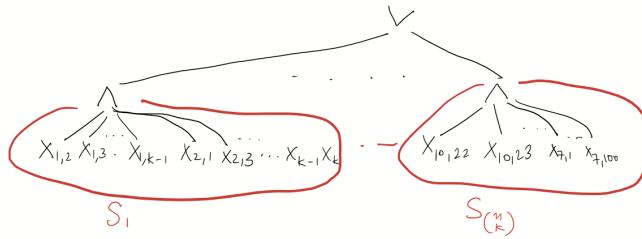
Note that **CLIQUE**(k, n) is a monotone function: If we add 1's to the input, we only *increase* the chance it has a k -clique!

It is known that **CLIQUE**(k, n) is NP-complete (see standard complexity textbooks; e.g., Papadimitriou 1994).

Since **CLIQUE**(n, k) is a monotone (Boolean) function we can compute it by a monotone Boolean circuit.

Example of a monotone circuit computing **CLIQUE**(n, k)

“Run” over all $\binom{n}{k}$ k -sub-graphs in G , and check if at least one of those is a clique:



$S_1, S_2, \dots, S_{\binom{n}{k}}$ are the $\binom{n}{k}$ subgraphs in G each of size k . Size of this circuit: $O(k^2 \cdot \binom{n}{k})$.

Crude-circuits $CC(X_1, \dots, X_m)$ for **CLIQUE**. A circuit for computing **CLIQUE**(n, k), consisting of a big \vee of all S_i 's, each computed by \wedge 's of edges, as in the example above, is called a **crude-circuit** for **CLIQUE**(n, k).

We denote by $CC(S_1, \dots, S_{\binom{n}{k}})$ the crude circuit computing the \vee of all subgraphs $S_1 \dots S_{\binom{n}{k}}$. In general, we can use different subgraphs of different size X_i in a crude-circuit: $CC(X_1, \dots, X_m)$ for $X_i \subseteq V$, where the X_i 's have different size, possibly different from k .

Note 2.1 When $k = w(\log n)$, $CC(S_1, \dots, S_{(k)})$ is of exponential size.

The following theorem shows this naive monotone circuit cannot be improved much:



Fig. 2.1 Alexander Razborov. Creator: Kozloff, Robert | Credit: Photo by Robert Kozloff. Copyright: The University of Chicago

Theorem 2.1 (Razborov [2])

Let $k = \sqrt[4]{n}$. Then, every monotone circuit computing $\text{CLIQUE}(n, k)$ has size $2^{\Omega(\sqrt[4]{n})}$.

That is, exists a constant c s.t. for large enough $n \in \mathbb{N}$, if C_n computes $\text{CLIQUE}(n, k)$ then $|C_n| \geq 2^{c \cdot \sqrt[4]{n}}$.

The rest of this chapter aims to prove this theorem. Our exposition is taken from Papadimitriou's textbook [?].

Approximation Method

We first provide an overview of the approach we take to prove Theorem 2.1 which is called *the approximation method*. We shall describe a way of approximating any **monotone** circuit for $\text{CLIQUE}(n, k)$ by a crude circuit, namely a big OR of cliques, as follows.

1. Given a monotone circuit C , we shall construct a crude circuit $CC(X_1, \dots, X_m)$ for some m and $|X_i| \leq l$ (for some l , all $i = 1, \dots, m$), that approximates $\text{CLIQUE}(n, k)$ with **precision** that is dependent on the number of gates in C .
2. I.e., if the precision is not good, namely the crude circuit $CC(X_1, \dots, X_m)$ for $\text{CLIQUE}(n, k)$ makes *many errors* on the $\text{CLIQUE}(n, k)$ function (i.e., says "NO" on an input that has a k -clique, and "YES" on an input that has no k -clique), it means that the circuit C has *many gates*, and vice versa.
3. We show that every crude circuit $CC(X_1, \dots, X_m)$ ($|X_i| \leq l$ for some l , for all $i = 1, \dots, m$), ought to make *exponentially many errors* on the function $\text{CLIQUE}(n, k)$. From 2 above we conclude that the number of gates in C was exponential.

The **approximation** (i.e., construction of a crude circuit for $\text{CLIQUE}(n, k)$ given the circuit C) will proceed in steps, one step for each gate of the monotone circuit:

1. If C is a monotone circuit computing $\text{CLIQUE}(n, k)$ we can *approximate* any gate OR or AND in C with a crude circuit.
2. Each such approximation step introduces rather few errors (false positives and false negatives).

2.1 Proof of monotone circuit lower bounds

Parameters & notation

Recall we want to compute $\text{CLIQUE}(n, k)$ with n the number of nodes in the graph and k the size of a clique within the graph. We set:

$$k = \sqrt[4]{n}.$$

Goal: Show that every monotone circuit computing $\text{CLIQUE}(n, k)$ has size at least $2^{c\sqrt[8]{n}}$ for some constant c (for sufficiently large n).

$$\begin{aligned} l &= \sqrt[8]{n} \\ p &\approx \sqrt[8]{n} \\ M &= (p - 1)^l \cdot l! \approx (\sqrt[8]{n} - 1)^{\sqrt[8]{n}} \cdot (\sqrt[8]{n})! \\ &\leq (\sqrt[4]{n})^{\sqrt[8]{n}} \end{aligned}$$

Each crude-circuit we use in the approximation is:

$$\begin{aligned} CC(x_1, \dots, x_m) \\ \text{for } m \leq M \text{ and } |X_i| \leq l, \forall i \in [m]. \end{aligned}$$

- The approximation of the monotone circuit C that computes CLIQUE(n, k) is done by induction on the size of C , ie., number of \vee, \wedge gates in C .
- Comment: Such induction is also called “Induction on the structure of C ”.
- Such induction proceeds as follows:

Base Case

$|C| = 1$, ie., C consists of only a single input gate g_{ij} . Recall g_{ij} is an input gate denoting whether $(i, j) \in E$, for $i, j \in V$. That is, if there is an edge between i and j in the input graph G .

This is an easy case: We need to show a crude cat $CC(X_1, \dots, X_m)$ with $m \leq M$ and $|X_i| \leq l \quad \forall i \in [m]$ that approximates g_{ii} (without introducing too many errors; We shall count precisely the number of potential errors later). But the circuit $CC(\{i, j\}) = g_{ij}$ by definition. (Hence, no errors here!)

Induction Step

Given two crude circuits $CC(\mathcal{X})$ and $CC(\mathcal{Y})$, with $\mathcal{X} = \{X_1, \dots, X_m\}$, $\mathcal{Y} = \{Y_1, \dots, Y_{m'}\}$, $m \leq M$, and $|X_i| \leq \ell$, for all i , $m' \leq M$ and $|Y_i| \leq \ell$, for all i .

We wish to construct another crude circuit for computing $CC(\mathcal{X}) \vee CC(\mathcal{Y})$, and $CC(\mathcal{X}) \wedge CC(\mathcal{Y})$.

Case 1: \vee -gate.

Naive attempt: $CC(\mathcal{X}) \vee CC(\mathcal{Y})$ is approximated by $CC(\mathcal{X} \cup \mathcal{Y})$. That is, $CC(X_1, \dots, X_m, Y_1, \dots, Y_{m'})$. At first glance this is a good solution because it does not introduce any errors (why?). But there is a *problem*: what if $m + m' > M$?

Solution: We need to cleverly *reduce* the number of sets $X_1, \dots, X_m, Y_1, \dots, Y_{m'}$. To do this we use a combinatorial lemma called The Sunflower Lemma.

2.2 The Sunflower Lemma

Definition 2.3 (Sunflower) Let U be some universe, namely a set of elements (e.g., nodes). Let $P = \{P_1, \dots, P_p\}$ be a family of distinct sets from the universe, i.e., $P_i \subseteq U$, for each i , with p some natural number. We call the family P a *sunflower* if each all pairs $P_i \neq P_j$ in the family P share the *same* intersection, called the *core* of the sunflower. In other words, there is a (possibly empty) set $\text{core} \subseteq U$, such that for all $i \neq j$, $P_i \cap P_j = \text{core}$. If P is a sunflower we call the P_i ’s the *petals* of the sunflower P .

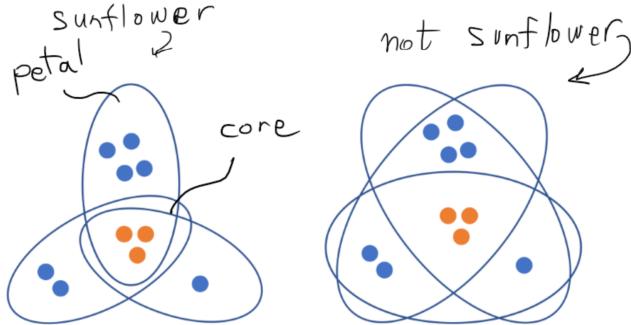


Fig. 2.2 From <https://theorydish.blog/2021/05/19/entropy-estimation-via-two-chains-streamlining-the-proof-of-the-sunflower-lemma/> by Lunjia Hu

Note: It's okay if the core is the empty-set! This means all petals are (pairwise) disjoint.

Sunflower Lemma (Erdős-Rado): For every ℓ, p , let Z be a family of more than $M = (p - 1)^\ell \cdot \ell!$ non-empty sets each of size $\leq \ell$ (over some universe U). Then, Z contains a sunflower of size p . In other words, Z contains p sets $\{P_1, \dots, P_p\}$, each P_i has size $\leq \ell$, and the intersection of every pair $P_i \neq P_j$ is fixed: $P_i \cap P_j = P_{i'} \cap P_{j'}$, for all $i \neq j \neq i' \neq j'$.

Proof (Proof of the Sunflower Lemma) By induction on ℓ .

Base case: $\ell = 1$. Thus the statement we need to show is that p different singletons form a sunflower. Which is true (the core is \emptyset).

Induction step: $\ell > 1$. Consider a family $D \subseteq Z$ of pairwise disjoint sets that is maximal in the sense that if we add any new set in Z to the family D , the sets in D are not pairwise disjoint anymore. That is, every set in $Z \setminus D$ intersects some set in D .

Case 1: If D contains $\geq p$ sets then D is a sunflower with empty core, and we are done.

Case 2: Otherwise, let E be the *union* of all sets in D . Since $|D| < p$, i.e., D contains less than p sets, and each set in D has size $\leq \ell$, we know that $|E| \leq (p-1) \cdot \ell$.

Moreover, E intersects every set in Z by assumption. Since Z has more than M sets by assumption, and each set intersects some element of E , there exists an element $d \in E$ that intersects $> \frac{M}{(p-1)\ell} = (p-1)^{\ell-1} \cdot (\ell-1)!$ sets in Z .

Remark 2.1 If a set E intersects all sets in a family of sets X_1, \dots, X_M then there is an element in E that appears in $\geq \frac{M}{|E|}$ sets X_i . Otherwise, each element in E appears in $< \frac{M}{|E|}$ sets X_i . Thus, E intersects $< |E| \cdot \frac{M}{|E|} = M$ sets X_i , which is a contradiction to the assumption. \square

Consider

$$Z' := \{z \setminus \{d\} \mid z \in Z \text{ and } d \in z\}.$$

We know that Z' contains more than $M' = (p - 1)^{l-1} \cdot (l - 1)!$ sets. By *induction hypothesis* (since M' is " M with ℓ decreased by one"), Z' contains a sunflower denoted $\{P_1, \dots, P_p\}$ with $|P_i| \leq \ell - 1$, for all i . Hence, $\{P_1 \cup \{d\}, \dots, P_p \cup \{d\}\}$ is a sunflower in Z . This concludes the Sunflower Lemma's proof. \square

Approximating OR and AND using Plucking - By the Sunflower Lemma, every family of $\geq M$ nonempty sets, each of cardinality $\leq l$, then we can find a sunflower in it with $l = \sqrt[3]{n}(p \approx \sqrt[3]{n})M = (p - 1)^l \cdot \ell!$. - Plucking a sunflower: replacing all petals by their core. 4 sets

Corollary: If we have $> M$ sets in a family, by repeated plucking we can reduce the number of sets to $\leq M$ (if we cant apply plucking, anymore we know by the Sunflower Lemma that the number of sets is $\leq M$). pluck (z): the result of repeated plucking of a family of sets Z , until $|Z| \leq M$. Definition (Approximate OR and AND): foxily of sets The approximate $-V$ of two crude -circuits $CC(x)$ and $cc(y)$ is: $cc(\text{pluck}(x \cup y))$.

The approximate $-\Lambda$ of $CC(x)$ and $CC(Y)$ is: $cc(\text{pluck}\{X_i \cup Y_j : |X_i \cup Y_j| \leq l \text{ and } X_i \in \chi, Y_j \in Y\})$

False Positives & False Negatives

We shall now show that V and Λ -approximators are good, in the sense that they introduce few errors!

Out of all possible inputs to a cat computing Clique (n, k) (for $k = \sqrt[4]{n}$), the ne are: Accept-instances: $G = (V, E)$ is a graph on n nodes that contains a k -clique. Reject-instances: $G = (V, E)$ is a graph on n nodes that does not contain a k -clique. We shall restrict attention to only subsets of Accept and Rejected instances. This will be sufficient for the proof (it's more convenient that way). Thus, our focus is on "extreme" cases of inputs:

- Positive-Inputs: $G = (V, E)$ has x nodes and a k -clique (k nodes w/ all edges between them); while no other edge exist in G .

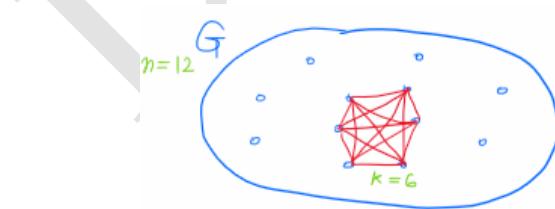


Fig. 2.3 Enter Caption

There are $\binom{x}{k}$ positive-inputs, each one is clearly an accept input to $CUQ \cup E(n, k)$.
- Negative Inputs: $G = (V, E)$ has n nodes and $(k - 1)$ -colouring (independent sets). In other words, nodes V are coloured by some $k - 1$ colours (one colour for each

independent set [ie., set of nodes w/ no edge between them]). And all edges between notes w/ different colours.

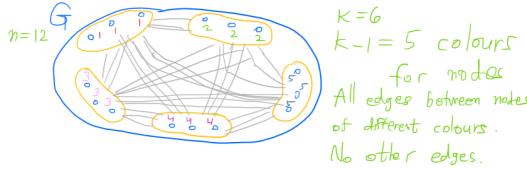


Fig. 2.4 Enter Caption

Note: 1) There are $(k-1)^n$ negative -inputs. (We count twice two identical graphs w/ colours interchanged.) 2) A $(k-1)$ -colouring is a negative-input for CUQUE(n, k) because a k -clique cannot be coloured by $k-1$ colours. 3) In fact, even a single edge added to a $(k-1)$ -colouring will make the graph contain a k -clique!

V -approximator of $\text{CC}(x) \vee \text{CC}(y)$ introduces 1) a false negative:

A k -clique $G = (v, E)$ s.t.: $(\text{cc}(x) \vee \text{cc}(y))(G) = 1$ 2) a false positive: $\text{cc}(\text{pluck}(x \cup y))(G) = 0$ V -approximator $A(k-1)$ -cobounding $G = (v, E)$ s.t.

$$\frac{(\text{cc}(x) \vee \text{cc}(y))(G) = 0}{\text{cc}(\text{pluck}(x \cup y))(G) = 1} = 1$$

1 -approximator of $\text{CC}(x) \wedge \text{CC}(y)$ introduces 1) a false negative:

A k -clique $G = (v, E)$ s.t.: $(\text{cc}(x) \wedge \text{cc}(y))(G) = 1$ 2) a false positive: $\text{cc}(\text{pluck}(\{x_i \mid y_i : |x_i \cup y_i| \leq l, x_i \in X, y_i \in Y\})) = 0$

$$\text{cc}(\text{pluck}(\{x_i \cup y_j : |x_i \cup y_j| \leq l, x_i \in X, y_j \in Y\})) = 1$$

Lemma 1: Each approximation step introduces at most $M^2 \cdot \frac{(k-1)^n}{2^p}$ false positives.

Lemma 2: Each approximation step introduces at most $M^2 \cdot \binom{n-l-1}{k-l-1}$ false negatives.

Proof of monotone Pkt lower bound from these lemmas. CLAIM: Every crude-ctt $\text{CC}(X_1, \dots, X_m)$ w/ $|X_i| \leq l$ and $m \leq M$ is either identically 0 (and thus is wrong on all positive-instances), or outputs 1 on at least half of the negative -instances. Proof: If $\text{CC}(x_1 \dots x_m) \neq 0$, then it accept's at least those graphs G that have cliques on at least one of the sets X_i , for some i . The size of X_i is $\leq l < k$, thus many graphs G that are not k -cliques still have e-cliques

cont. Let's count how many such graphs exist, using probability. Consider a graph $G = (V, E)$; assign randomly (and independently) from among $(k-1)$ colours to the modes of G . Let $v_1 \neq v_2 \in V$ be two nodes.

$$\Pr[\text{colour of } v_1 = \text{colour of } v_2] = \frac{1}{k-1}$$

Denote by $R(X_i)$ the event that in the random colouring to G , there exists a pair of nodes with the same colour in X_i . Then, $\Pr[R(x_i)] \leq \frac{\binom{|x_i|}{2}}{k-1} \leq \frac{\binom{l}{2}}{k-1} \leq \frac{1}{2}$. There are $\binom{|x_i|}{2}$ pairs of nodes in X_i . We use the union bound. This means that out of all negative-instances to CLIQUE (n, k) (namely, all $(k-1)$ -colouring of a graph w/ n nodes), at least half of them are going to colour X_i w/ different colours. Hence, for these negative instances, there will be edges between all nodes in X_i , and thus $(C(x_1, \dots, x_m))(G) = 1$ for each of these negative instances G .

We are now ready to conclude the main result. Recall: $l = \sqrt[3]{n}$ and Let $p = \sqrt[3]{n} \cdot \log n$. Thus, $M_l = (p-1)^l \cdot l! < \left(n^{\frac{1}{3}}\right)^{\sqrt[3]{n}}$, for large l . Let C be a monotone circuit computing CLIQUE $(n, \sqrt[3]{n})$. - We apply the approximators at each gate of C iteratively. - The output gate of C thus is written as a $\text{CC}(x_1, \dots, x_m)$ for some $m \leq M$ and $|x_i| \leq l$. - Based on the above, we have two cases:

Case 1: $\text{cc}(x_1, \dots, x_m) \equiv 0$. Thus, the number of false negatives introduced is the total of all possible positive-instance, namely all possible $k-c$ cliques: $\binom{n}{k}$. Thus,

$$|C| \geq \frac{\binom{n}{k}}{\text{lemmax}^2 \binom{n-l-1}{k-l-1}} \geq \frac{1}{M^2} \left(\frac{n-l}{k}\right)^l \geq n^{c\sqrt[3]{n}},$$

Case 2 : $\text{CC}(x_1, \dots, x_n)$ has at least $\frac{1}{2}(k-1)^n$ false positives (half of $(k-1)$ -colourings). By Lemma 2: $|c| \geq \frac{\frac{1}{2}(k-1)^n}{M^2 \frac{(k-1)^n}{2^p}} = \frac{2^p}{2 \cdot M^2} > n^{c+8}n$, for $c = \frac{1}{3}$.

Lemma 1: Each approximation step introduces at most $M^2 \frac{(k-1)^n}{2^p}$ false positives.

Proof of F: Case 1: OR-approximator We start with $\overline{CC}(x_1, \dots, x_m)$ and $\text{CC}(y_1, \dots, y_{m_m})$ and consider a false positive introduced by

$$\text{cc}(\text{pluck}(X_1, \dots, X_m, Y_1, \dots, Y_{m'})).$$

That is, a $G = (v, E)$ s.t.,

$$\text{cc}(x_1, \dots, x_m)(G) = 0, \quad \text{cc}(y_1, \dots, y_m)(G) = 0$$

and

$$\text{cc}(\text{pluck}(X_1, \dots, X_m, Y_1, \dots, Y_{m'}))(G) = 1.$$

We consider each plucking involved in (2), and bound from above the number of false positive introduced by this plucking. (Note this is the only reason a false positive can be introduced.)

- Consider a single plucking : replace sunflower $\{z_1, \dots, z_p\}$ by its core Z .

By (1): Y'_i 's & X'_i 's (& 's) are all hon-cliques. Thus, by (2) Z is a clique and every petal z_i has two nodes w/ the $L \text{ bg}(1)$ same colour!

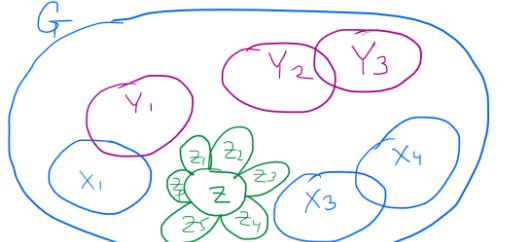


Figure: Here $z_1, z_2, z_3 \in \{x_1, \dots, x_m\}$ and $z_4, \dots, z_p \in \{y_1, \dots, y_m\}$ (arbitrary choice).

Fig. 2.5 Enter Caption

- We count the number of such colourings. - We do this probabilistically:
 Choosing a $(k - 1)$ -colouring of the nodes randomly and independently, what's the probability every z_i has repeated colours but z does not? - As before, let $R(X)$ be the probability that X has repeated colours.

As before, let $R(X)$ be the probability that X has repeated colours.

$$\Pr[R(z_1) \wedge \dots \wedge R(z_p) \wedge \neg R(z)] \leq \Pr[R(z_1) \wedge \dots \wedge R(z_p) \mid \neg R(z)]$$

$$\begin{aligned} &= \prod_{i=1}^p \Pr[R(z_i) \mid \neg R(z)] \\ &\leq \prod_{i=1}^p \Pr[R(z_i)] \leq \frac{1}{2^p} \end{aligned}$$

$$\Pr[A \parallel B] = \Pr[A \mid B] \cdot \Pr[B]$$

Li's don't have common nodes, except those in Z .
 We've seen before that

$$\Pr[R(x)] \leq \frac{1}{2}$$

Probability of repetition of colours is increased if we do it restrict ourselves to colourings w/ no repetitions in $z \subseteq Z_i$.

Finally, since the approximation step entails up to $\frac{2M}{p-1}$ pluckings (each plucking decreases the number of sets by $p - 1$, and there are no more than $2M$ sets when we start), the lemma holds for the OR approximation step: because e

$$M^2 \cdot \frac{(k-1)^n}{2^p} \geq \frac{2M}{p-1} \cdot \frac{(k-1)^n}{2^p}$$

Cont. of proof of Lemma 1

Consider now an AND approximation step of crude circuits $CC(x)$ and $C(y)$. It can be broken down in three phases: First, we form $CC(\{X \cup Y : X \in \chi, Y \in Y\})$; this introduces no false positives, because any graph in which $X \cup Y$ is a clique must have a clique in both X and Y , and thus it was accepted by both constituent crude circuits. The second phase omits from the approximator circuit several sets (those of cardinality larger than ℓ), and can therefore introduce no false positives. The third phase entails a sequence of fewer than M^2 plucking, during each of which, by the analysis of the OR case above, at most $2^{-P}(k-1)^n$ false positives are introduced. The proof of the lemma is complete: because at total we introduced $\leq M^2 \cdot \frac{(k-1)^n}{2^P}$ false positives

Lemma 2: Each approximation step introduces $\leq M^2 \binom{n-l-1}{k-l-1}$ false negatives.
PRoOF: Case V:a false negative: A k -clique $G = (v, E)$ st.: $(cc(x) \vee ccc(y))(G) = 1$ (

$$\underbrace{\text{colpluck}(x \cup y)(G) = 0}_{V\text{-aproximator}}$$

This is impossible, because plucking only deletes sets and make them smaller; hence if (1) holds then (2) cannot. Case 1: A false negative: A k -clique $G = (v, E)$ st.: $(cc(x) \wedge cc(y))(G) = 1$

$$c(P / \{x_i \cup Y_j : |x_i \cup Y_j| \leq l, x_i \in \chi, Y_j \in Y\}) = 0 \quad (2)$$

In the first stop we replace $ca(x) \wedge c(y)$ by $CC(\{X \cup Y : x \in X, y \in Y\})$. Hence, if G is a K -clique and both X and Y are each cliques in G , it must be that $X \cup Y$ is also a clique in G (why?). Thus, no false negatives are introduced in this step.

(cont.) — denoted larger than l . This introduce several false negatives:

We next delete all sets $\overbrace{X_i \cup Y_j}^Z$

All k -cliques G that contain Z . We calculate an upper bound on these false negatives: There are precisely $\binom{n-|z|}{k-|z|}$ k -cliques er that contain z (as part of the clique). Since, $|z| > l$, the upper bound on the false negatives introduced by each deletion is $\binom{n-l-1}{k-l-1}$.

Since there are $\leq M^2$ deletions of sets, (because $|\chi| = |y| = M$), we get that the number of false negatives introduced by a_n 1-approximation step is $\leq M^2 \cdot \binom{n-l-1}{k-l-1}$.

DRAFT

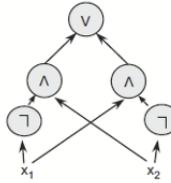
Chapter 3

Constant Depth Circuit Lower Bounds

3.1 Defining constant depth circuits

Recall that the depth of a circuit is the maximal length of a path from a leaf to the output node.

Here is an example of a circuit of depth 3:



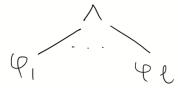
Recall that we are interested in the asymptotic study of circuit families, not a single circuit. That is, we want to consider how circuit size grows when the number of inputs n to a family of Boolean functions grows.

We can consider circuits of constant depth. This means that the depth of the circuit is *independent of the number of inputs n* . In other words, while n the number of input gates can grow to infinity in the Boolean circuit family $\{C_n\}_{n=1}^{\infty}$, the depth of each circuit C_n stays the same!

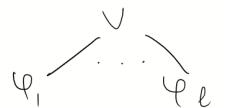
Note: If the depth of the circuit is constant & the fan-in of gates is at most 2, then the number of functions we can compute w/ such constant-depth circuits is constant. For example, the number of variables (appearing as leaves, i.e., input nodes) is constant that way, so for a large enough number of inputs n , we will not be able to compute functions that read all the n inputs. Thus, this model is *not* complete: for a constant d , a depth- d circuit cannot compute all Boolean functions over n inputs for every $n \in \mathbb{N}$.

Corollary: To make the model of const. depth meaningful we allow *unbounded* fan-in gates:

Unbounded fan-in AND ($\varphi_1 \wedge \varphi_2 \wedge \dots \wedge \varphi_\ell$):



Unbounded fan-in OR ($\varphi \vee \dots \vee \varphi_\ell$): Where φ_i 's are circuits (of constant depth)



by themselves, with possibly *joint* nodes.

Important: When speaking about constant-depth circuits, we assume by default the fan-in of \vee, \wedge is *unbounded*. (\neg has fan-in one as always.)

To Be Completed!

References

1. Sanjeev Arora and Boaz Barak. *Computational Complexity: A Modern Approach*. Cambridge University Press, New York, NY, USA, 2009.
2. Alexander A. Razborov. Lower bounds on the monotone complexity of some Boolean functions. *Dokl. Akad. Nauk SSSR*, 281(4):798–801, 1985. In Russian. English translation in *Soviet Math. Doklady*, 31:354-357, 1985.