


REV	Description	DATE	BY
v1	First release PCB version is v1	3/6/2015	QXN

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3	PROCESSOR 1 OF 3, JTAG HEADER
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5	PROCESSOR 3 OF 3
6	LED, CONFIGURATION AND BUTTON
7	DDR3 MEMORY
8	eMMC FLASH
9	10/100 ETHERNET
10	EXP CONN, uSD

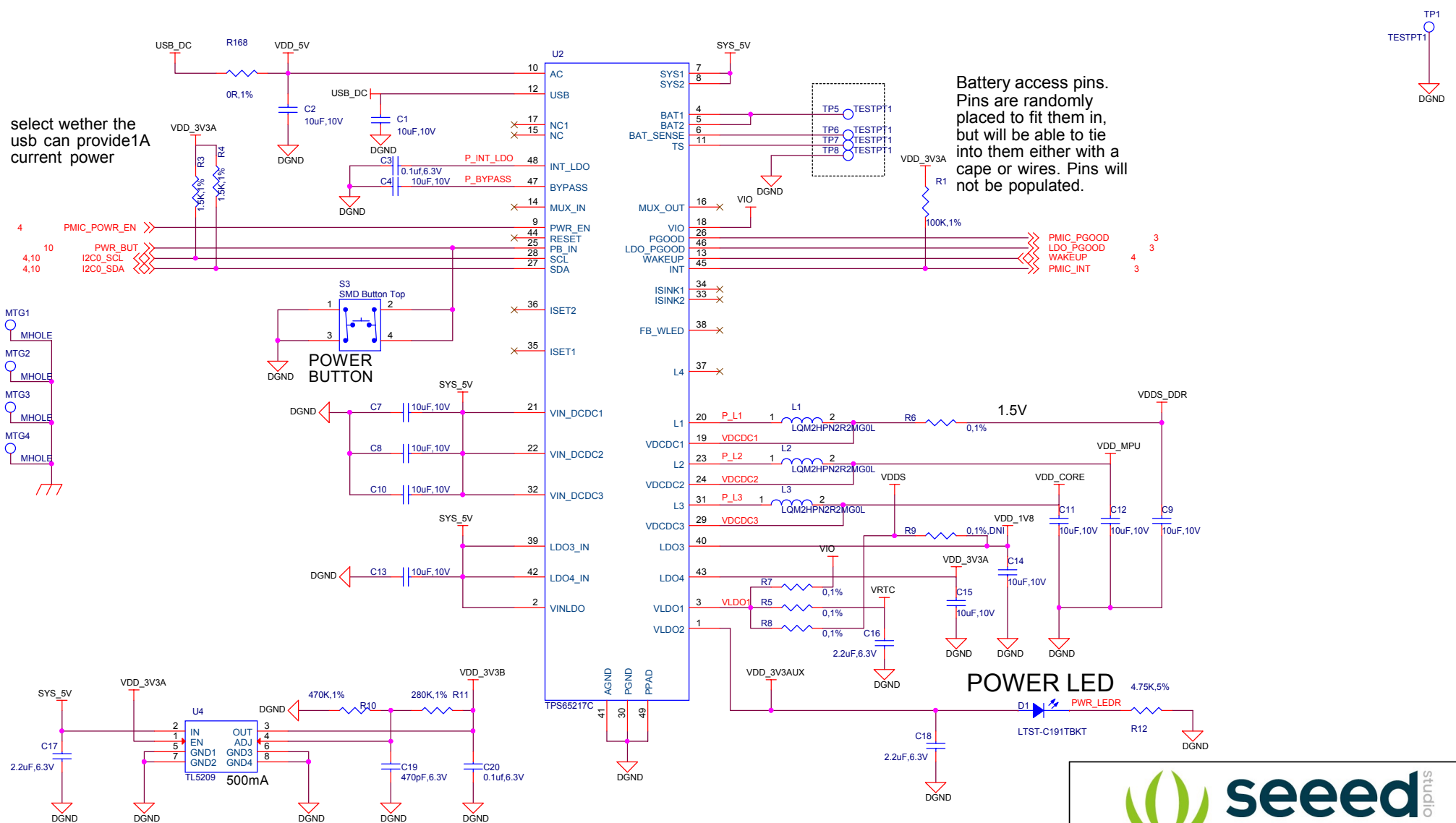
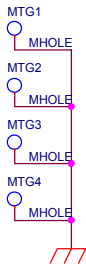
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Title BeagleBone Green Cover Page		
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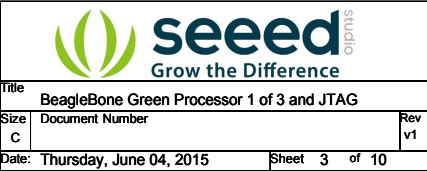
select wether the  
usb can provide 1A  
current power



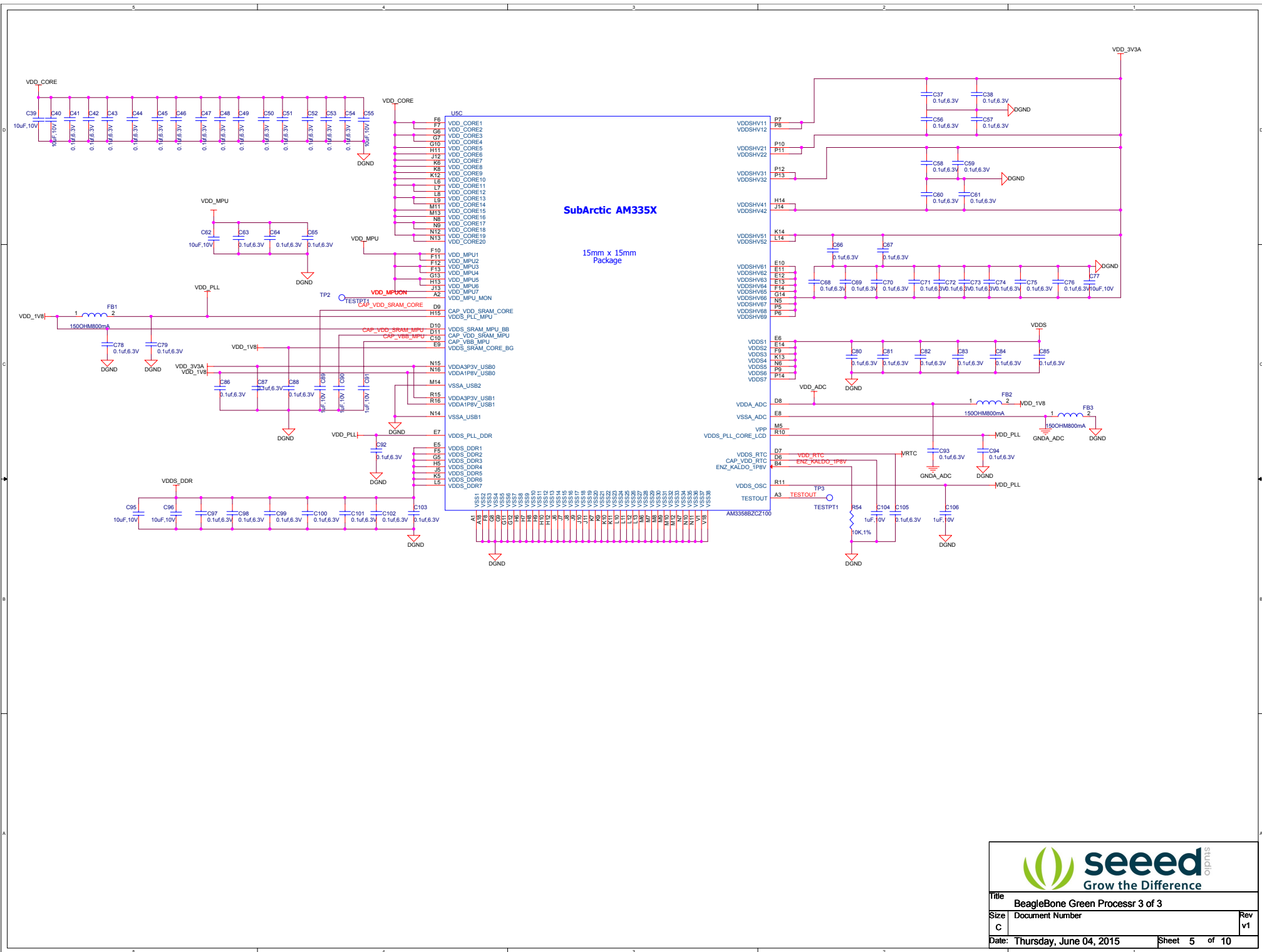
Battery access pins.  
Pins are randomly  
placed to fit them in,  
but will be able to tie  
into them either with a  
cape or wires. Pins will  
not be populated.

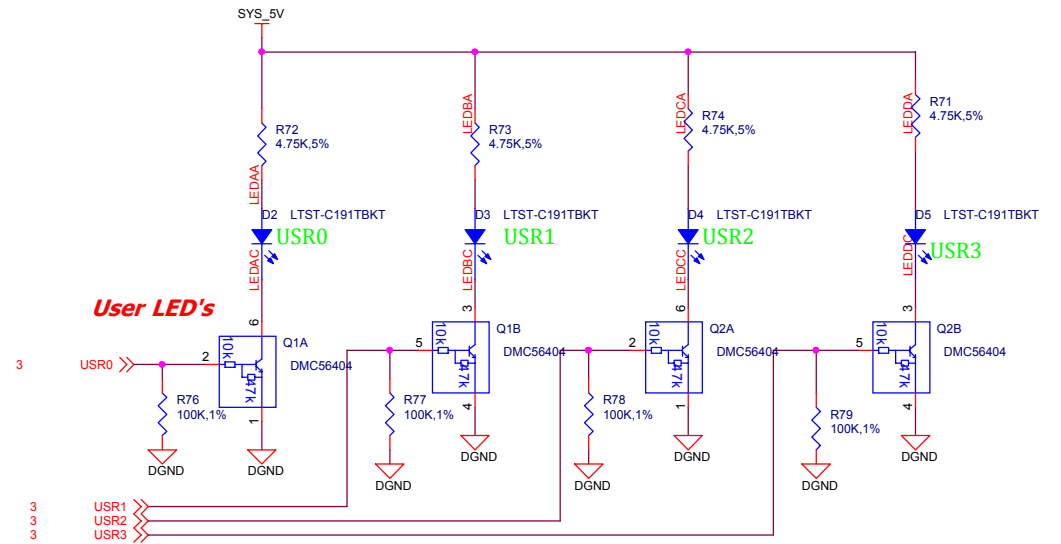
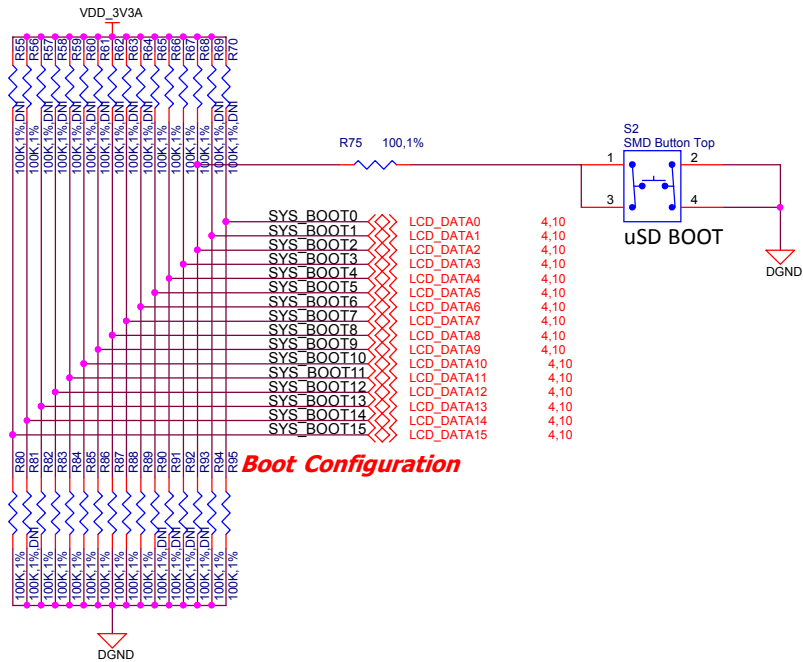


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SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5] 1	UART0



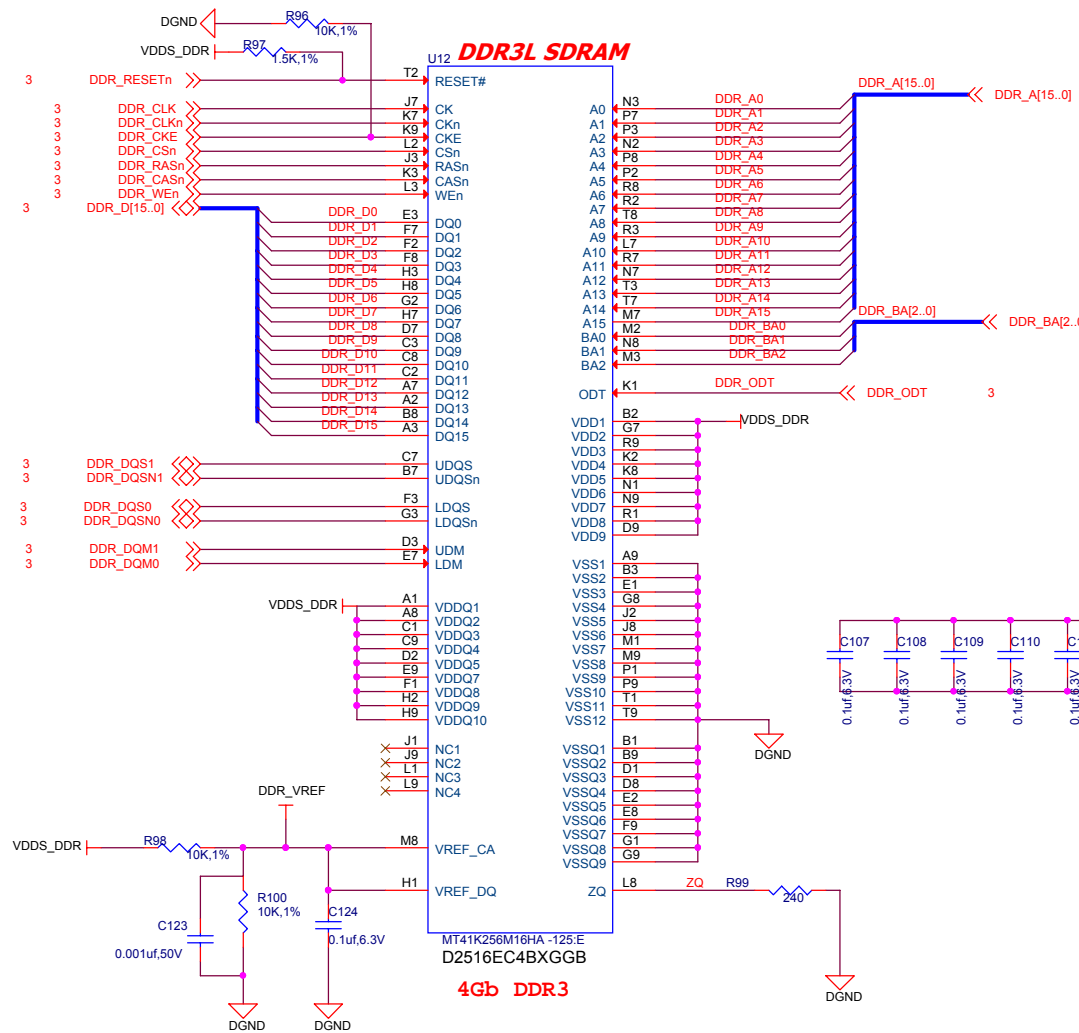
Title BeagleBone Green LED, Configuration, and Reset

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Title		
BeagleBone Green DDR3 Memory		
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3,10  
3,10  
3,10  
3,10  
3,10  
3,10  
3,10  
3,10  
3,10  
3

MMC1\_DAT0  
MMC1\_DAT1  
MMC1\_DAT2  
MMC1\_DAT3  
MMC1\_DAT4  
MMC1\_DAT5  
MMC1\_DAT6  
MMC1\_DAT7  
MMC1\_CMD  
MMC1\_CLK  
eMMC\_RSTn

VDD\_3V3B

VDD\_3V3B

eMMC\_VCCI

2.2uF,6.3V

VDD\_3V3B

C126  
10uF,10V  
C127  
0.1uF,6.3V  
C128  
0.1uF,6.3V  
C129  
0.1uF,6.3V  
C130  
0.1uF,6.3V

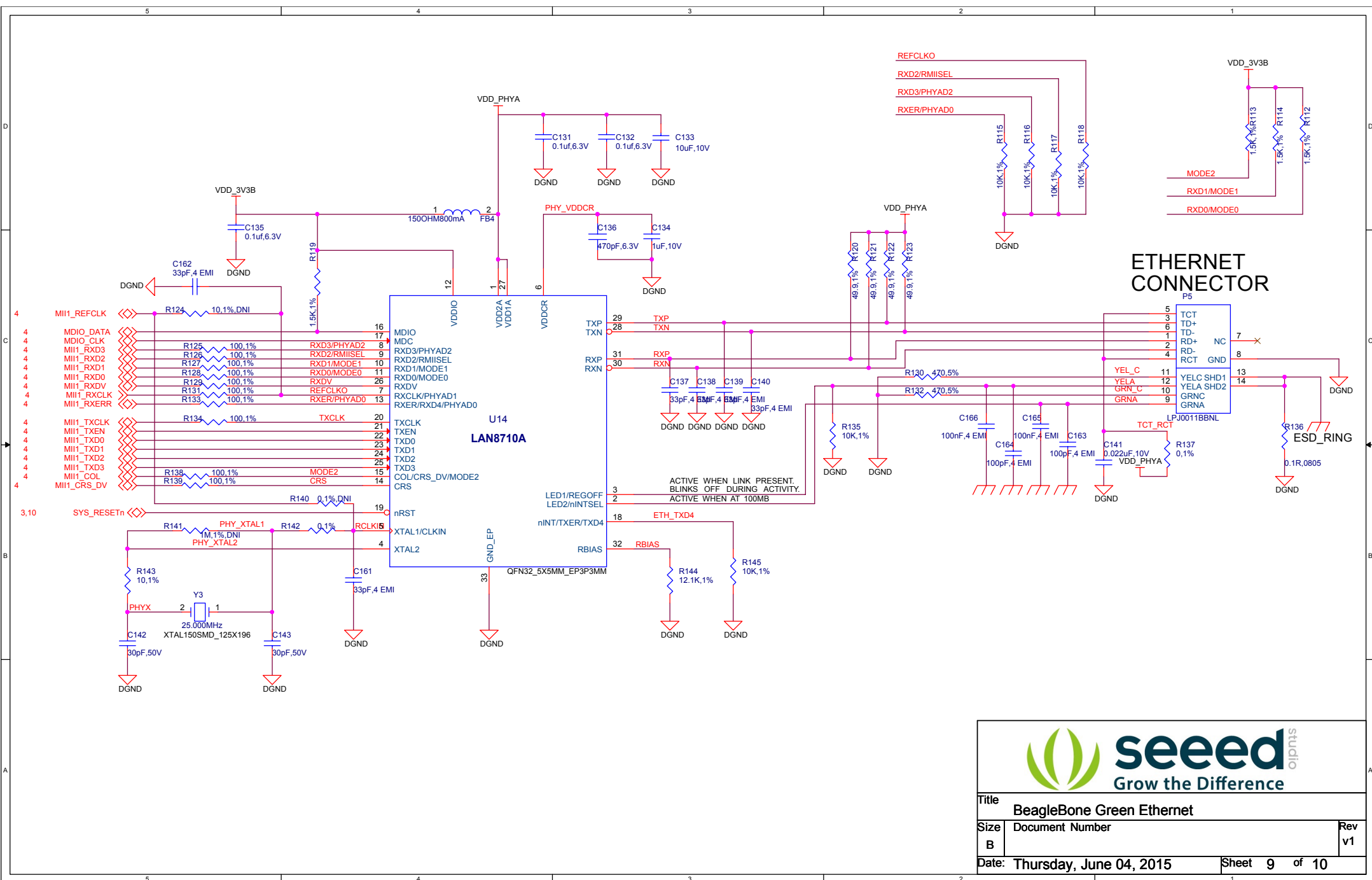
MEM\_MNAND\_4GB

MTFC4GACAAAM-1M WT  
KE4CN2H5A-A58



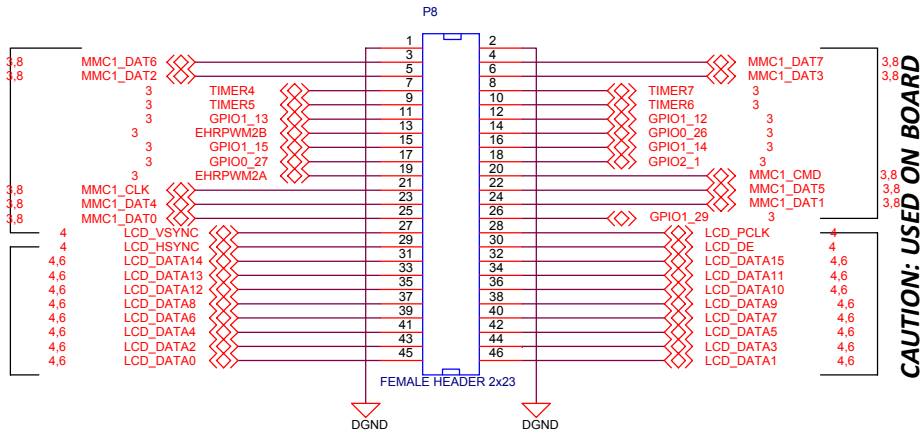
Title			BeagleBone Green 4G eMMC		
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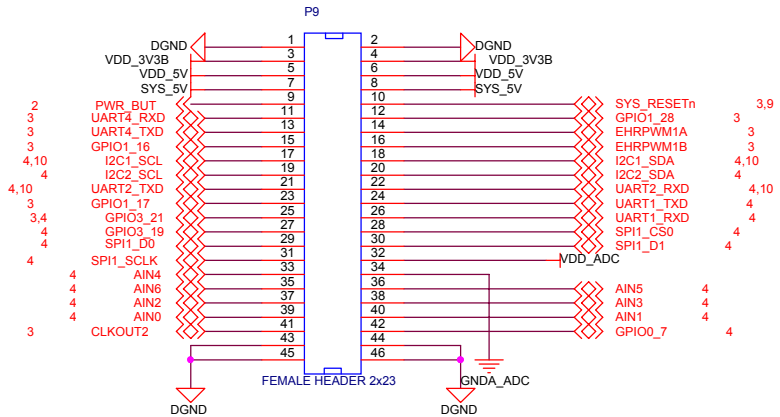
Title			BeagleBone Green Ethernet
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CAUTION: USED ON BOARD

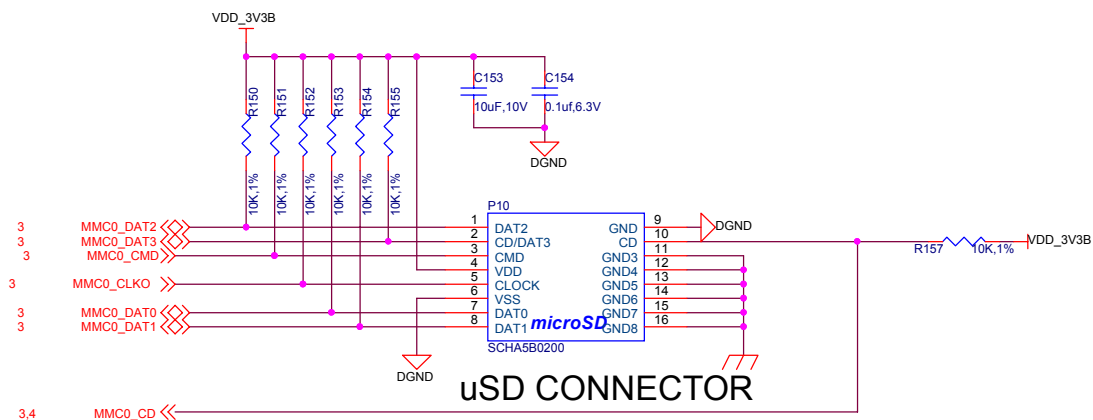


CAUTION: USED ON BOARD

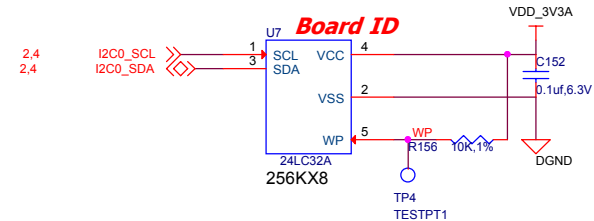
## EXPANSION HEADER



## EXPANSION HEADER



## uSD CONNECTOR



## GROVE CONNECTOR



Title		
BeagleBone Green Expansion Headers, uSD and EEPROM		
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