

**μProc**

The diagram illustrates a microprocessor (μProc) at the top, connected by a dashed line to a memory stack structure below. The stack structure consists of a horizontal row of blocks. From left to right, the first block is light blue and labeled 'Header'. The second block is gray and unlabeled. The third block is light blue and labeled 'IP'. The fourth block is light blue and labeled 'Stack'. The fifth block is light blue and labeled 'Env'. The sixth block is light blue and labeled 'VStack'. The seventh block is gray and unlabeled. The eighth block is gray and unlabeled. The ninth block is gray and unlabeled. The dashed line connects the μProc to the top of the 'Header' block and the top of the final gray block.

**Header**

**IP**

**Stack**

**Env**

**VStack**