

List of DLCA Assignments (C1/C2/C3)

Assignment No	Assignment
1	Decimal to binary and vice versa conversion and 2's complement
2	Verify the truth table and realize the logic expression
3	Implement NOT, AND ,OR and EXOR gates using the universal gates NAND and NOR
4	Implement Half Adder and Full Adder.
5	Implement conversion of 4-bit binary to gray code and vice versa.
6	Implement a 4-bit ripple carry adder.
7	Booth's Algorithm
8	Carry look ahead Generator
9	IEEE 754 conversion
10	Implement a 8:1 Mux and 3:8 Decoder
	<b>Written Assignment</b>
1	Write a short note on cache mapping techniques (Direct, Associative and Set associative mapping) with examples for each
2	Write a short note on multicore architecture

Prof . Tasneem Mirza

Prof. Vaishali Suryavanshi

Prof. Vaibhav Ambhire