

- Input is in IEEE 754 Standard format of binary representation of the floating point number (sign bit, exponent bit, fraction bits).
- The shifter circuit provided by the simulator software tool may be used. Alternatively, you may implement them by yourselves.
- Implementations of Overflow/Underflow flags are necessary. Rounding is also necessary. There is no need to involve clock pulses if you do one-step rounding. Alternatively, you may decide to do it in one clock pulse.