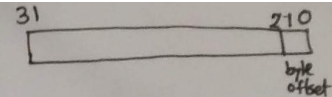
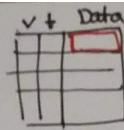


Bits in a Cache



How many bits are required for a direct mapped cache with 4 KB of data and 1-word block size?

$$\begin{aligned} \text{cache data} &= 4 \text{ KB} \\ &= 4 \times 1024 \text{ B} \\ &= 1024 \times 4 \text{ bytes} \\ &= 1024 \times 1 \text{ word} \\ \# \text{ row in cache} &= \frac{\text{total data}}{\text{block size}} \\ &= \frac{1024 \times 1 \text{ word}}{1 \text{ word}} \\ &= 2^{10} \end{aligned}$$

$$\text{index bit, } n = 10$$

$$\begin{aligned} \text{Cache size} &= 2^n \times (b + t + v) \\ &= 2^{10} \times (32 + 20 + 1) \\ &= 43 \text{ Kbits } 53 \text{ Kbits} \end{aligned}$$

$$\begin{aligned} t &= 32 - (n + m + 2) \\ &= 32 - (10 + 0 + 2) \\ &= 32 - 12 \\ &= 20 \end{aligned}$$

[block size = 1 word = 2^0 word] [2^m words]

Same problem

16 KB of cache data
4-word blocks

$$\begin{aligned} \text{Cache data} &= 16 \text{ KB} \\ &= 2^{10} \times 16 \text{ B} \\ &= 2^{10} \times 4 \times 4 \text{ B} \\ &= 2^{10} \times 4 \text{ word} \end{aligned}$$

$$\# \text{ row} = 2^{10}$$

$$n = 10$$

$$\begin{aligned} \text{cache size} &= 2^{10} \times (b + t + v) \text{ bits} \\ &= 2^{10} \times (2^4 \text{ Byte} + t + 1) \text{ " } \\ &= 2^{10} \times (2^7 + t + 1) \text{ " } \\ &= 2^{10} \times (128 + 19) \text{ " } \\ &= 147 \text{ Kbits} \end{aligned}$$

block size = 2^m word
m word offset

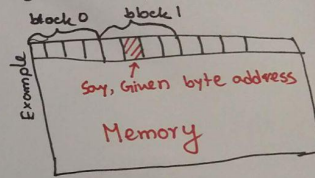
$$\begin{aligned} t &= 32 - (n + m + 2) \\ &= 32 - (10 + 2 + 2) \\ &= 32 - 14 \\ &= 18 \end{aligned}$$

Mapping an address

consider a cache with 64 blocks and a block size of 16 bytes. To what block number does byte address 1200 map?
in cache of memory

cache has 64 blocks
= 2^6

block size = 16 bytes



cache block Number = $\left(\overset{\text{memory}}{\text{block address}} \right) \bmod (\# \text{ of blocks in cache})$

first find this

$$\text{memory block address} = \left\lfloor \frac{\text{Given byte address}}{\# \text{ of bytes in a block}} \right\rfloor = \left\lfloor \frac{1200}{16} \right\rfloor = 75$$

$$\text{cache block Number} = 75 \bmod 64 = 11$$

\therefore Cache block 11

byte address 1200 is in 75th block of memory which maps to 11th block of cache

Similarly, all addresses between

$$\left\lfloor \frac{1200}{16} \right\rfloor \times 16 \quad \text{and} \quad \left\lfloor \frac{1200}{16} \right\rfloor \times 16 + (16-1)$$

or 1200 and 1215 map to 11th block of cache