

## **Discussion on Flags During Logic Operations:**

Along with the rules of Assembly language, we have added some exceptions (only for this assignment) to incorporate flexibility to flag status bits after logical operations. Remember that this flexibility is to make your assignment easier although it breaks some of the rules of the assembly programming language.

### **NOT Operation:**

After the NOT operation which makes the result to 0000, if Z becomes 0 from 1, it will not be accepted. However, if Z becomes 1 or if Z value remains unchanged, both will be accepted.

After the NOT operation, if the S bit remains unchanged, or it reflects the highest order bit of the result, both will be accepted. But if S bit changed and it changed to a wrong value, it will not be accepted.

To make your life easier, we shall not check the C and V bits after NOT operation, i.e., you can consider these as Don't care.

### **AND/OR/XOR Operation:**

C and V should be cleared (0) after the operation.

S and Z should be changed according to the output.

## **Deadline Extension:**

The deadline of assignment 1 submission has been extended to 11:00 AM, March 31, 2021 (Wednesday). Note that it is 11:00 AM, not 11:00 PM.