

Assignment

Student ID \rightarrow 1705056

Verifying student ID - 1705086

Chapter - 1

Section - 2

Problem - 42

42. Q. Construct a combinational circuit using inverters, OR gates, and AND gates that produces the output $(p \wedge \neg r) \vee (\neg q \wedge r)$ from input bits $p, q,$ and r .

Answer:

