

UUT Report

Station ID:

Serial Number:

Date:

Time:

Operator:

Execution Time:

Number of Results:

UUT Result:

Serial Number:

Type Designator:

FW Version:

Power Supply:

IT-W-7303101

NONE

lunedì 11 settembre 2023

13:47:02

administrator

114.7902898 seconds

1026

Terminated

4294967295

B3-D4MB-111A

0.18.143

Production Port

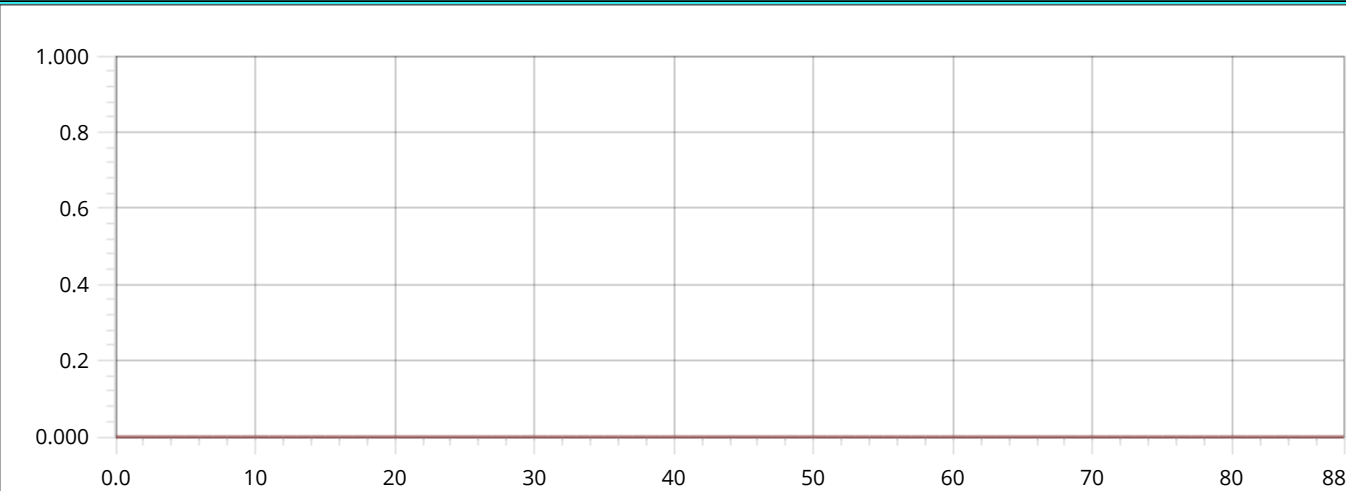
Begin Sequence: MainSequence  
(C:\Users\ITLUBAR5\ABB\LABRND VITTUONE - LabRnD\_Shared\TestStand\Sequences\EQ Meter\New Sequences\D13-Input\_State.seq)

Pass/Fail Test

Status:Failed

Additional Results

Status:Done

Voltages\_increment[0..88]:

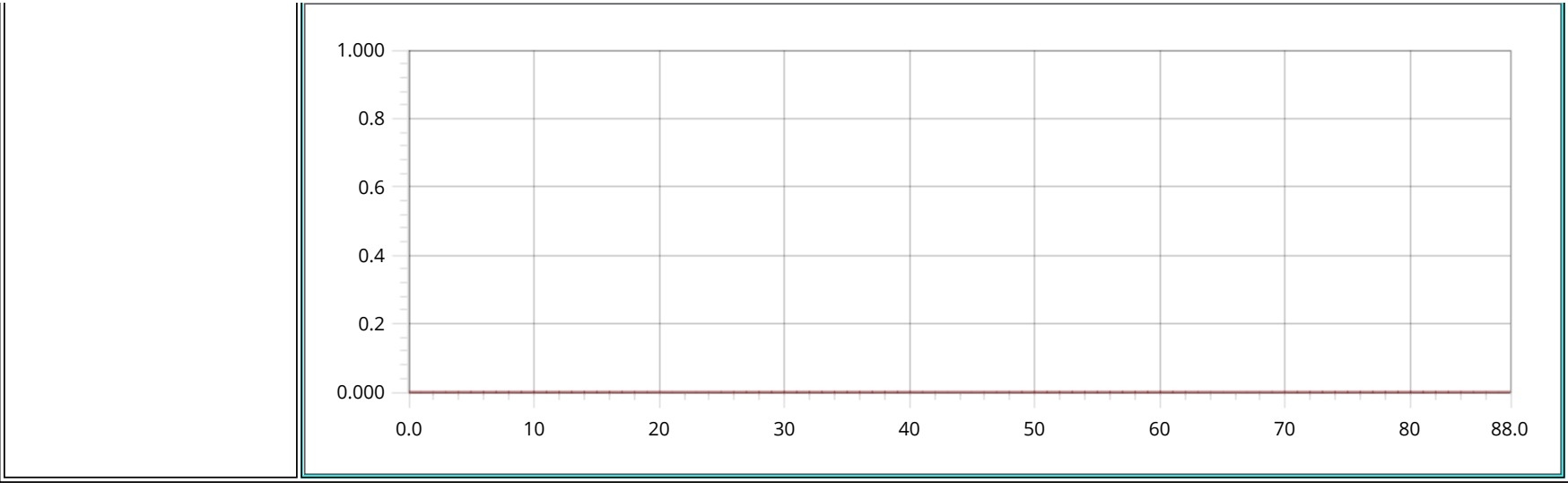
Pass/Fail Test

Status:Failed

Additional Results

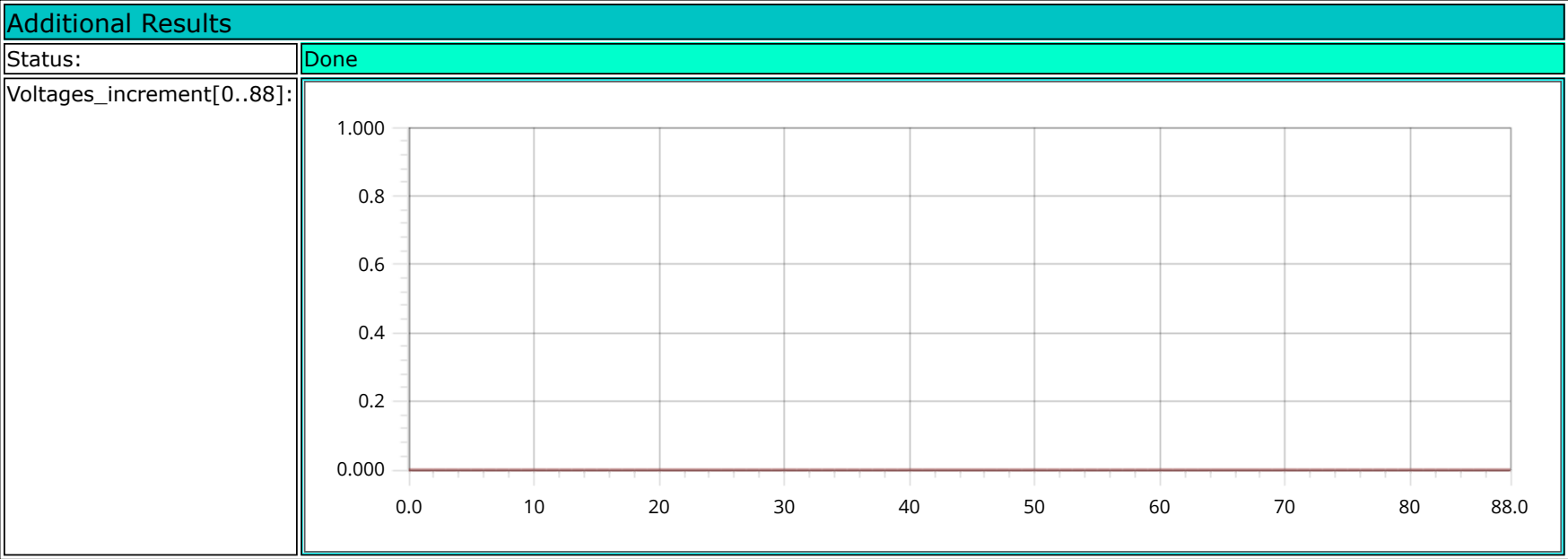
Status:Done

Voltages\_increment[0..88]:



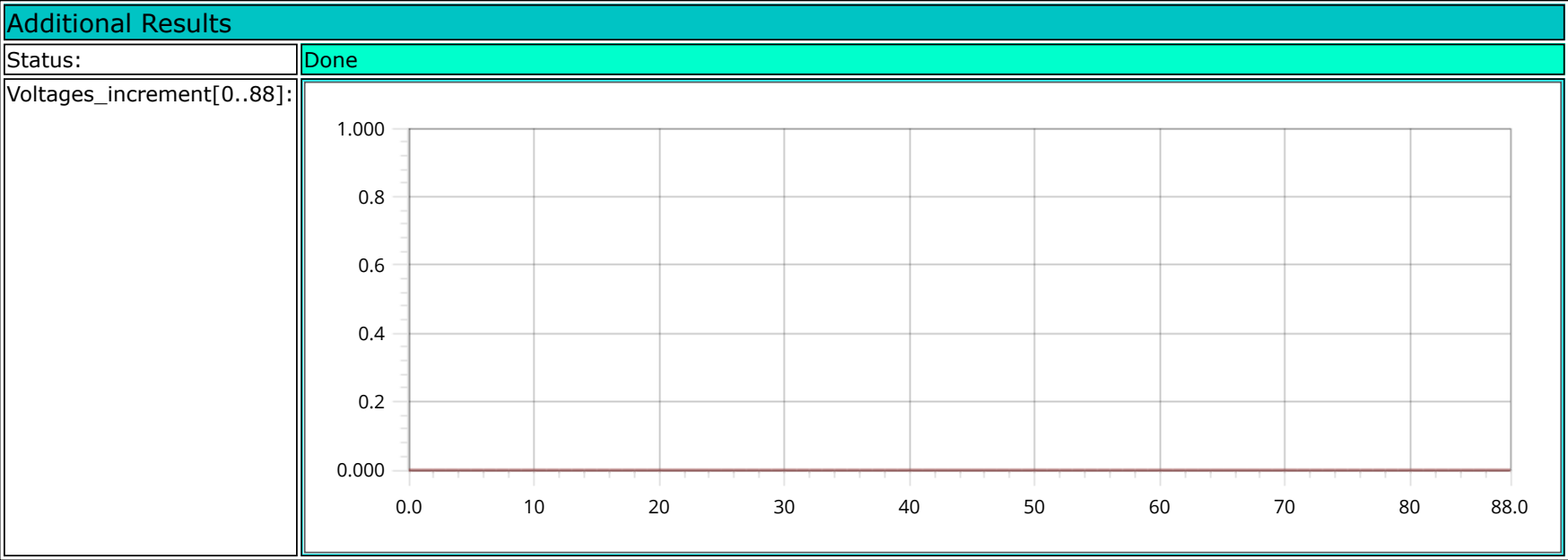
Pass/Fail Test

Status: Passed

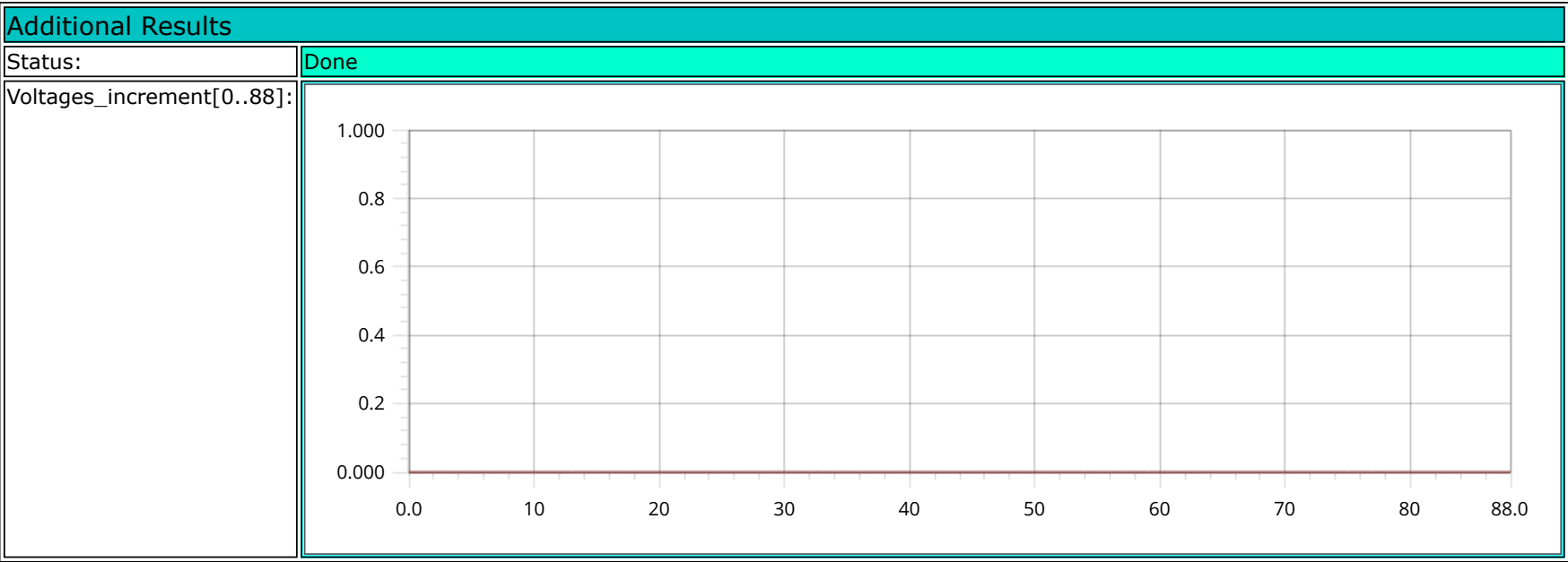


Pass/Fail Test

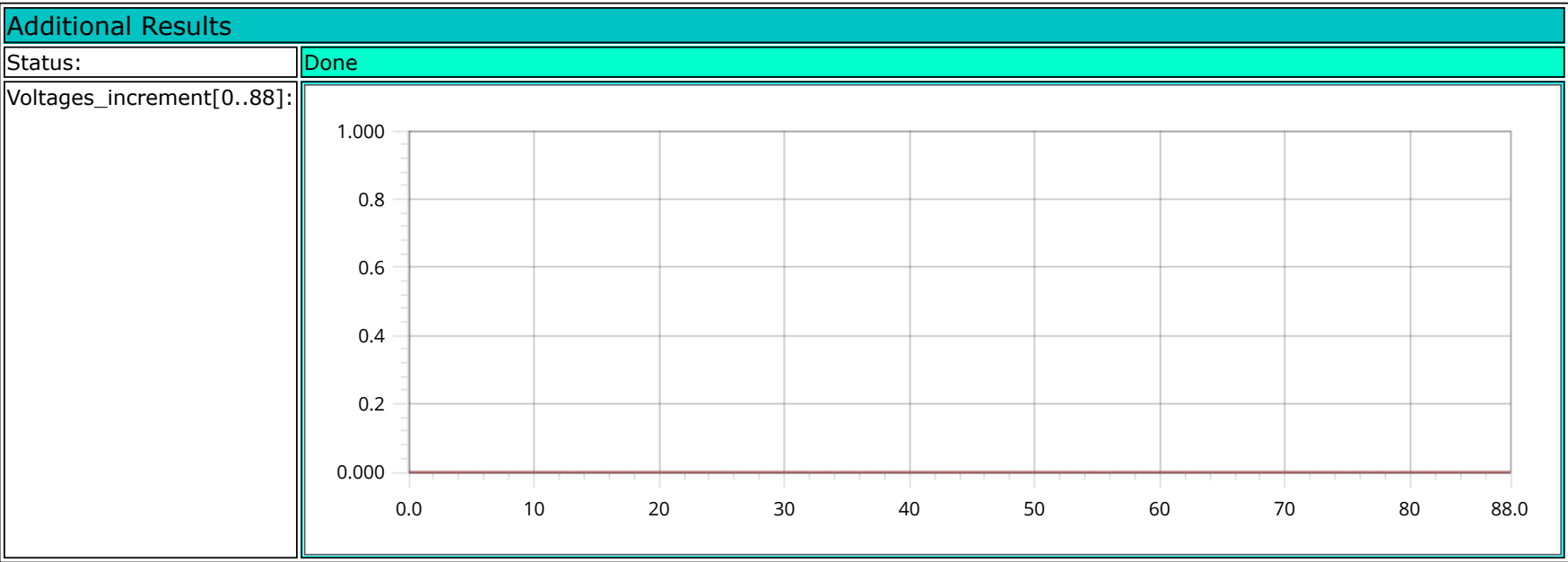
Status: Passed



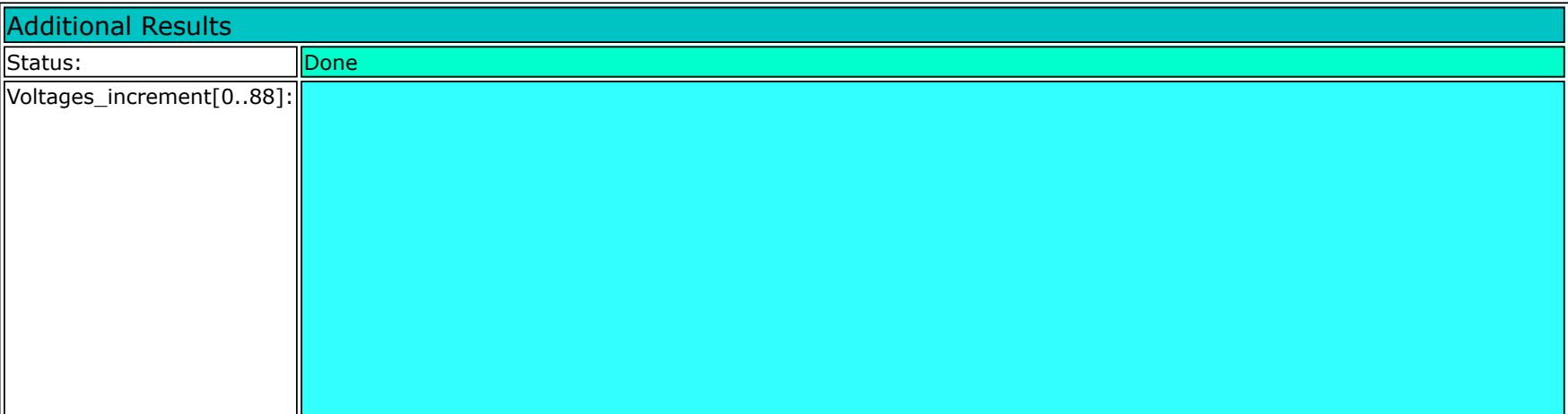
Pass/Fail Test	
Status:	Passed

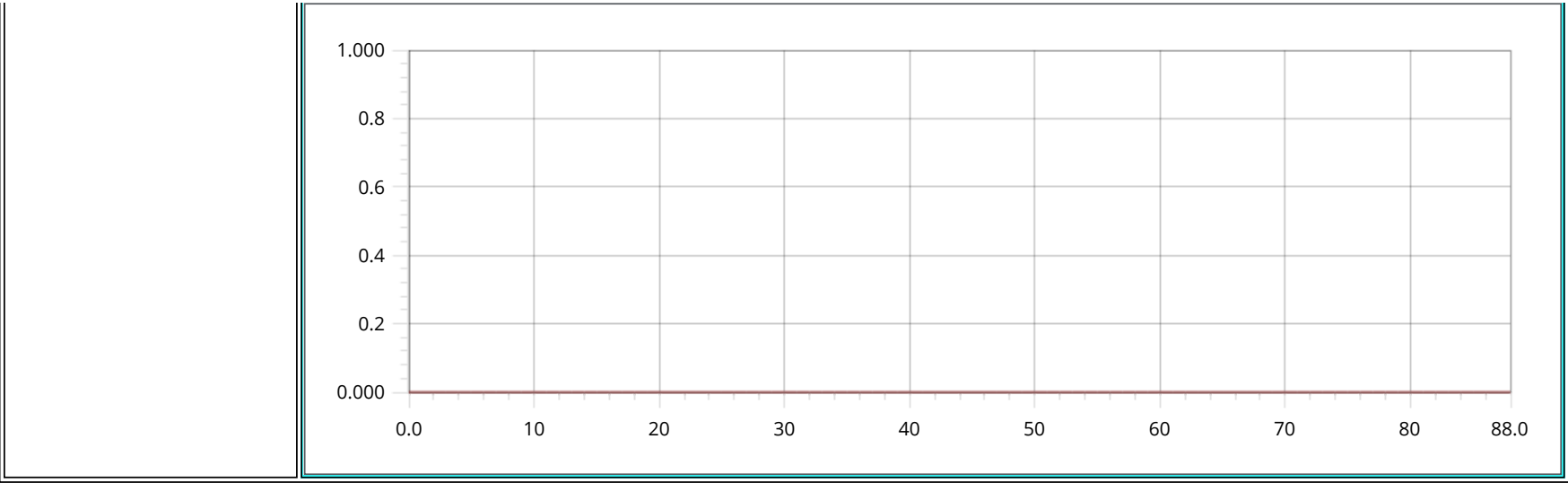


Pass/Fail Test	
Status:	Passed

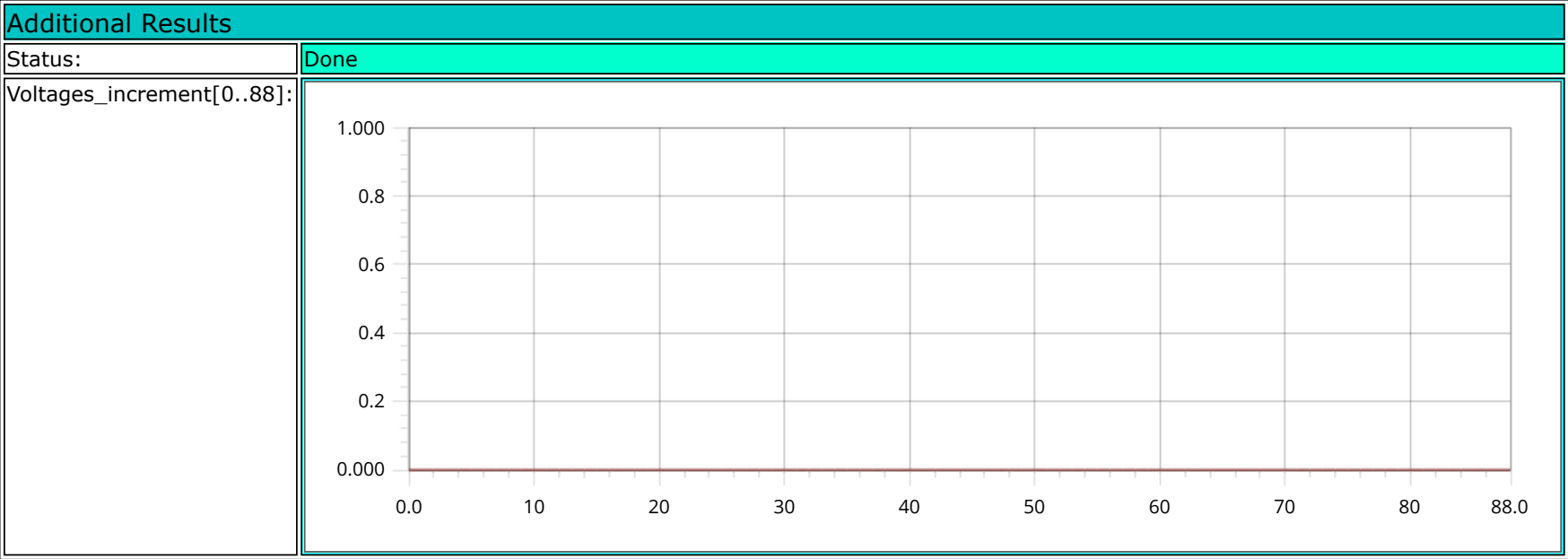


Pass/Fail Test	
Status:	Passed

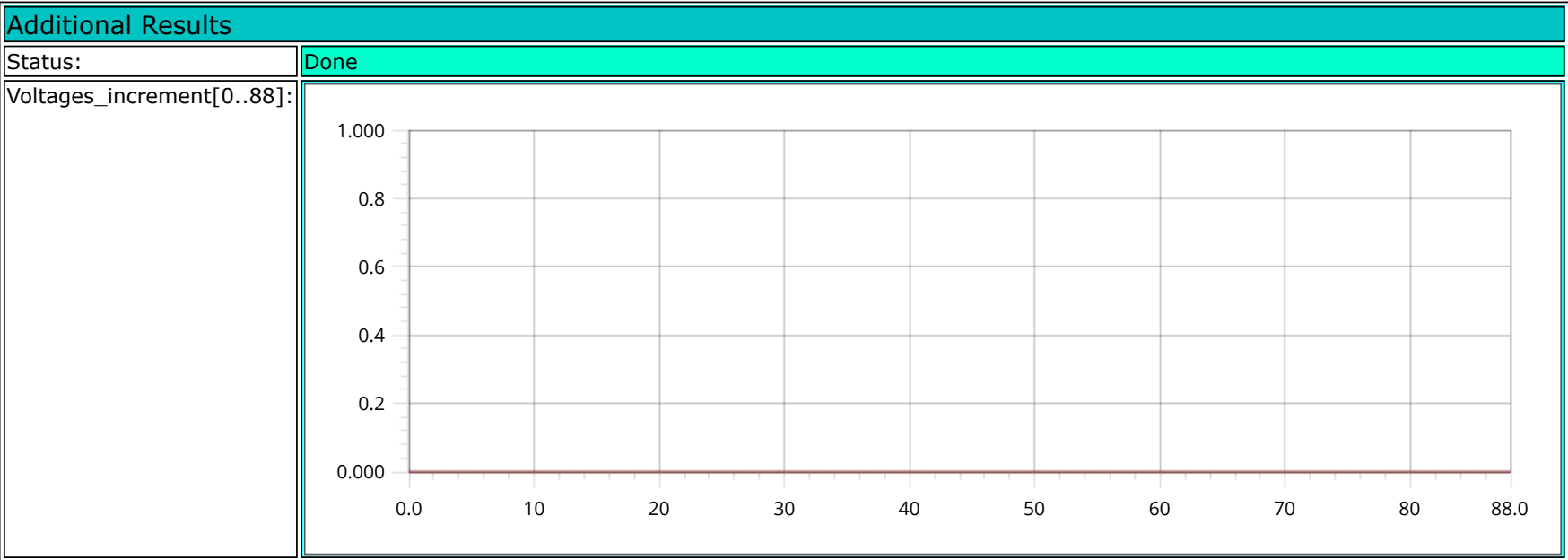




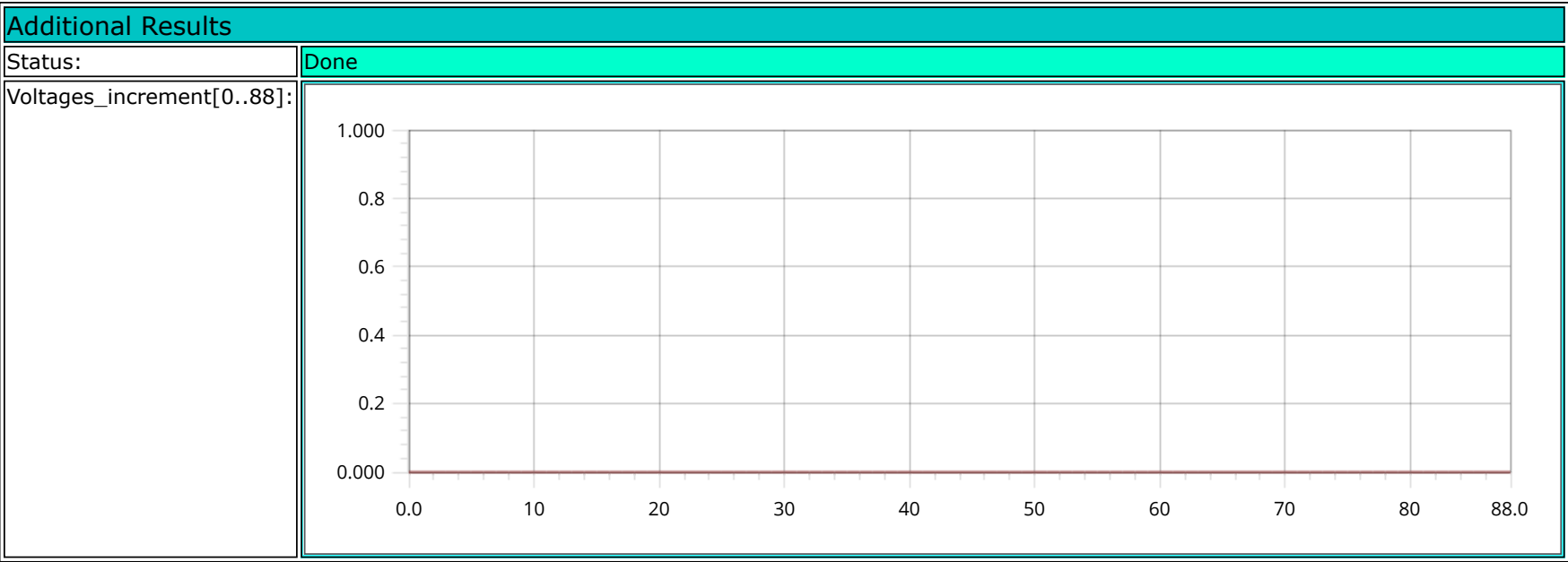
Pass/Fail Test	
Status:	Passed



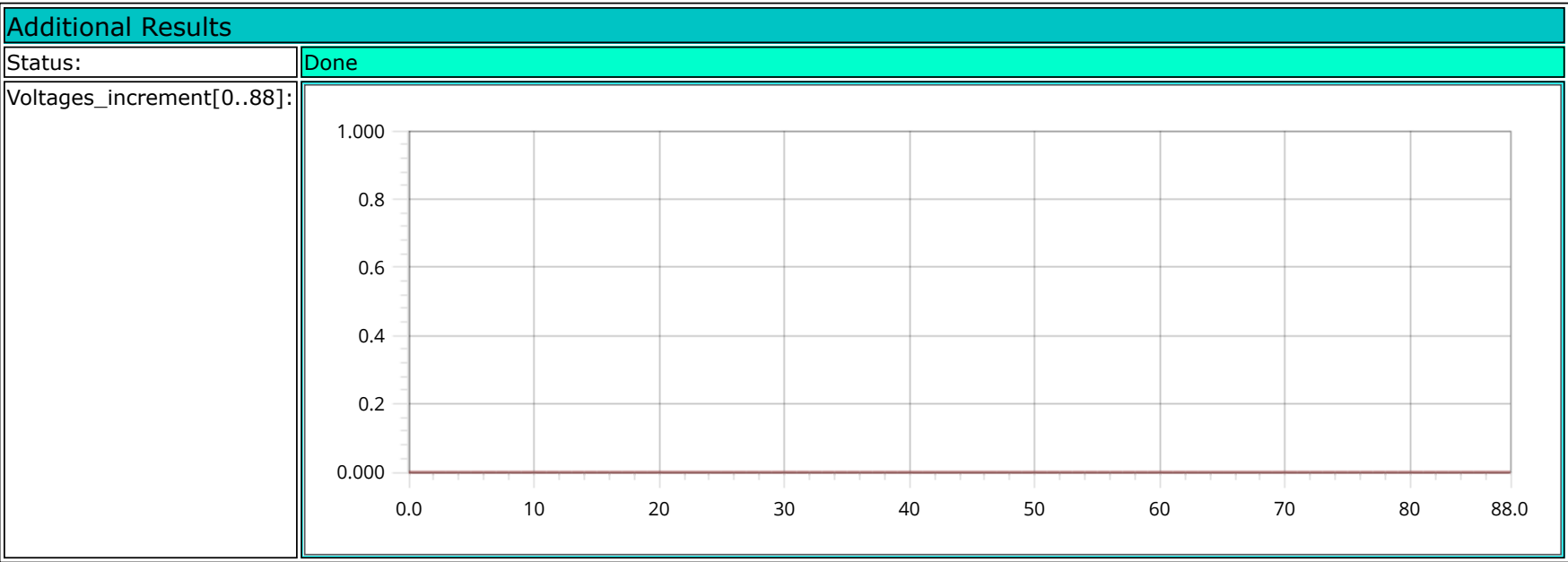
Pass/Fail Test	
Status:	Passed



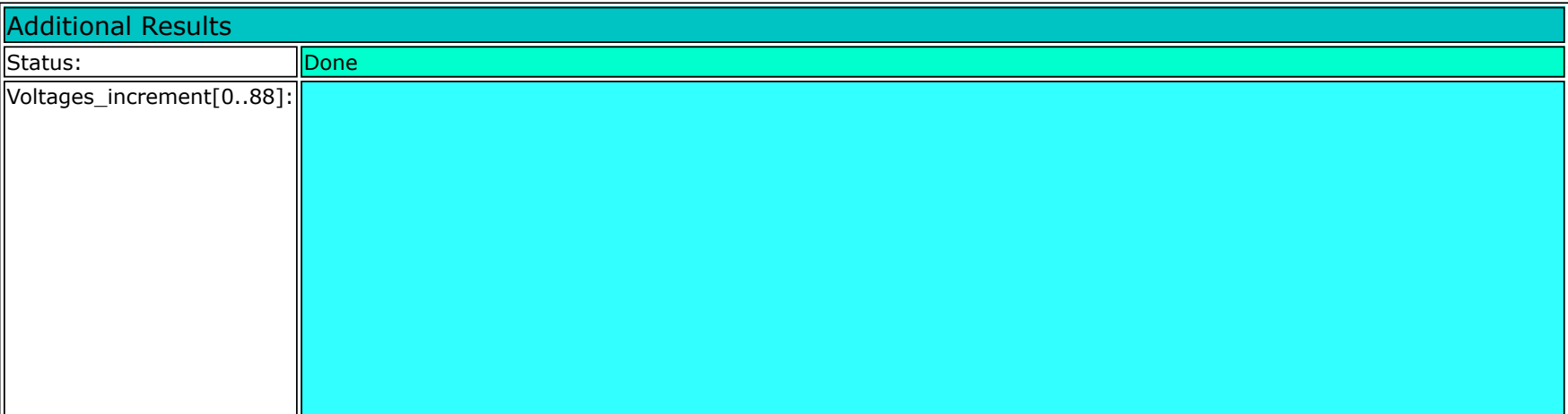
Pass/Fail Test	
Status:	Passed

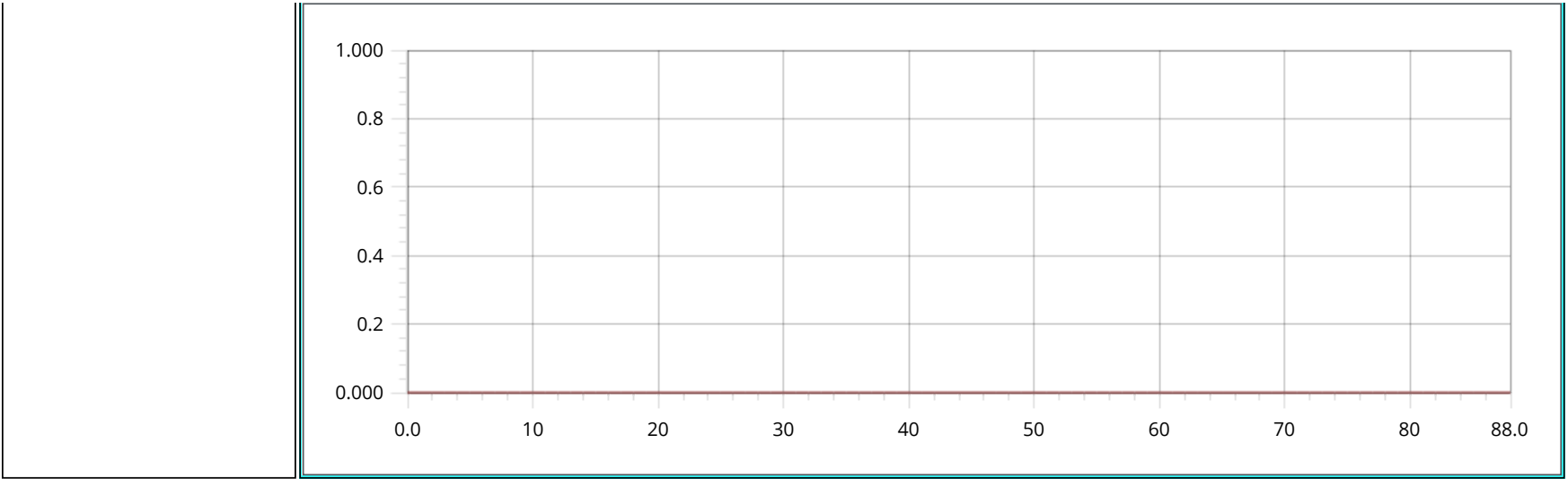


Pass/Fail Test	
Status:	Passed

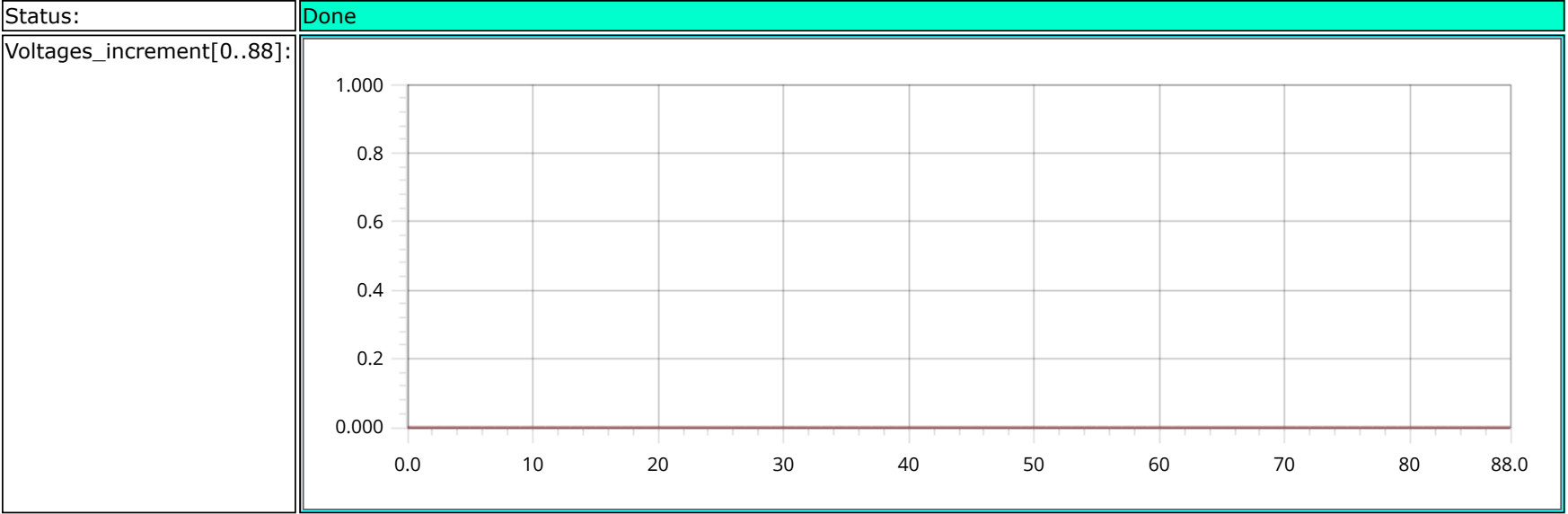


Pass/Fail Test	
Status:	Passed

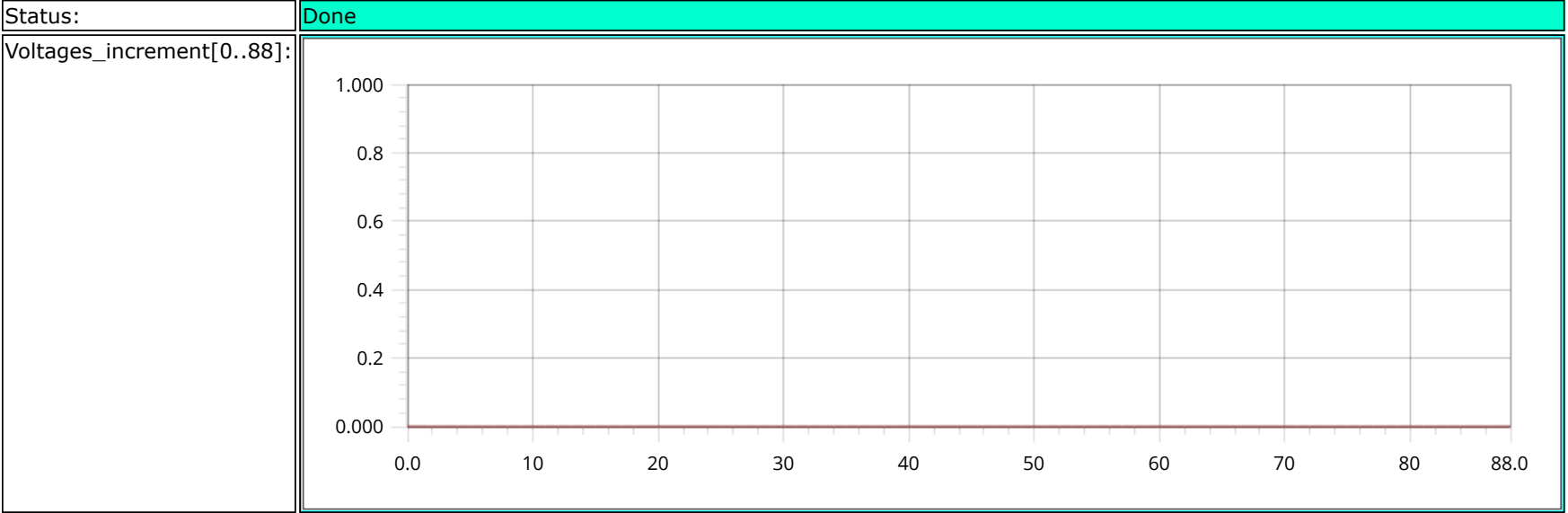




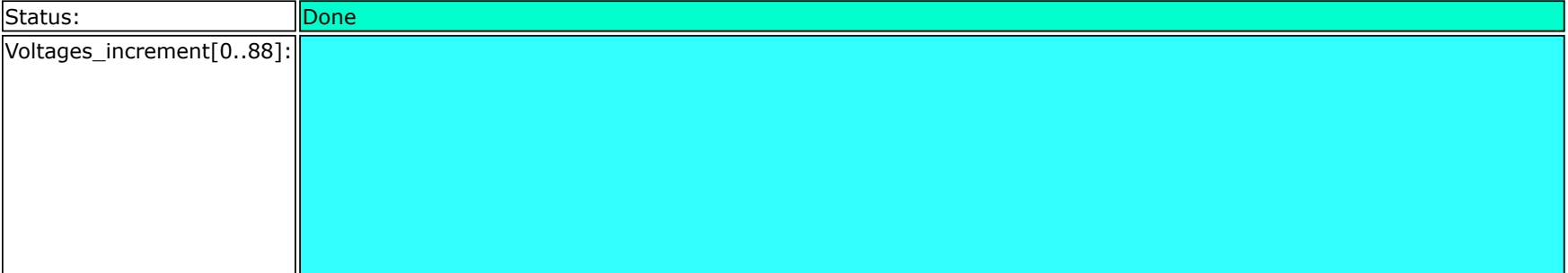
Additional Results

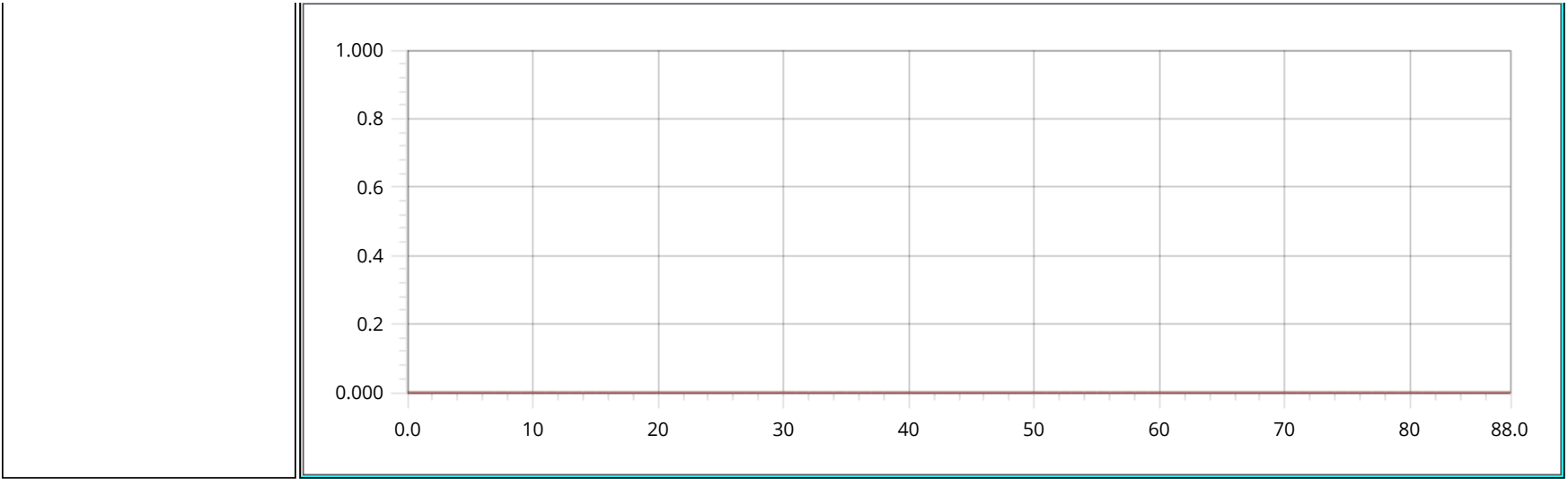


Additional Results

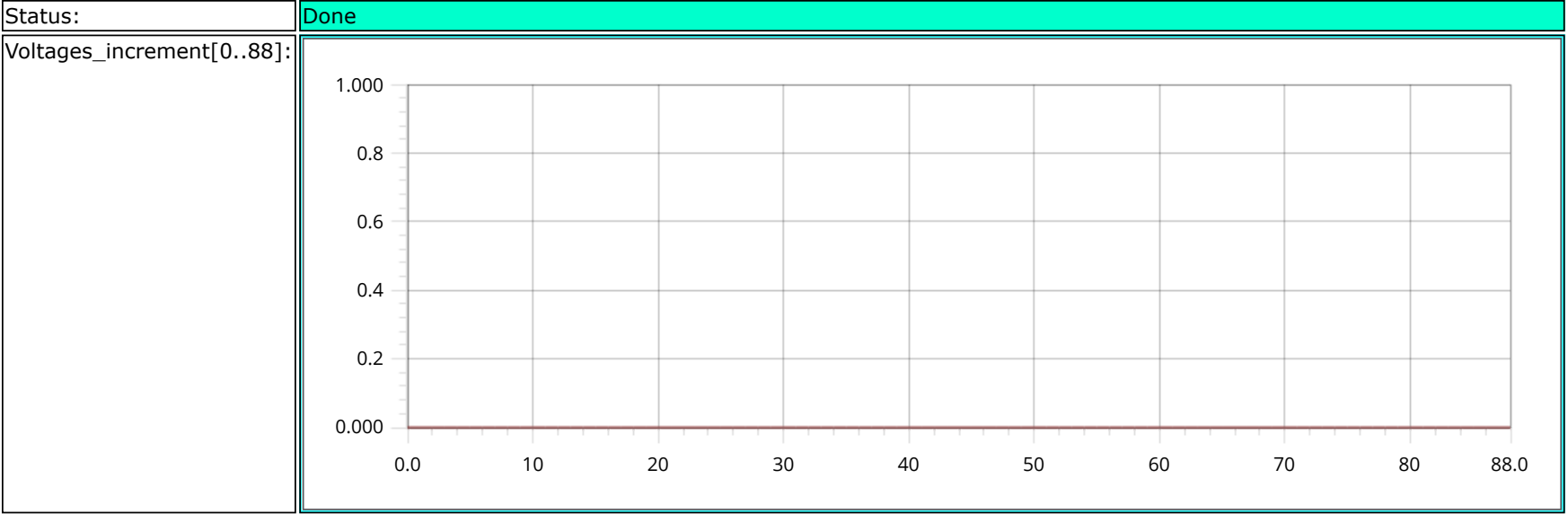


Additional Results

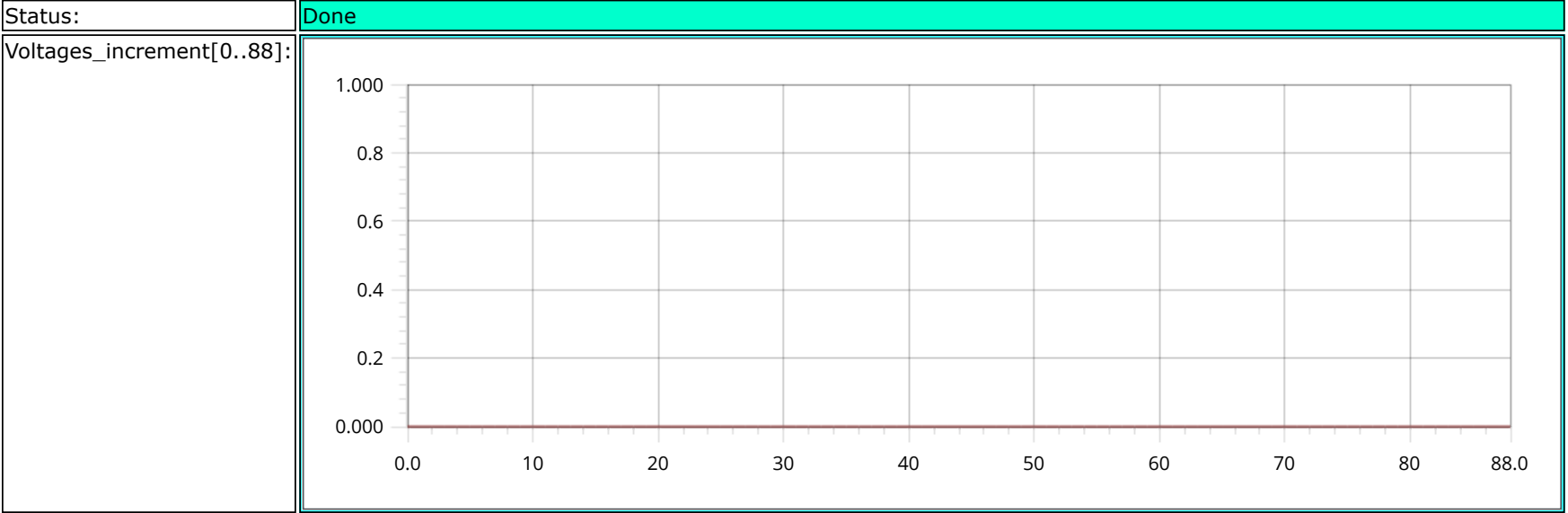




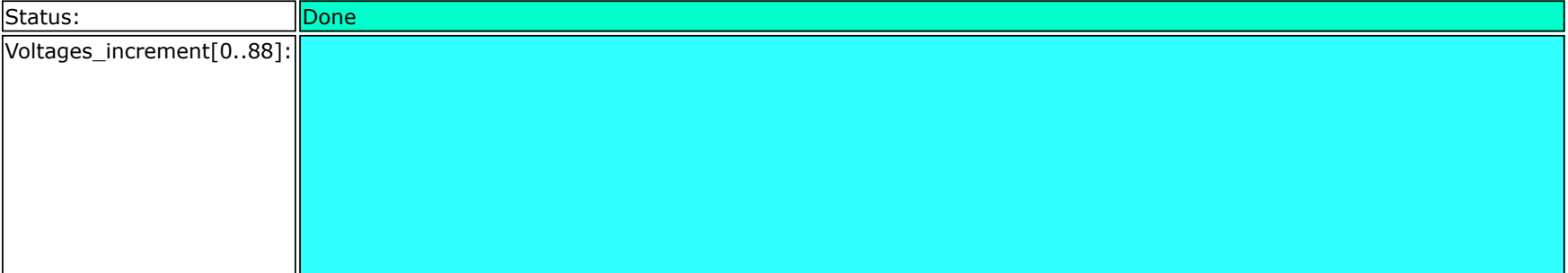
Additional Results

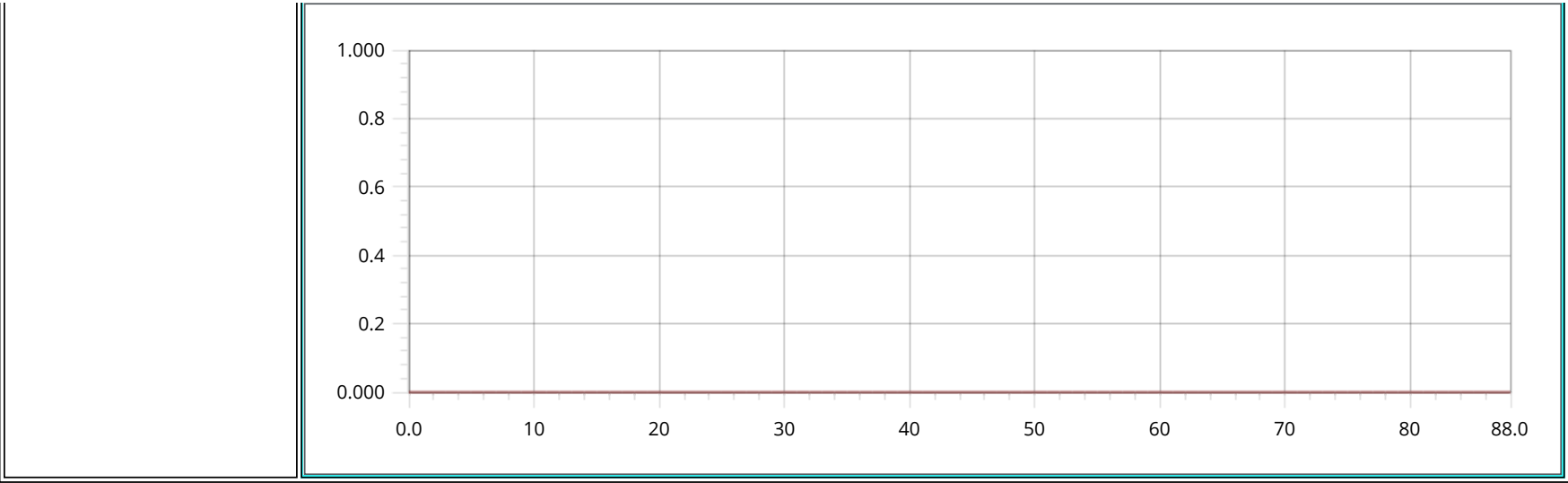


Additional Results



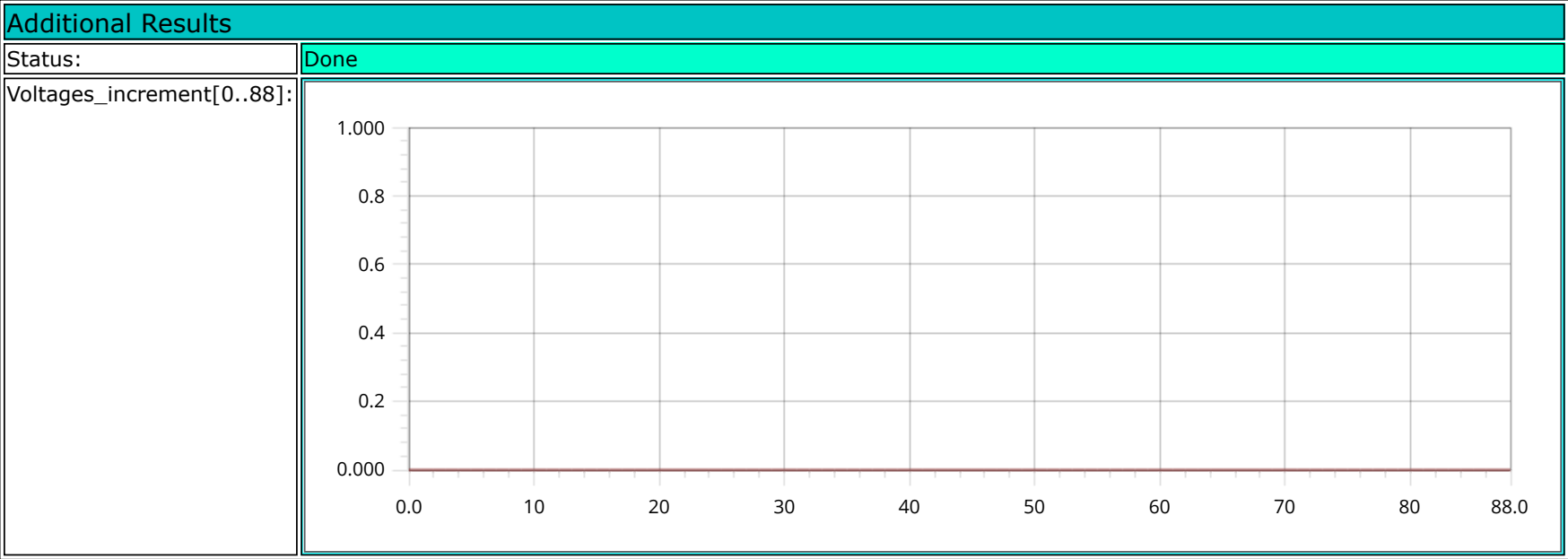
Additional Results





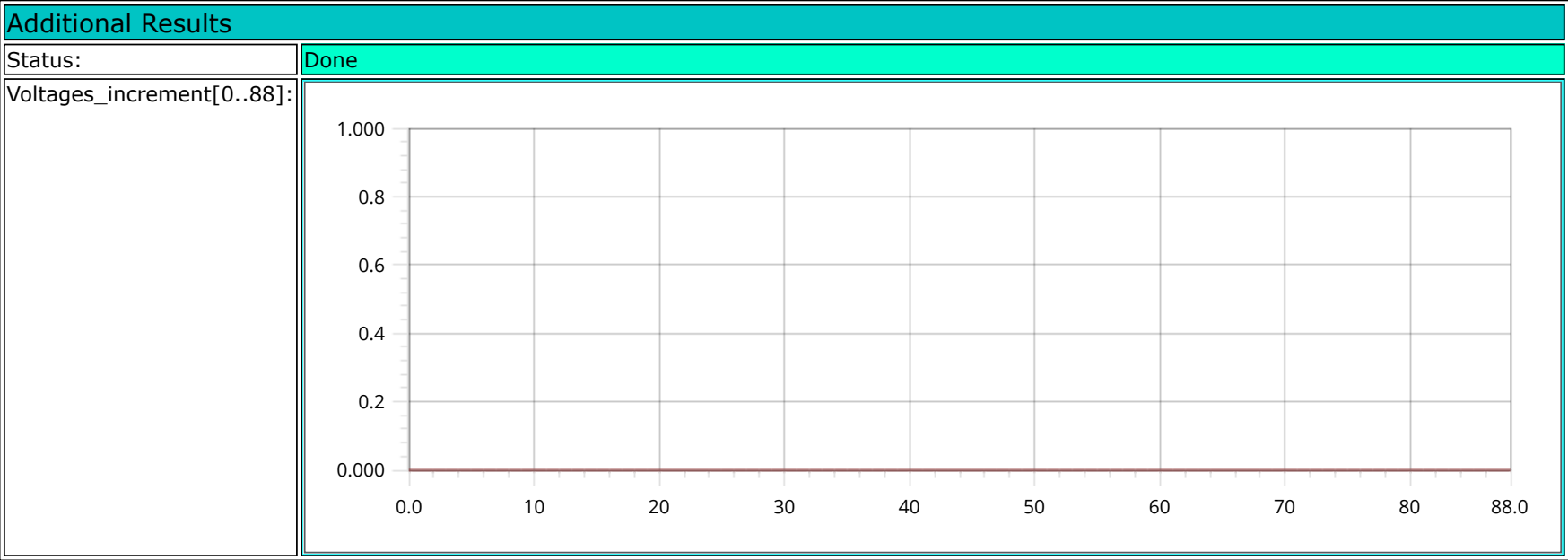
Pass/Fail Test

Status: Passed



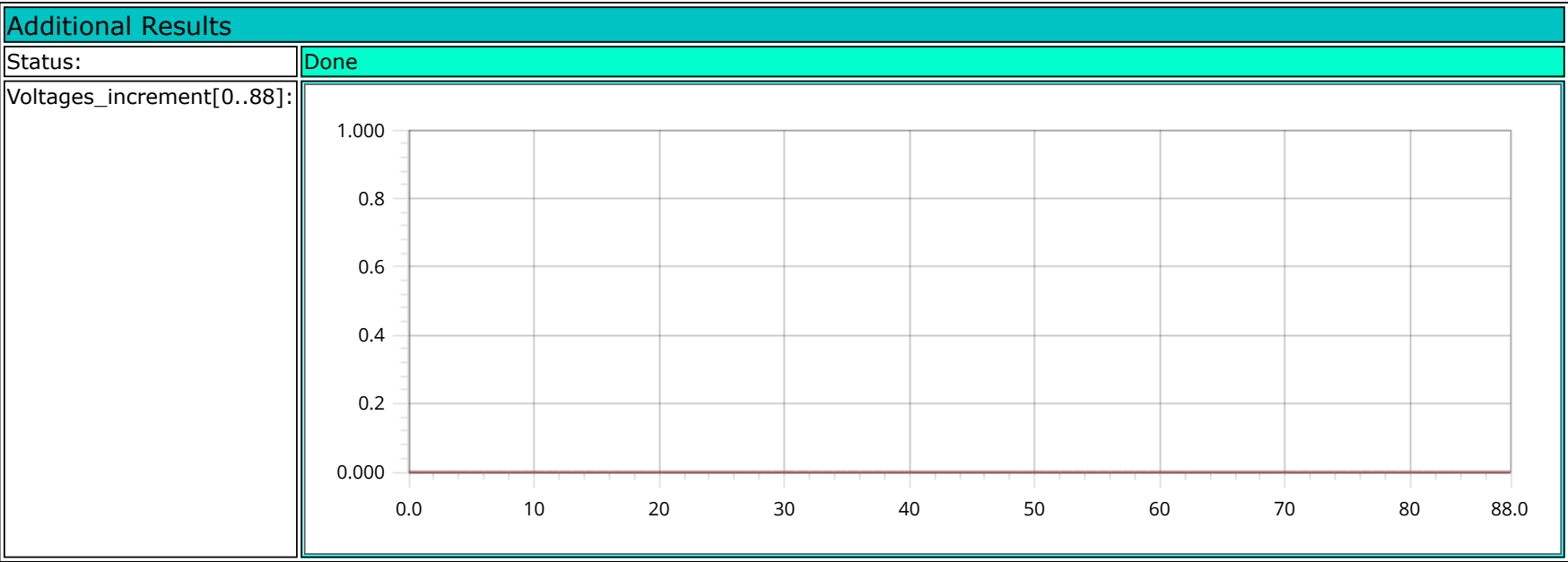
Pass/Fail Test

Status: Passed

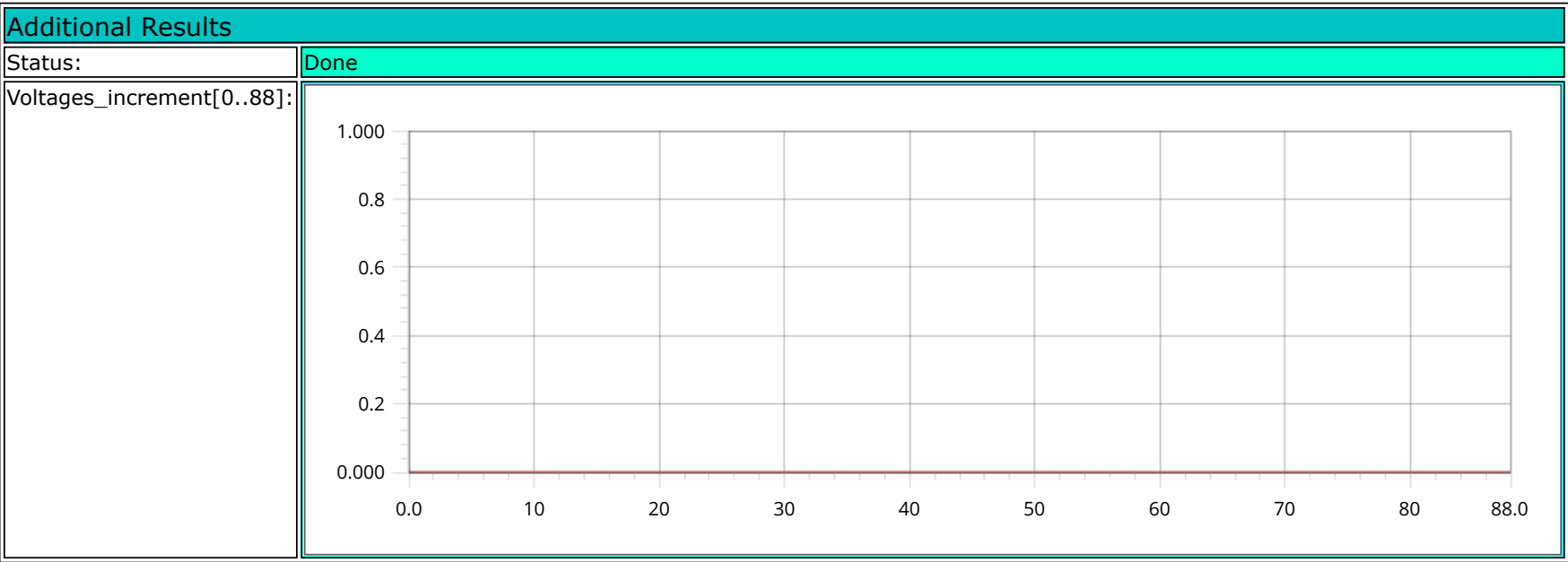




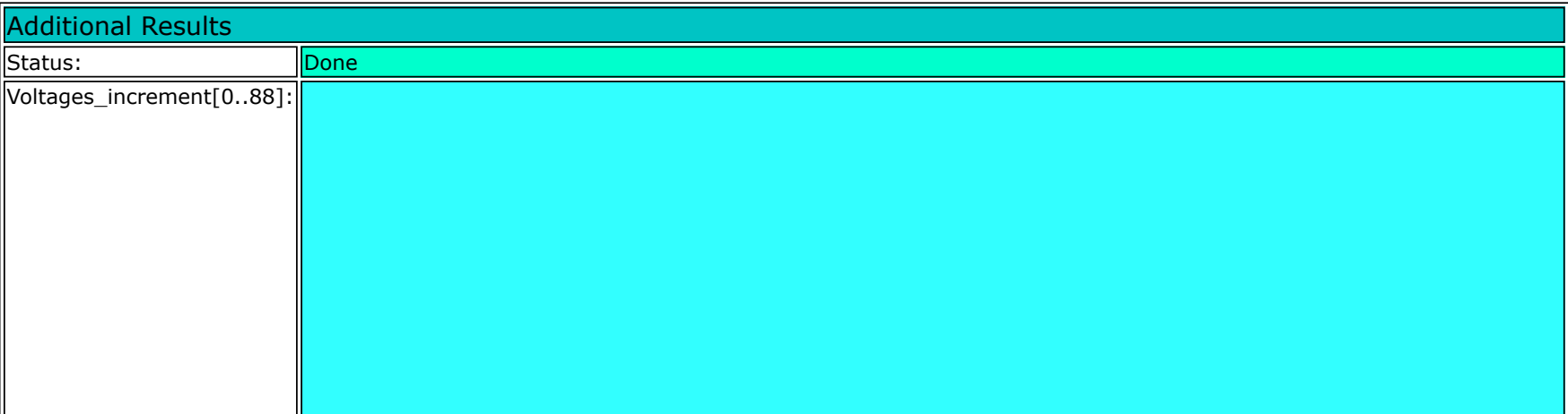
Pass/Fail Test	
Status:	Passed

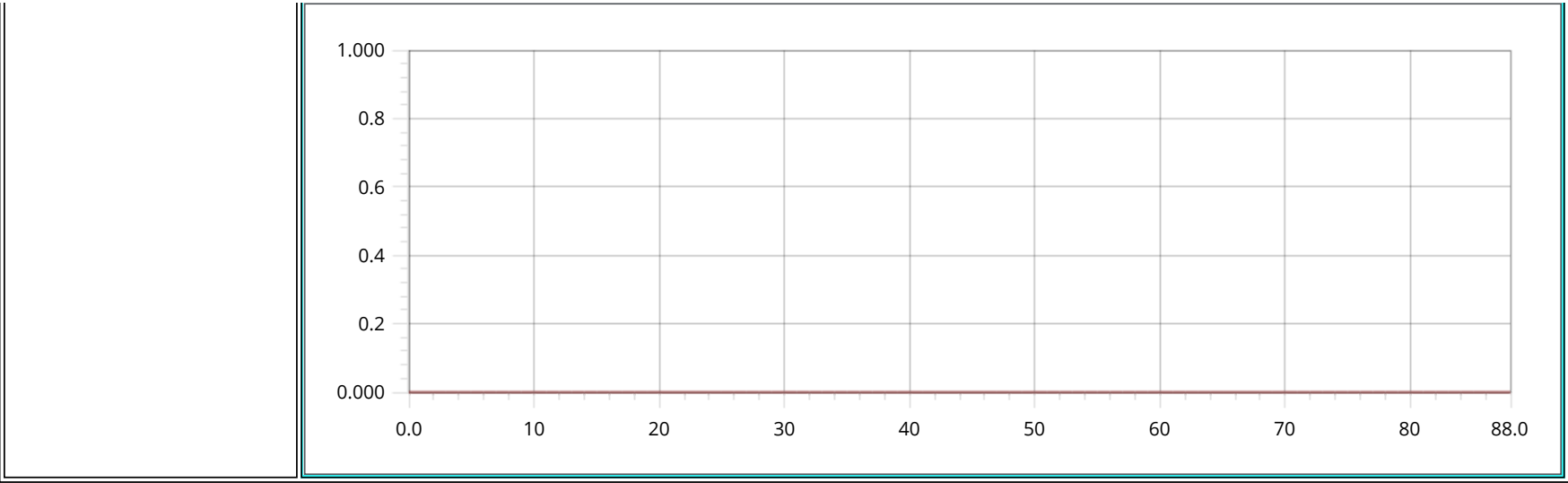


Pass/Fail Test	
Status:	Passed



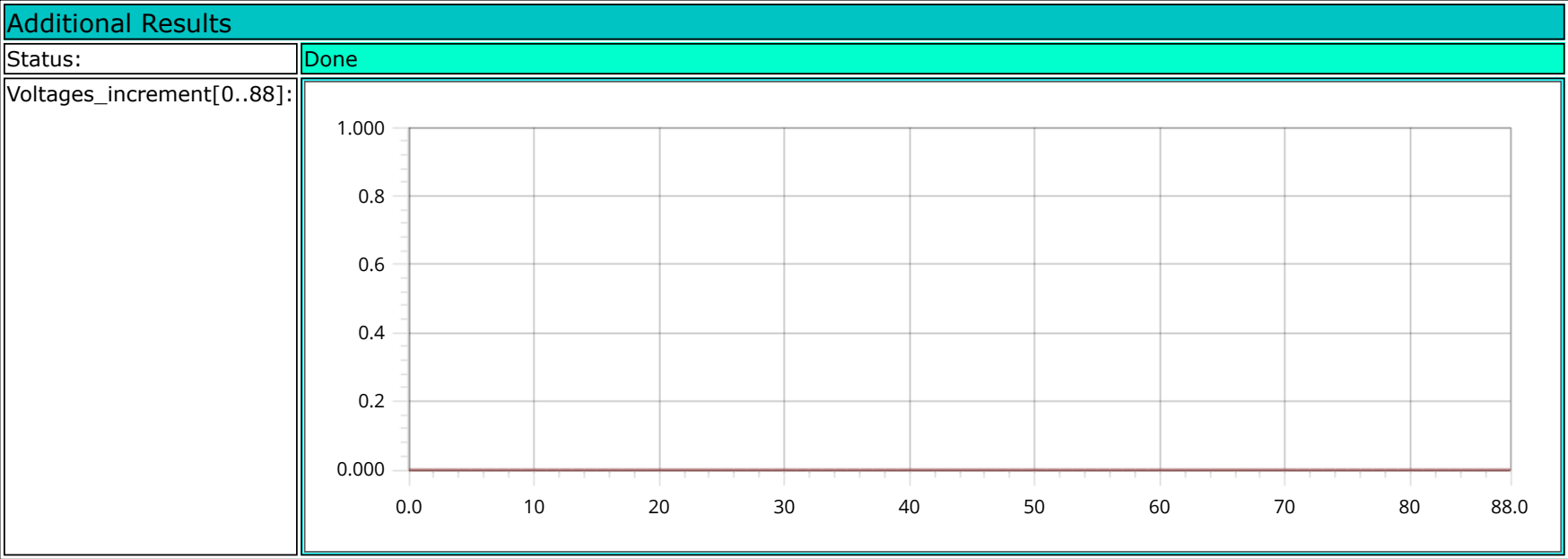
Pass/Fail Test	
Status:	Passed





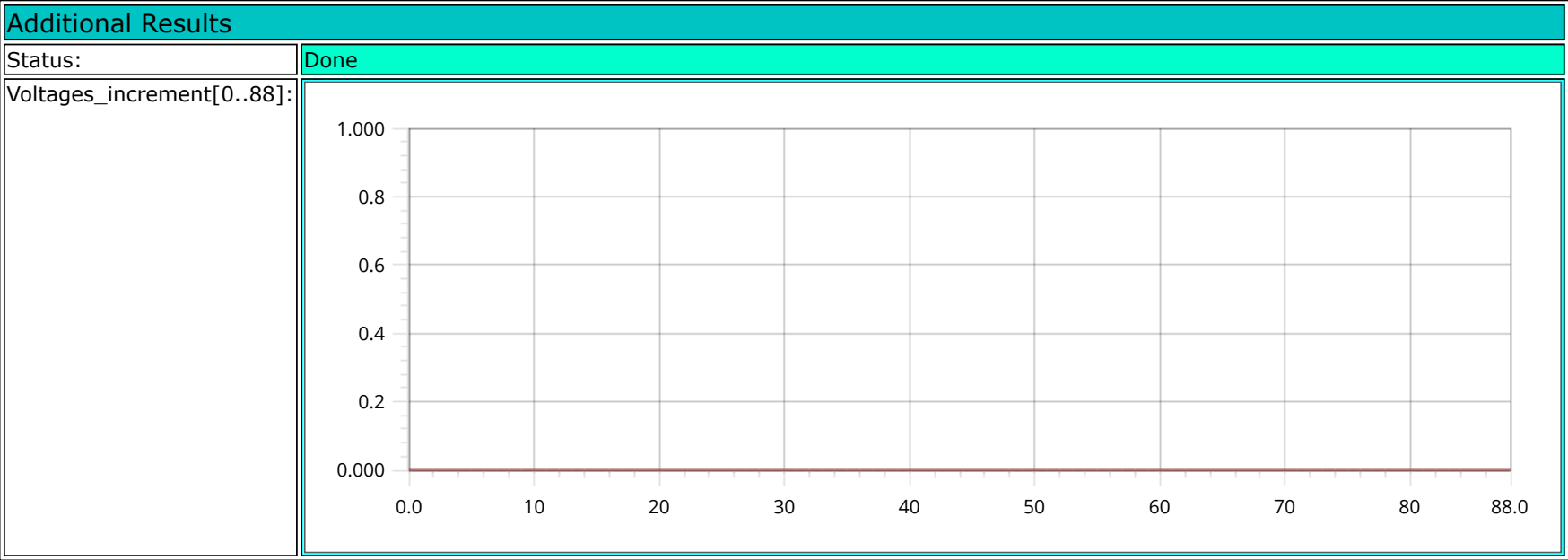
Pass/Fail Test

Status: Passed

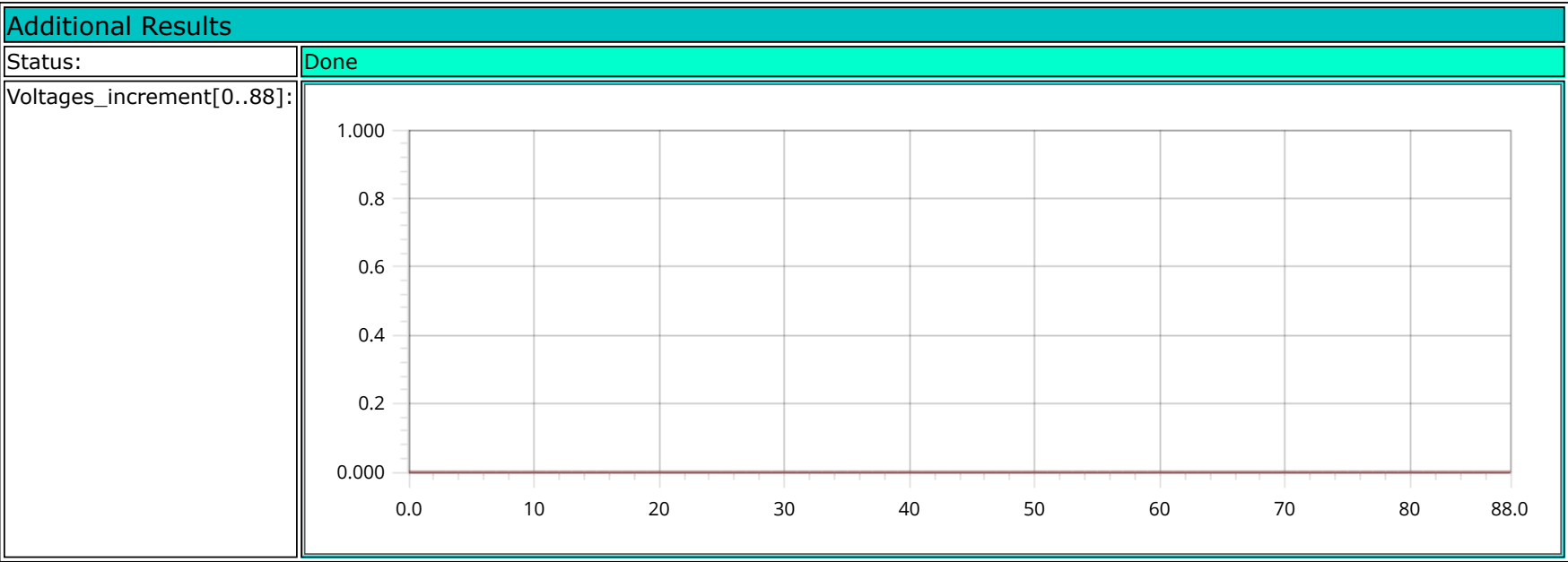


Pass/Fail Test

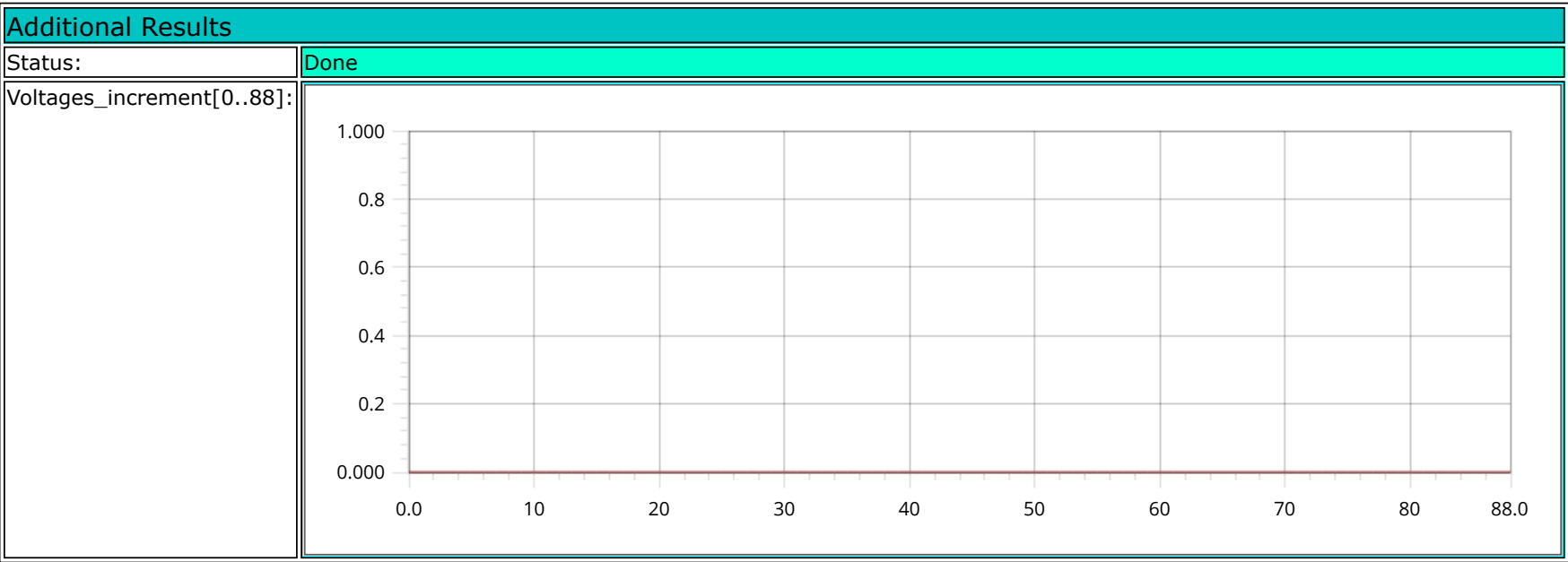
Status: Passed



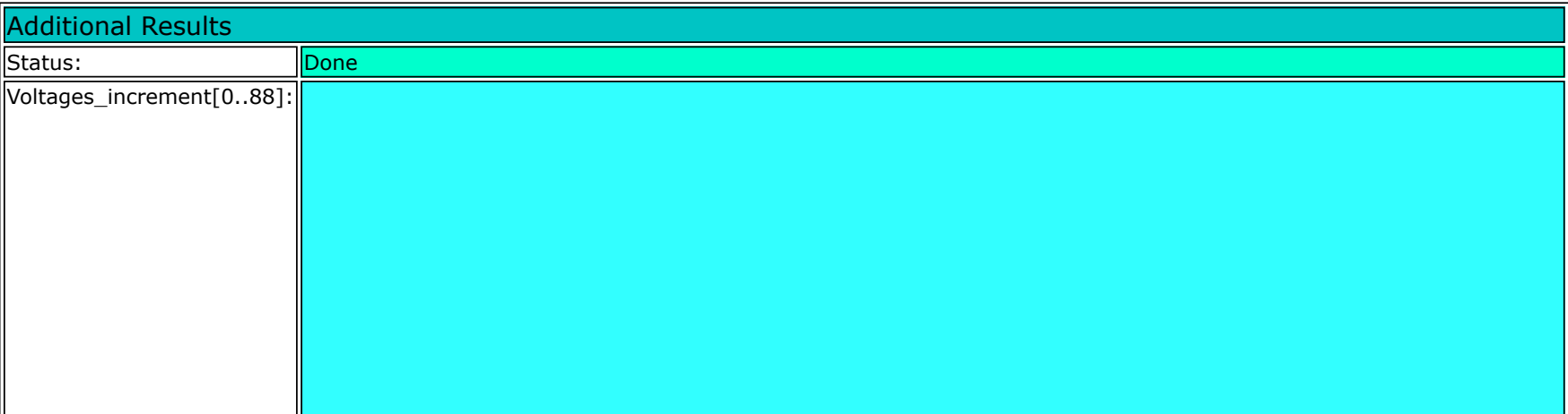
Pass/Fail Test	
Status:	Passed

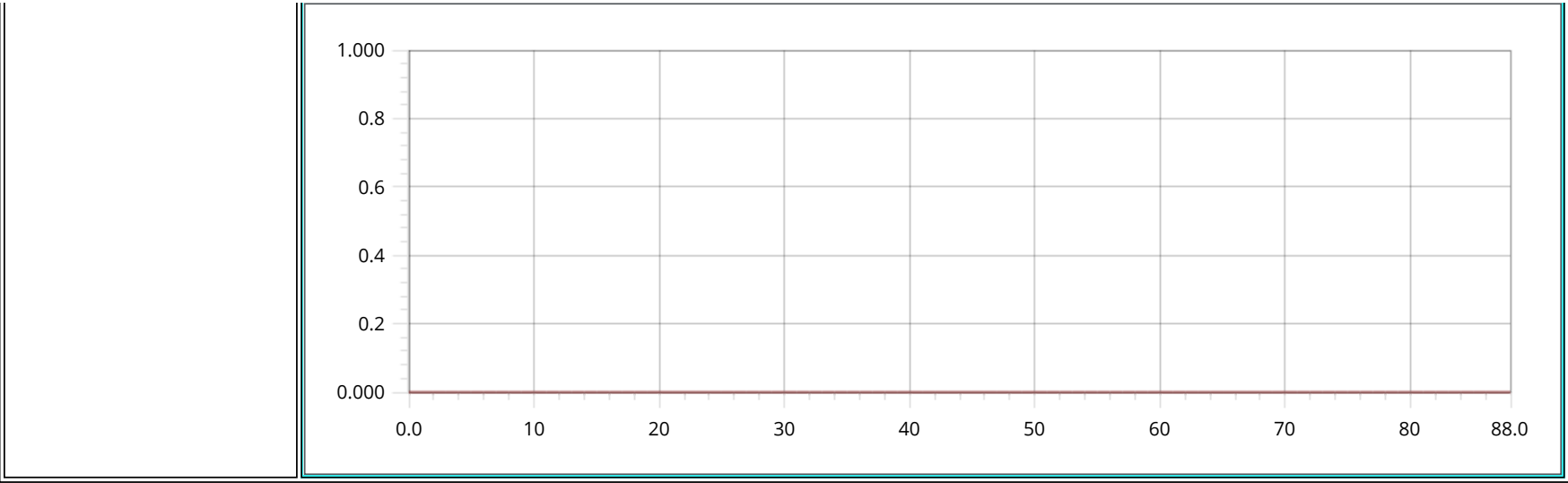


Pass/Fail Test	
Status:	Passed



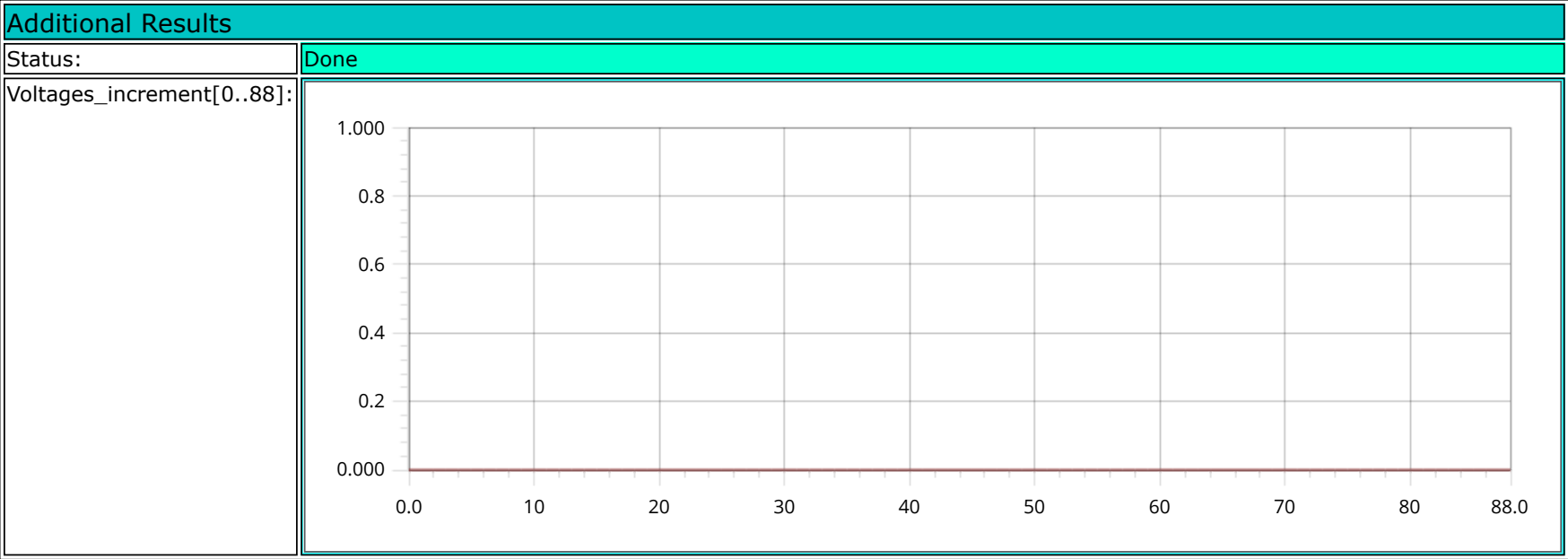
Pass/Fail Test	
Status:	Passed





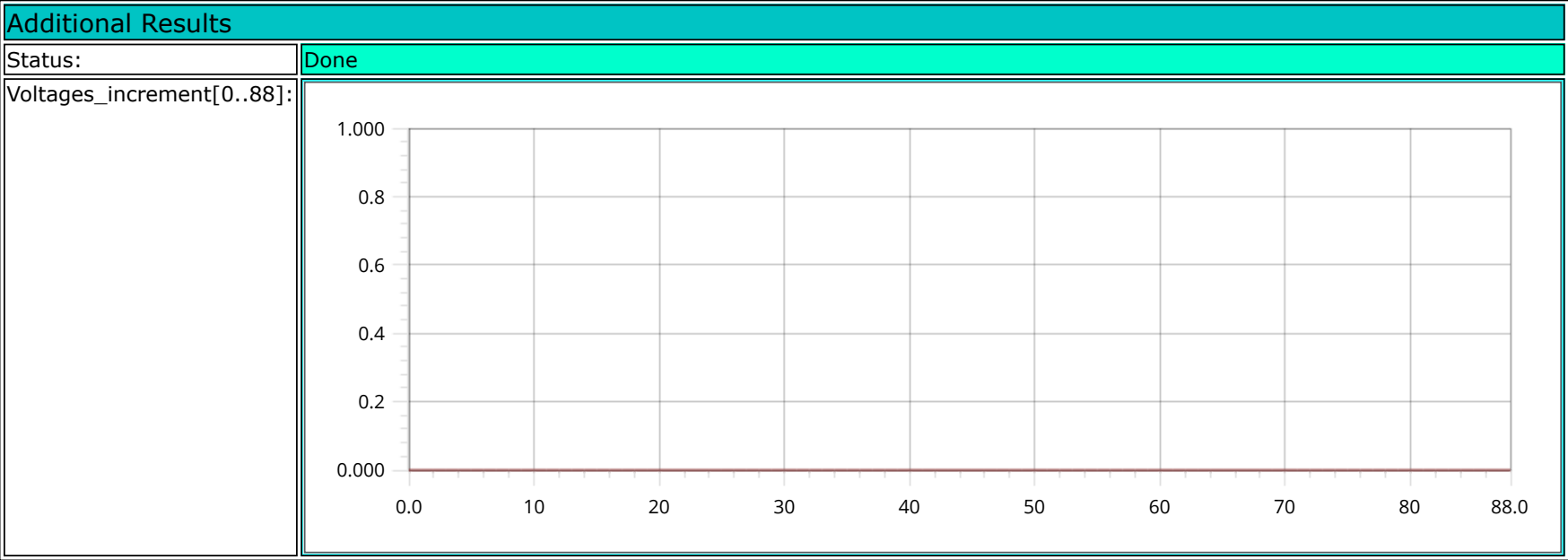
Pass/Fail Test

Status: Passed

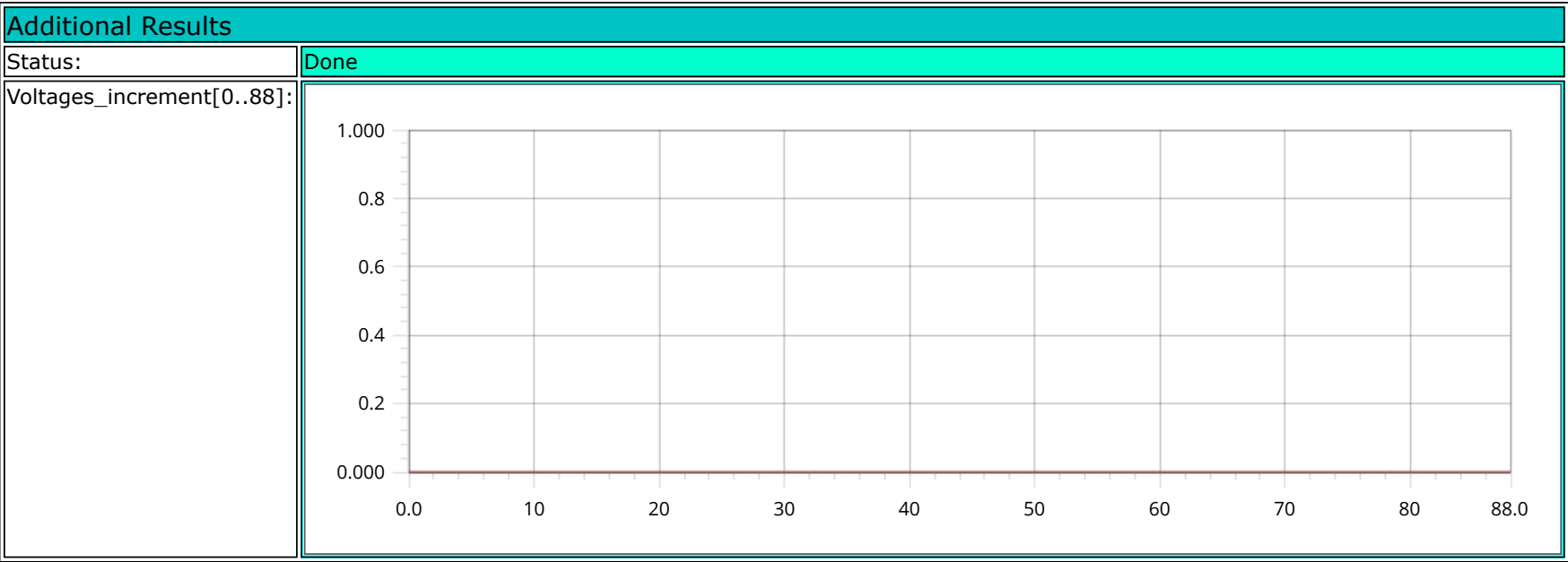


Pass/Fail Test

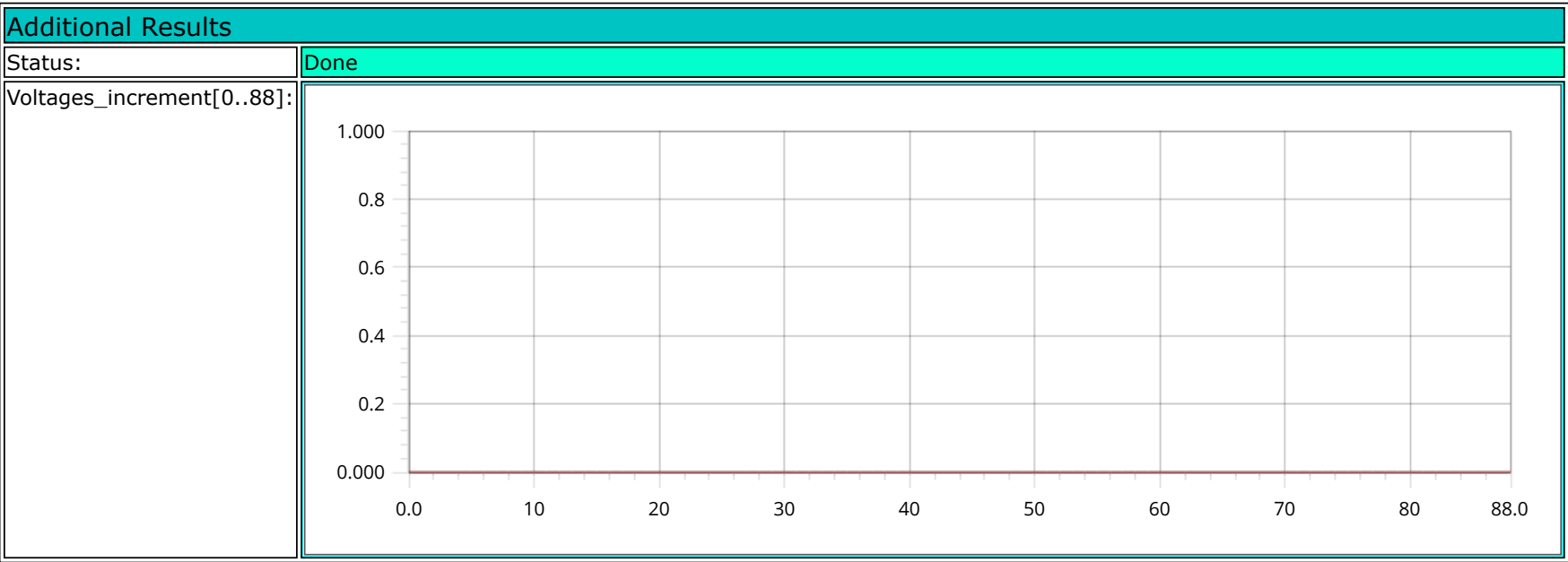
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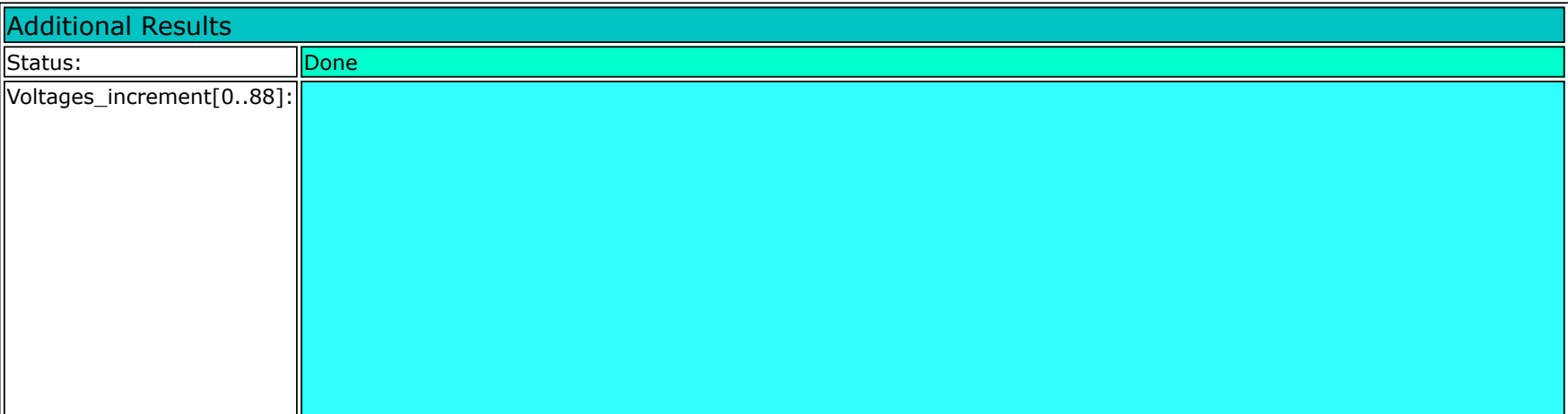
Pass/Fail Test	
Status:	Passed

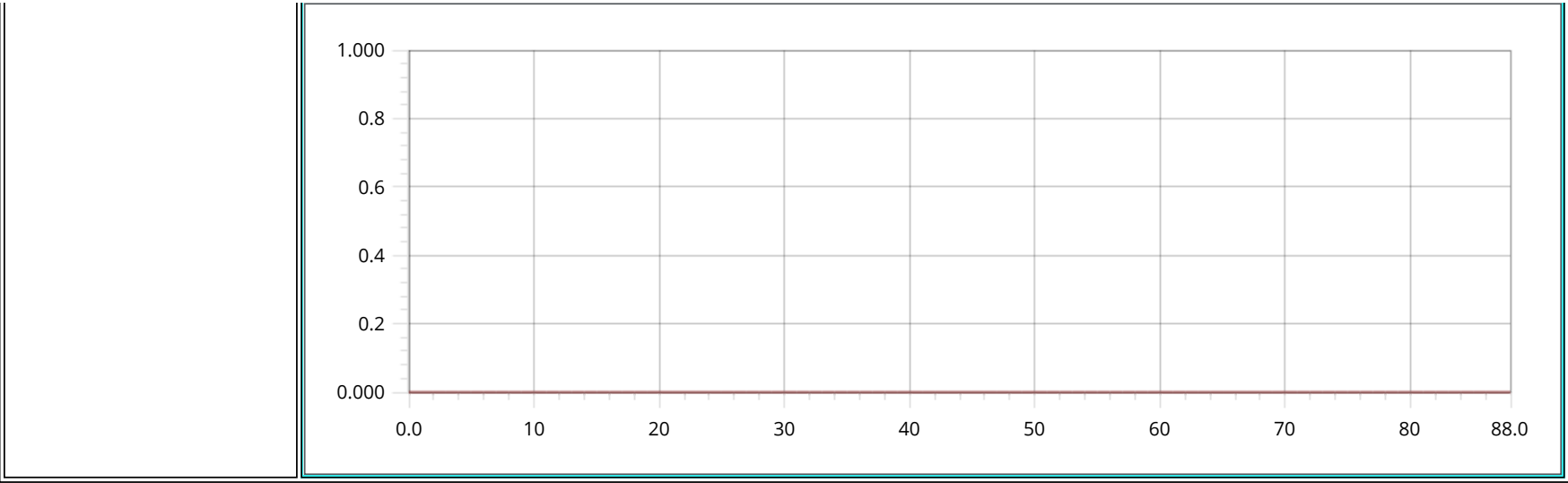


Pass/Fail Test	
Status:	Passed



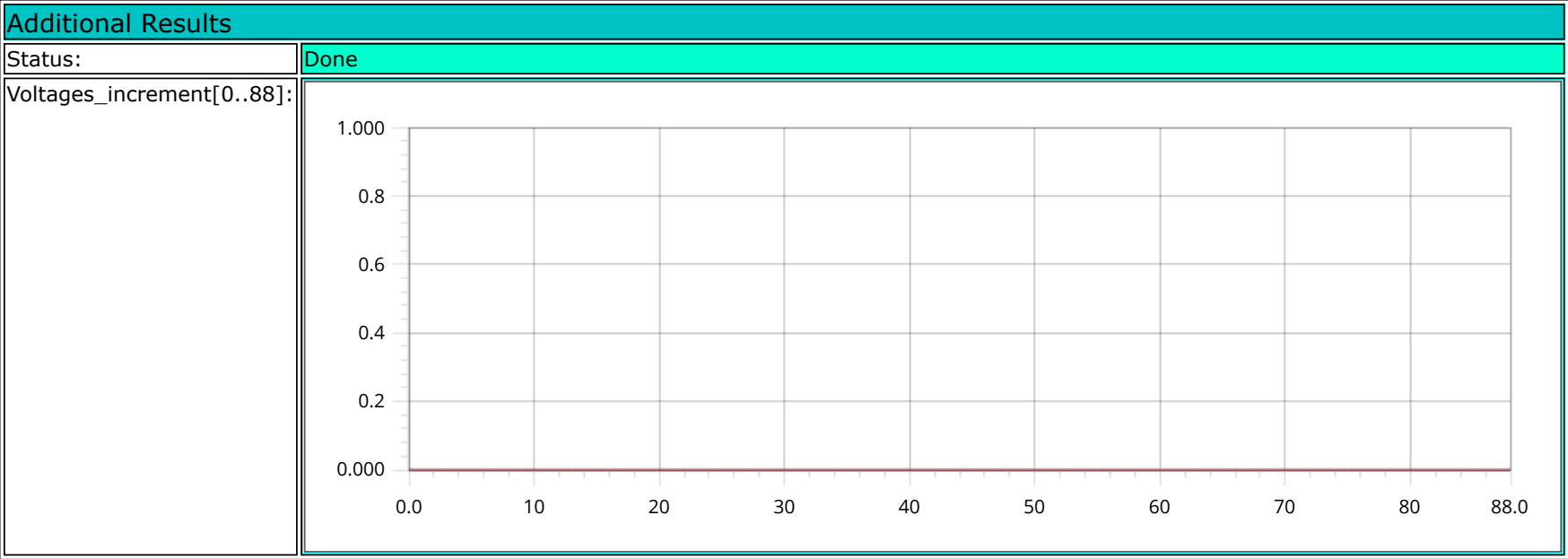
Pass/Fail Test	
Status:	Passed





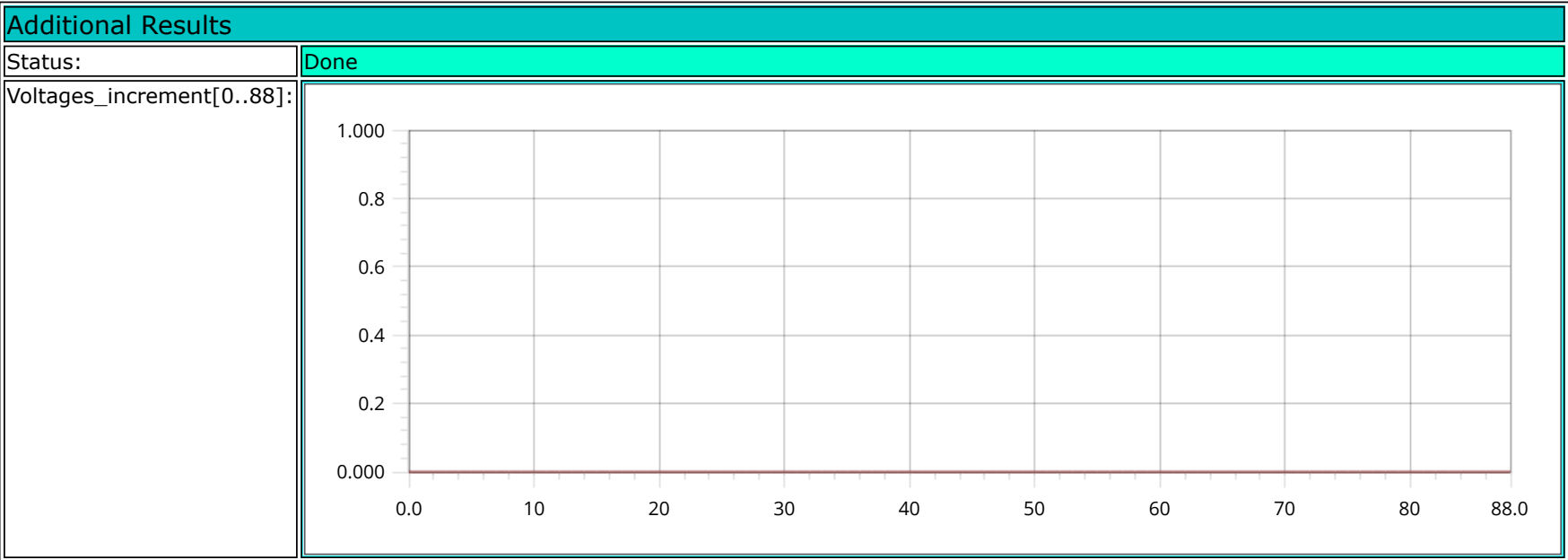
Pass/Fail Test

Status: Passed

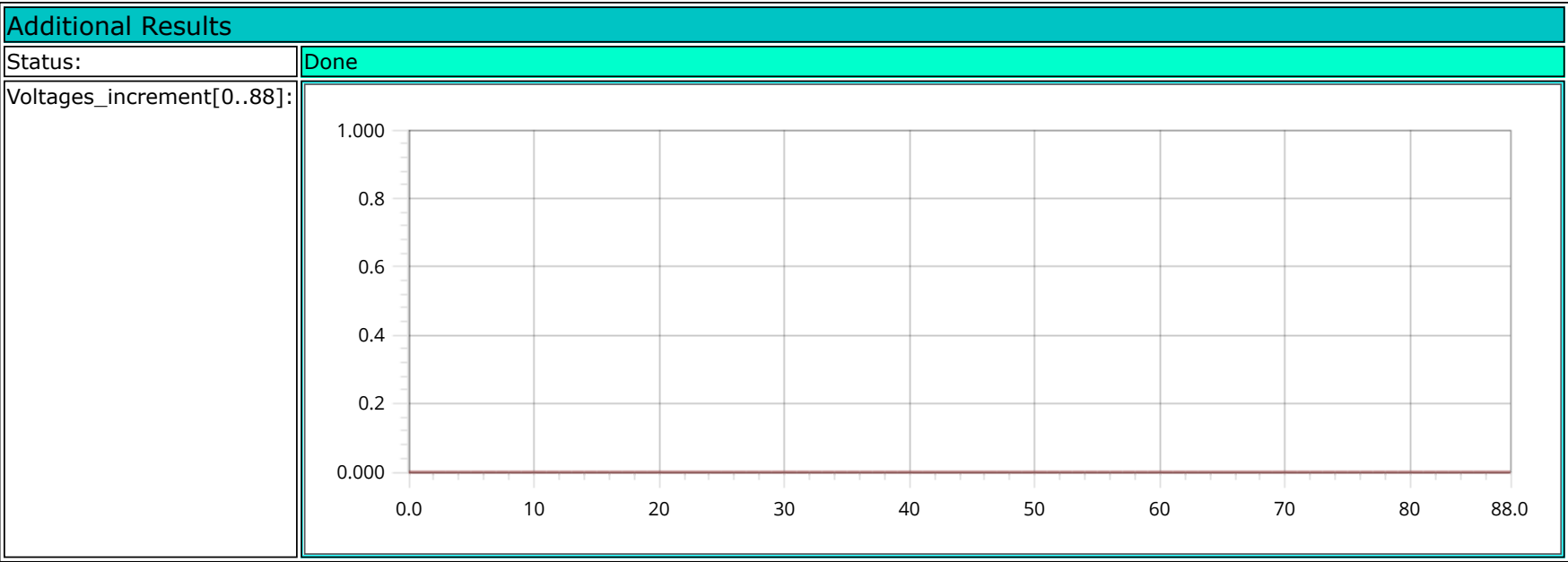


Pass/Fail Test

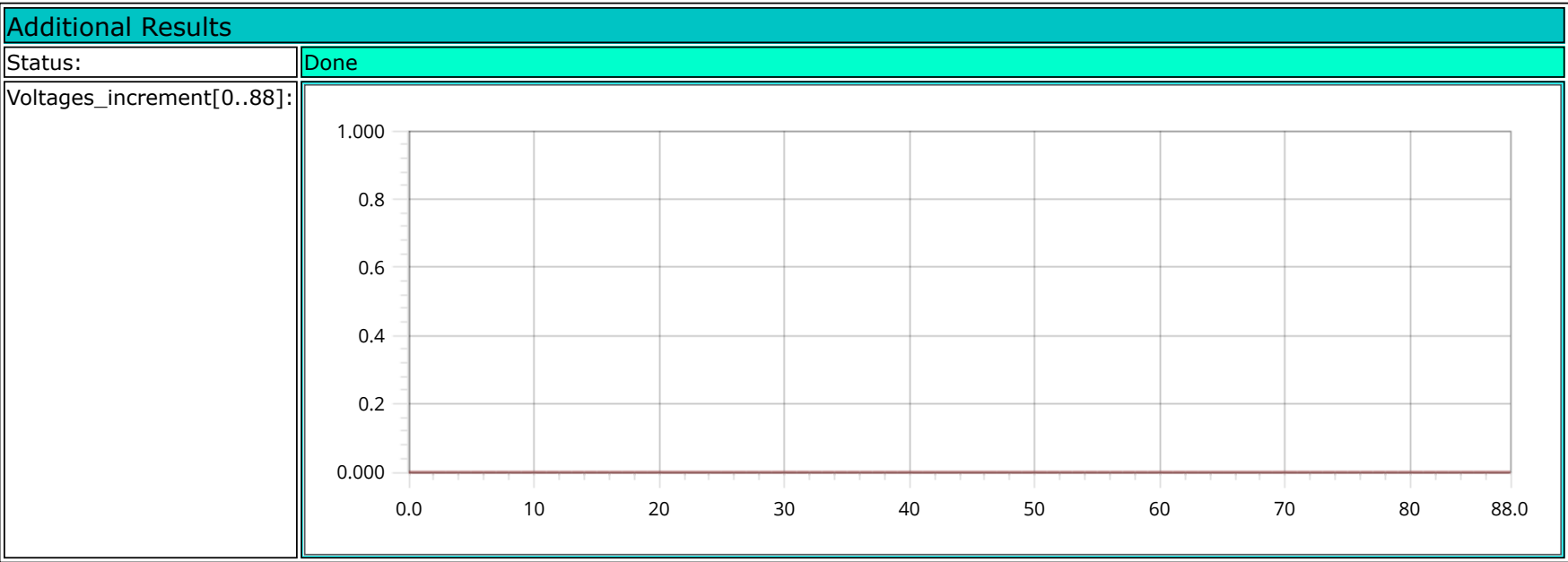
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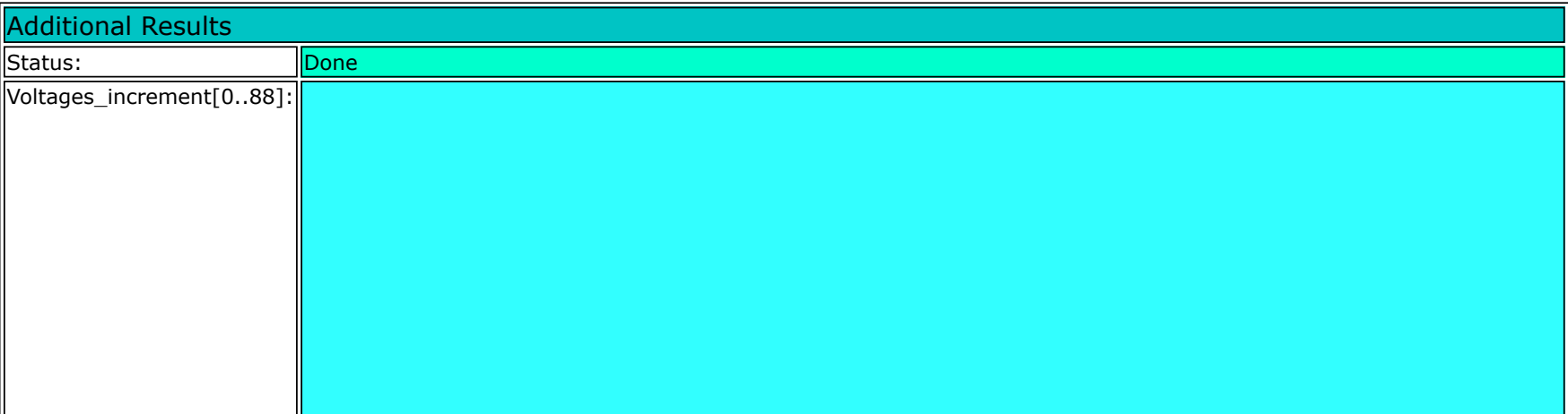
Pass/Fail Test	
Status:	Passed

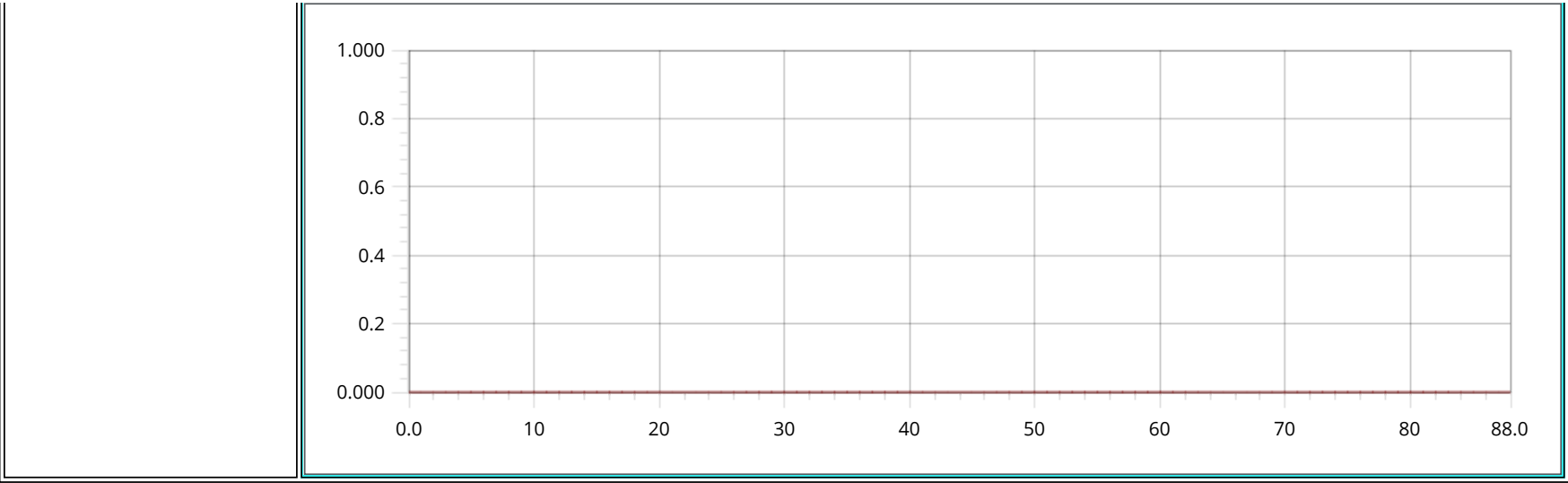


Pass/Fail Test	
Status:	Passed



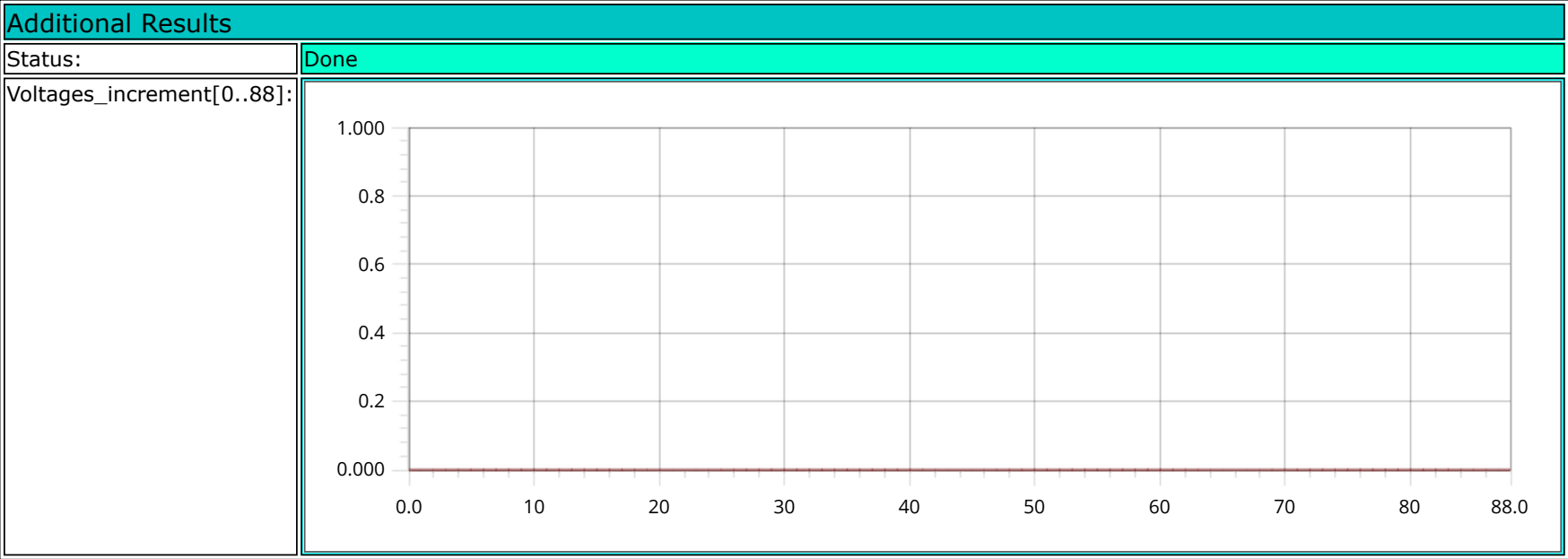
Pass/Fail Test	
Status:	Passed





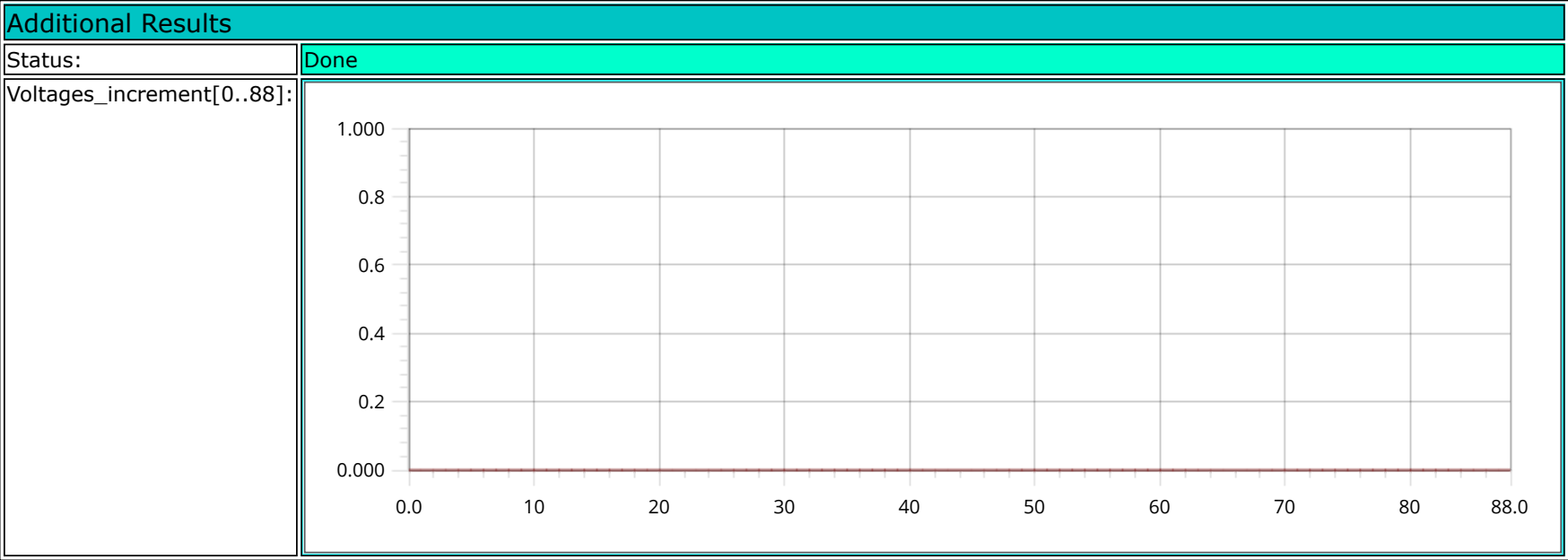
Pass/Fail Test

Status: Passed



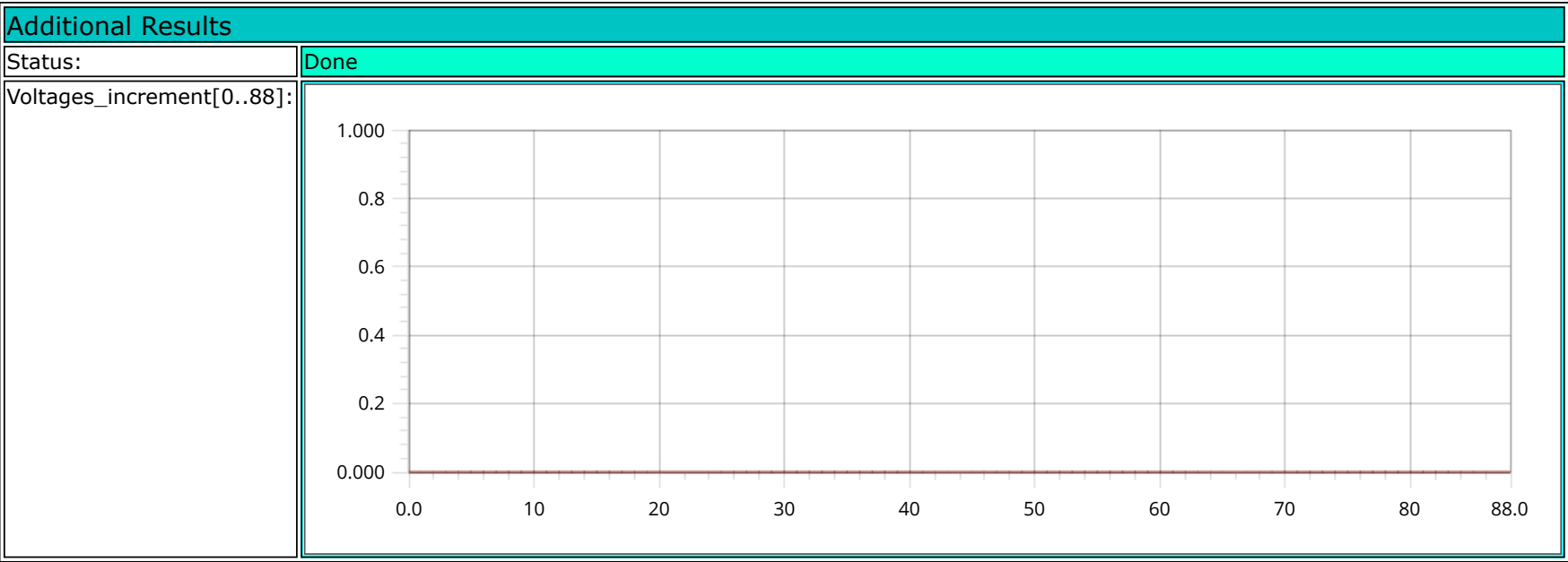
Pass/Fail Test

Status: Passed

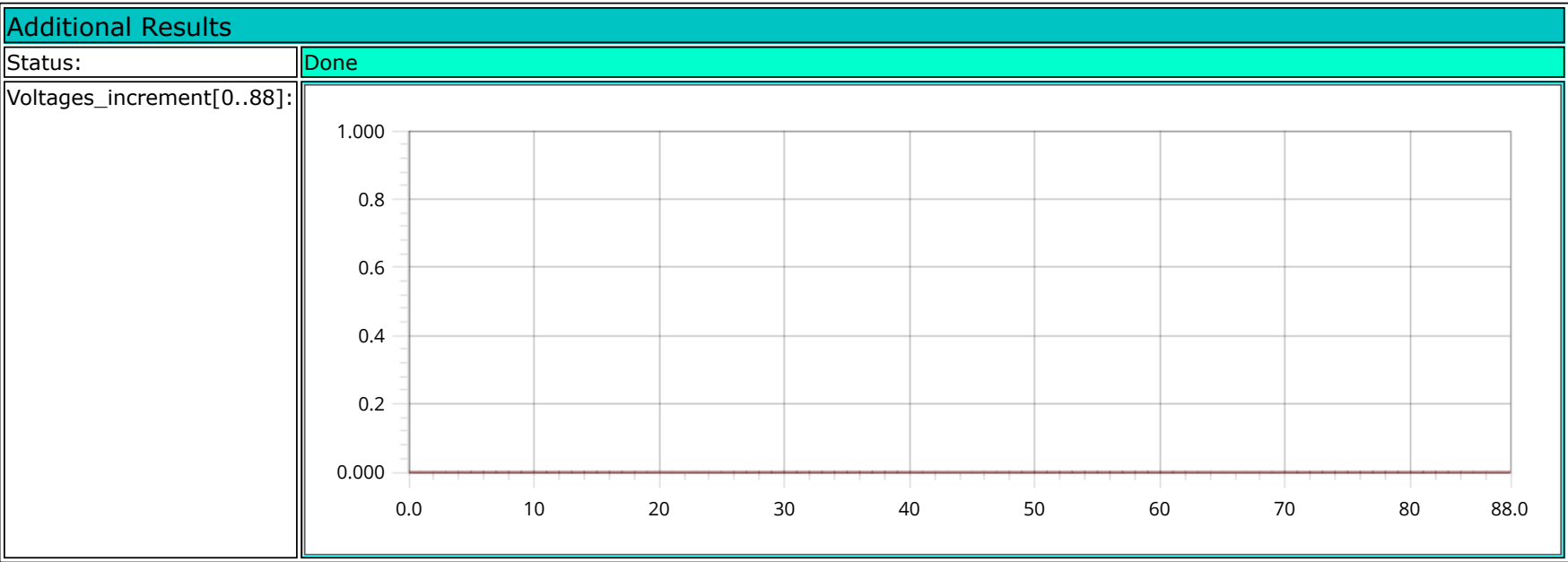




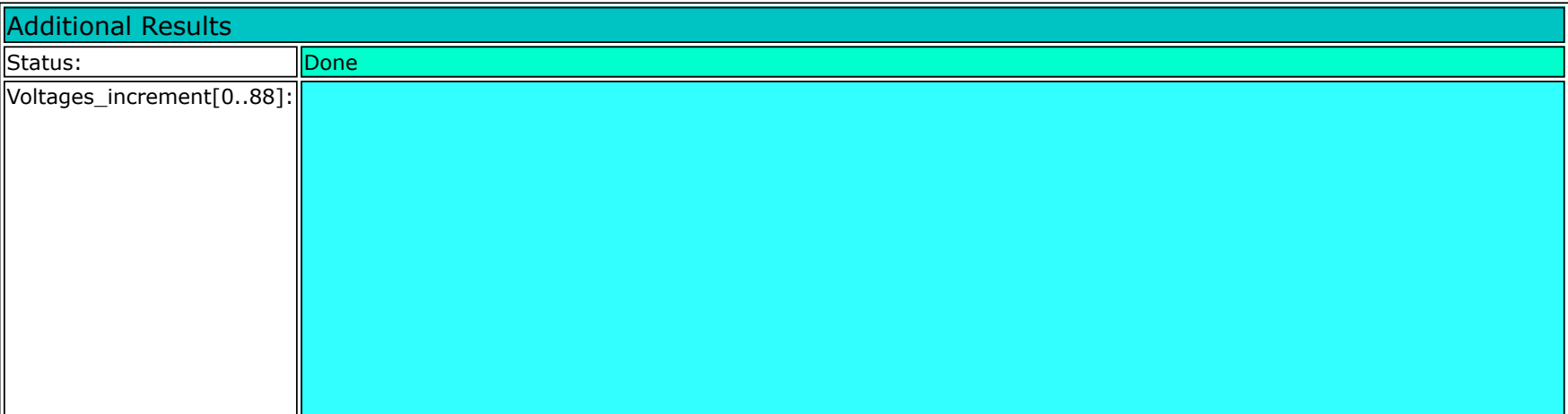
Pass/Fail Test	
Status:	Passed

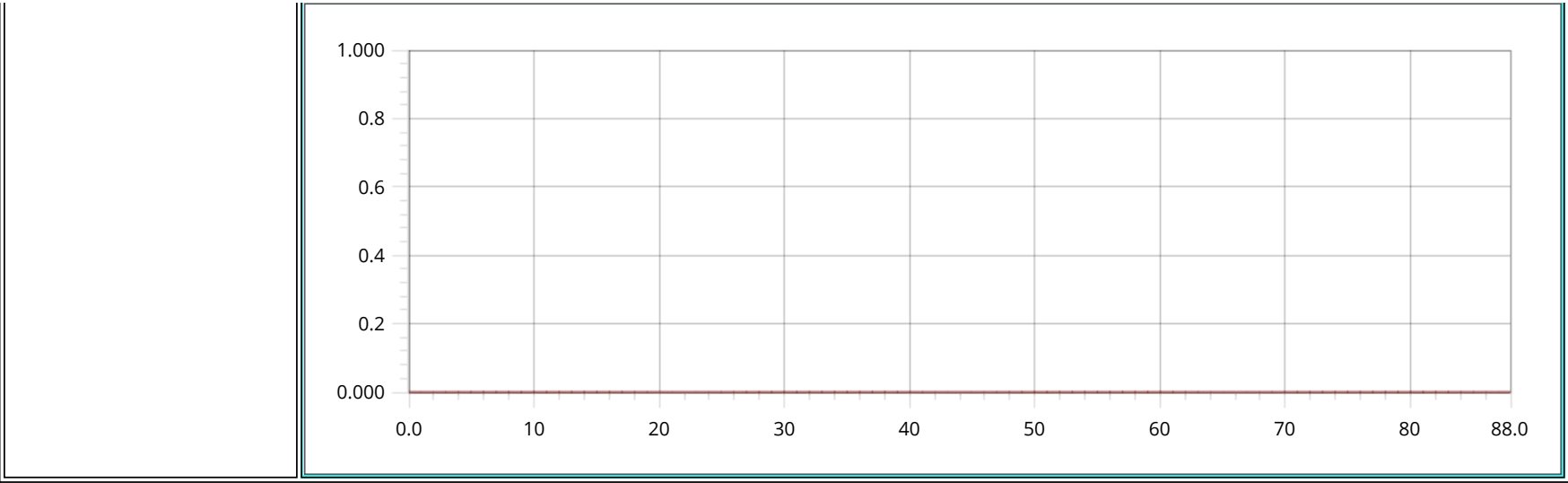


Pass/Fail Test	
Status:	Passed



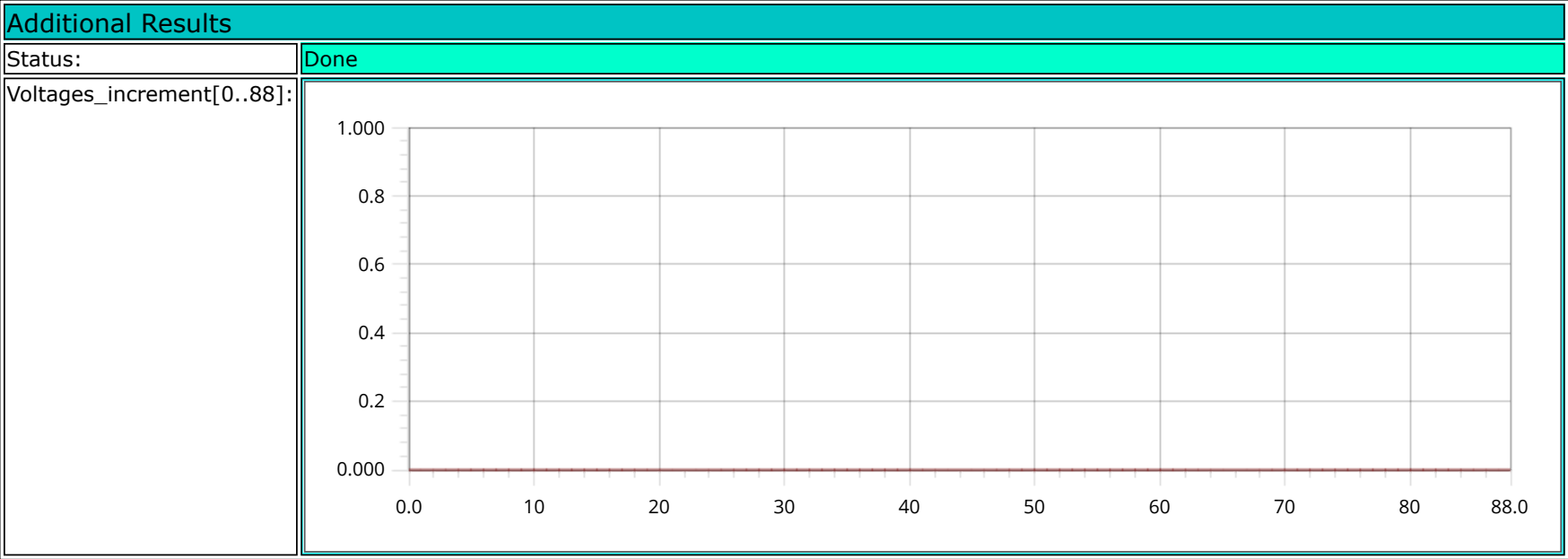
Pass/Fail Test	
Status:	Passed





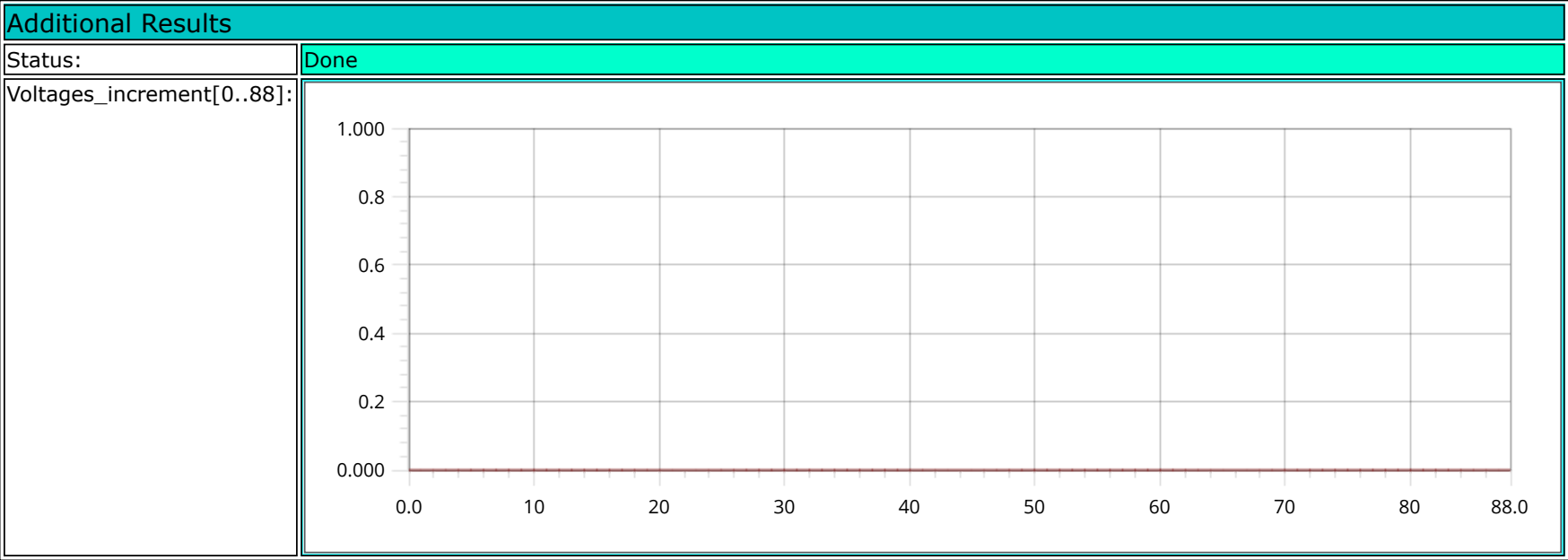
Pass/Fail Test

Status: Passed

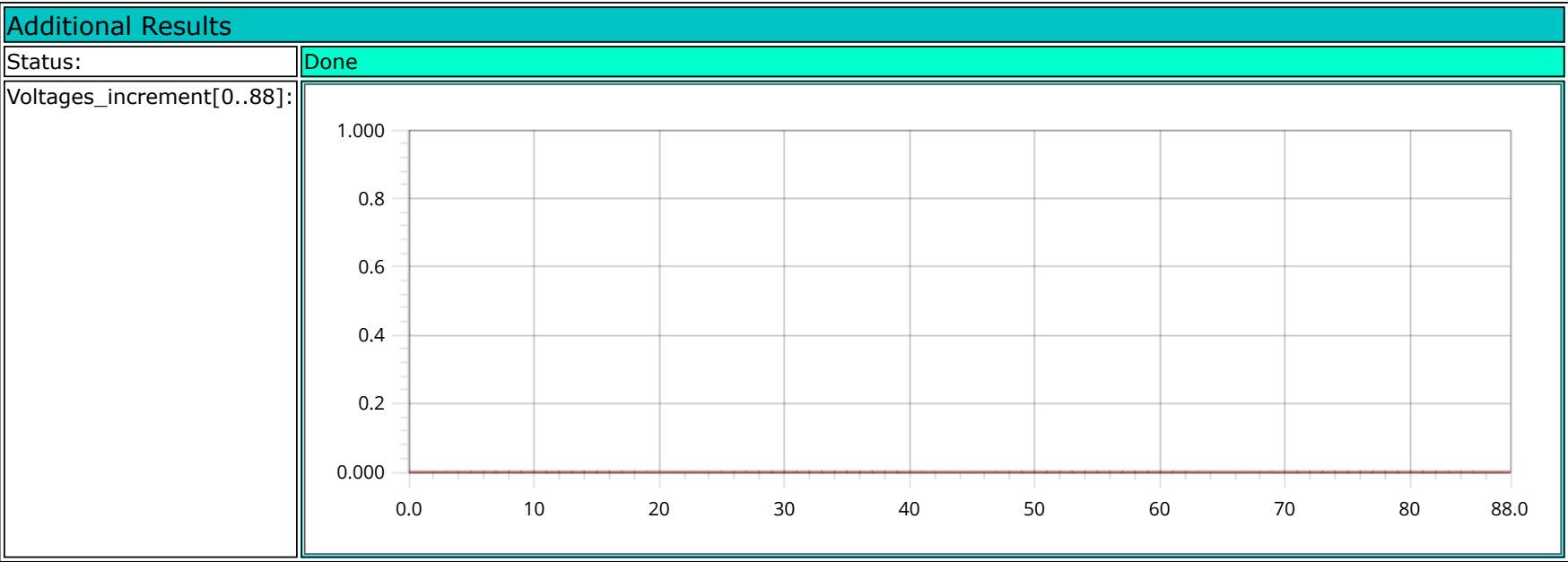


Pass/Fail Test

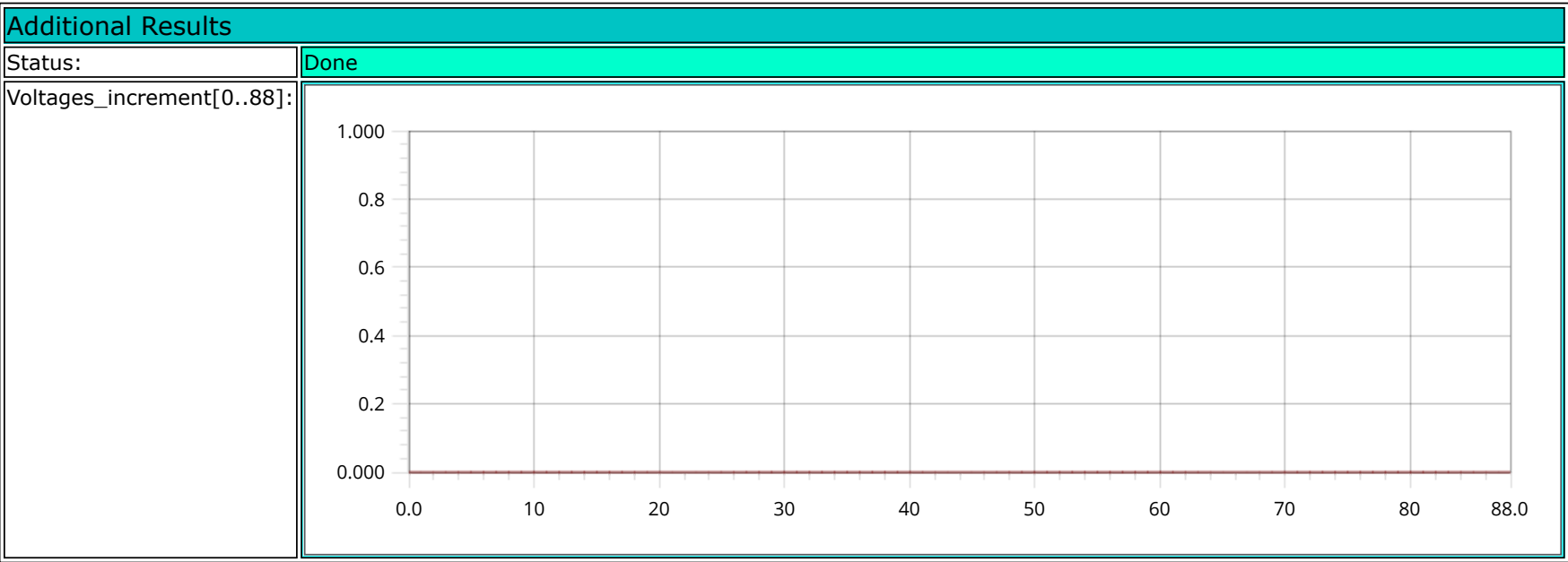
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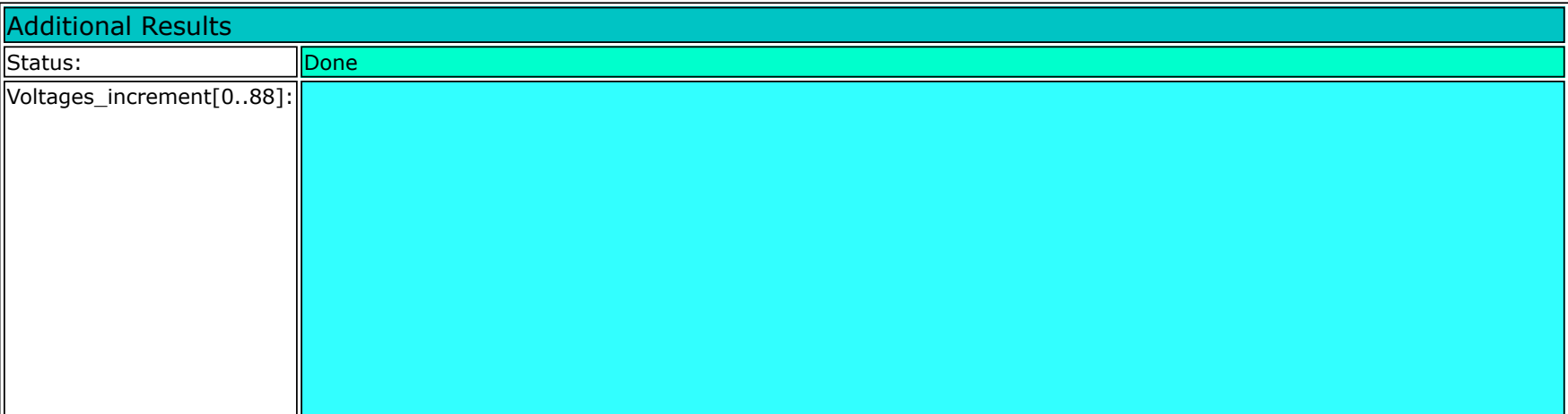
Pass/Fail Test	
Status:	Passed

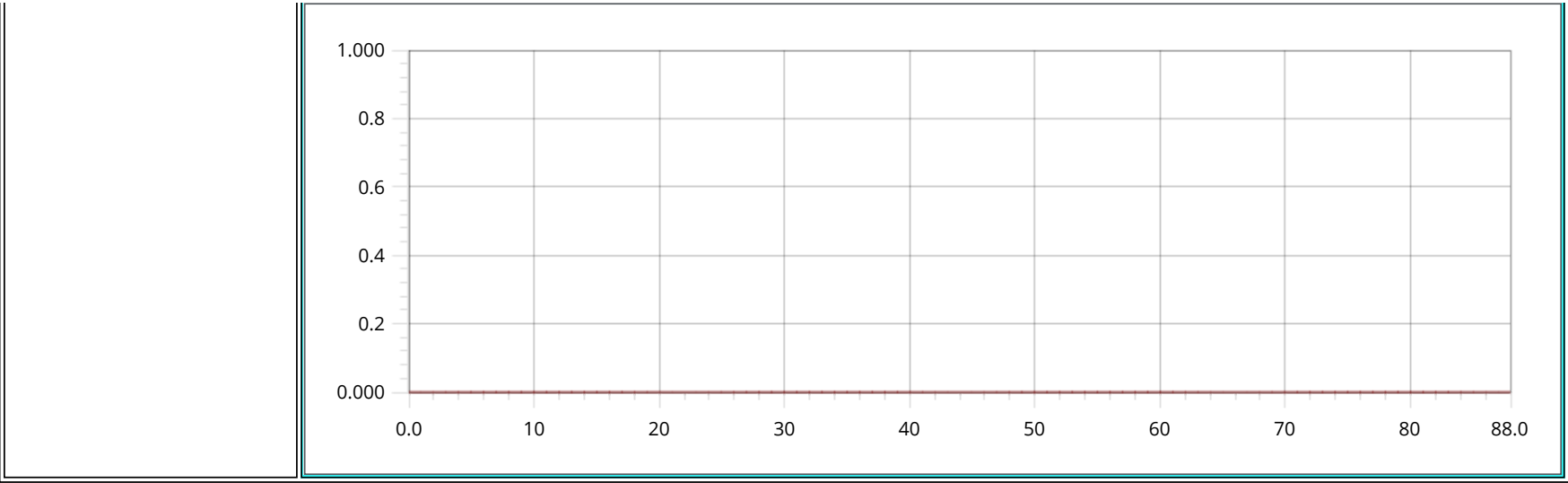


Pass/Fail Test	
Status:	Passed



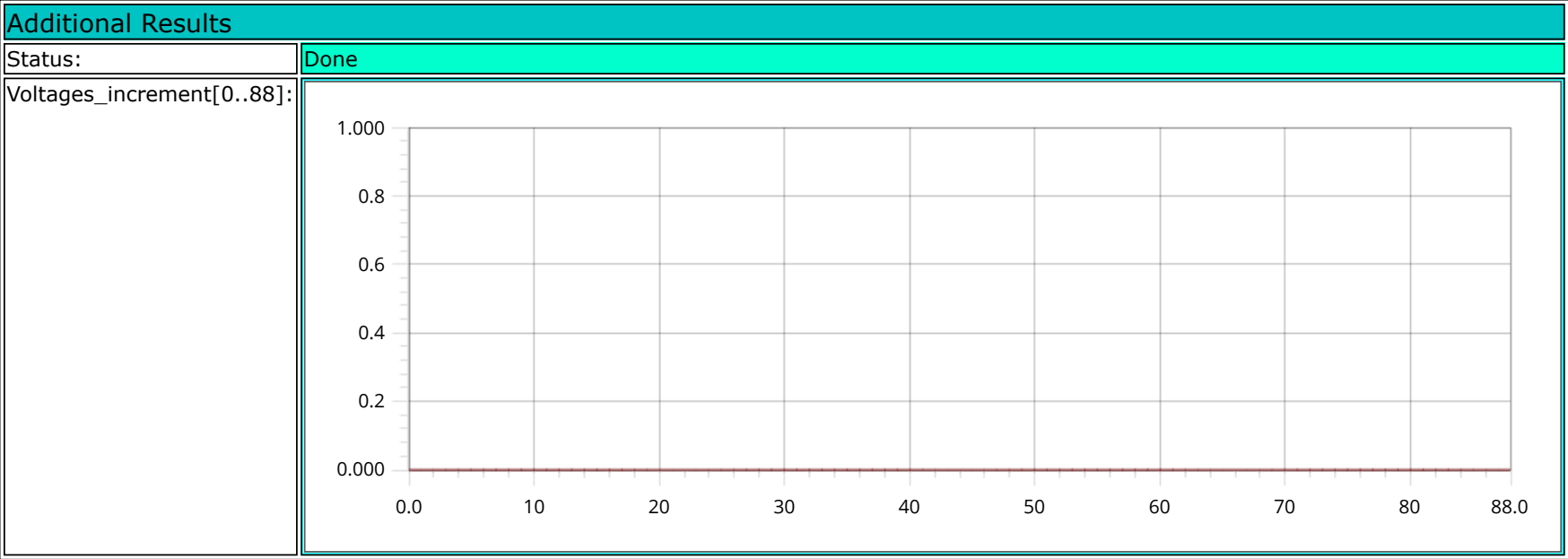
Pass/Fail Test	
Status:	Passed





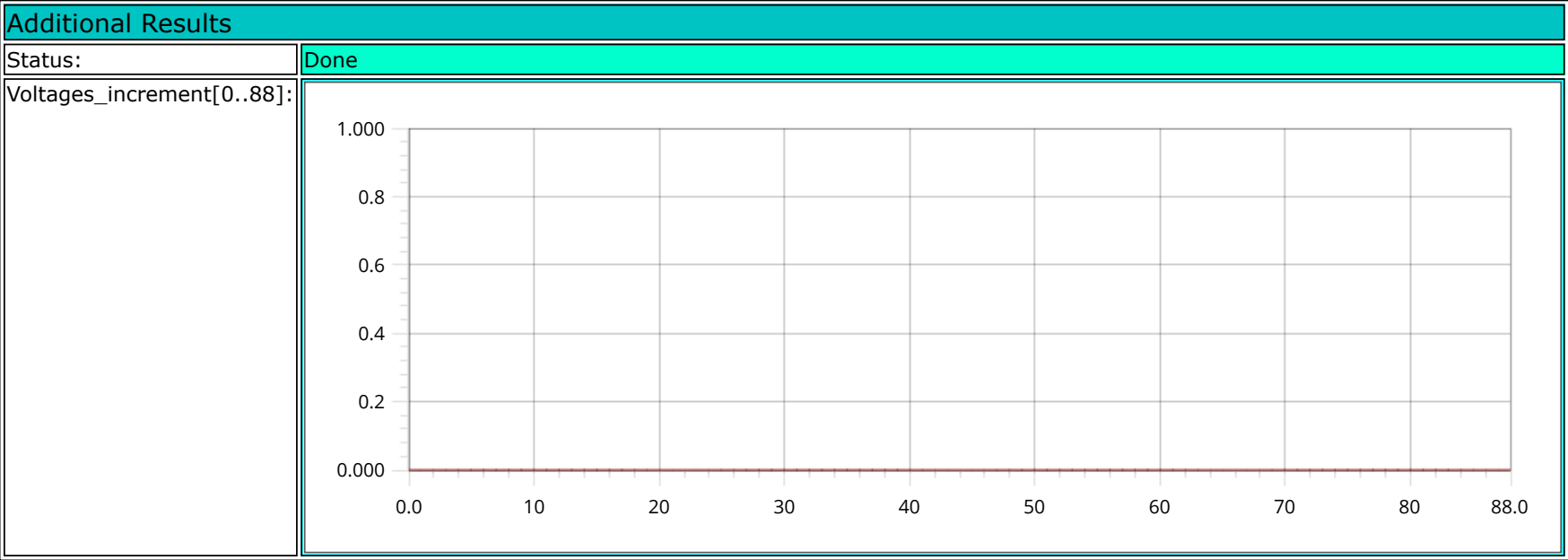
Pass/Fail Test

Status: Passed

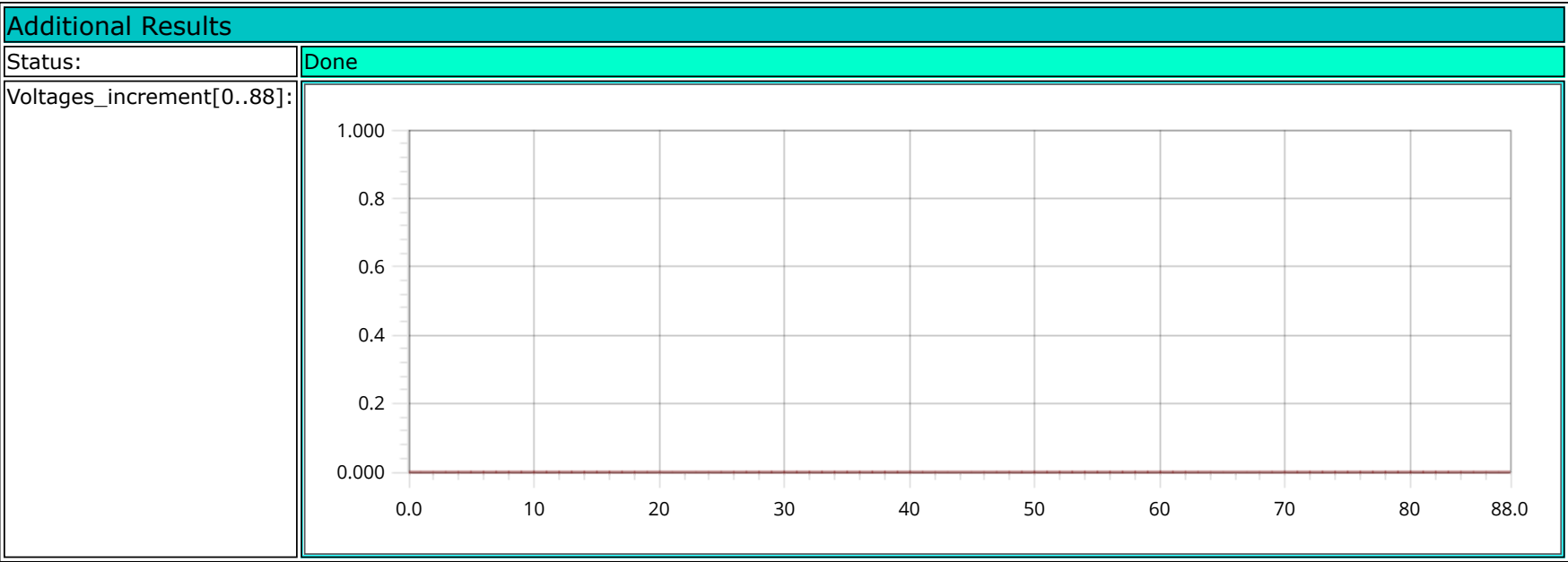


Pass/Fail Test

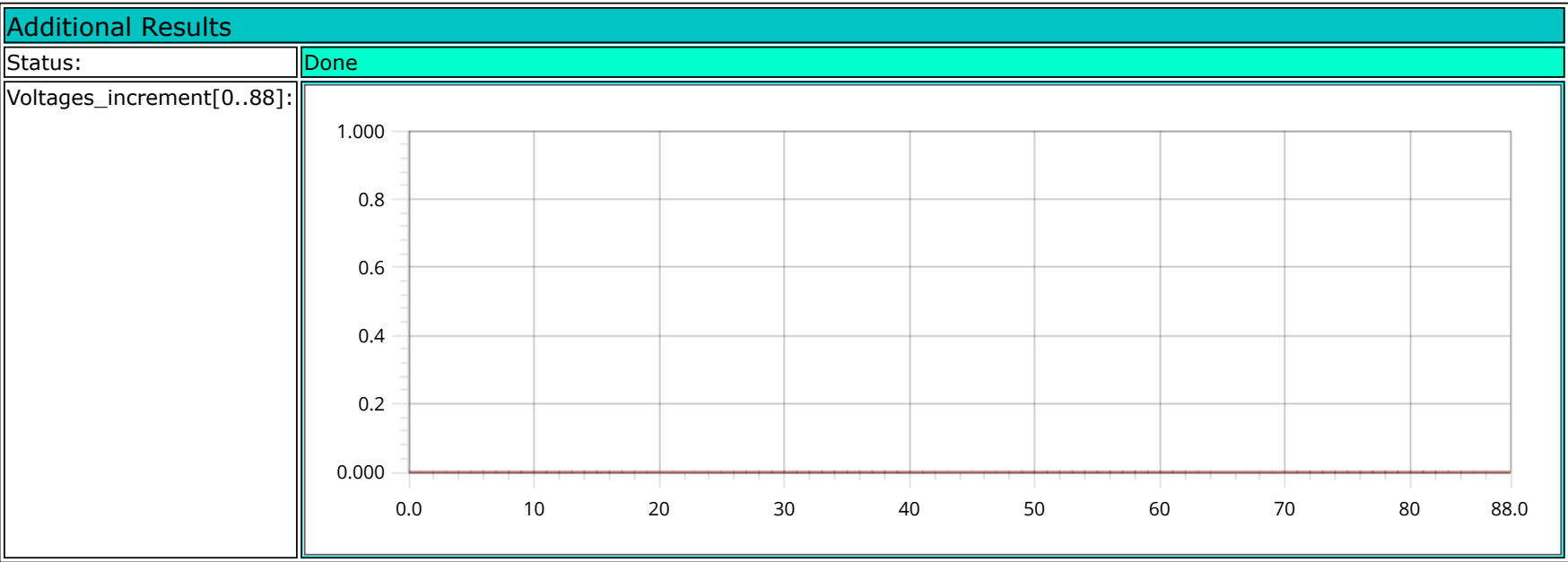
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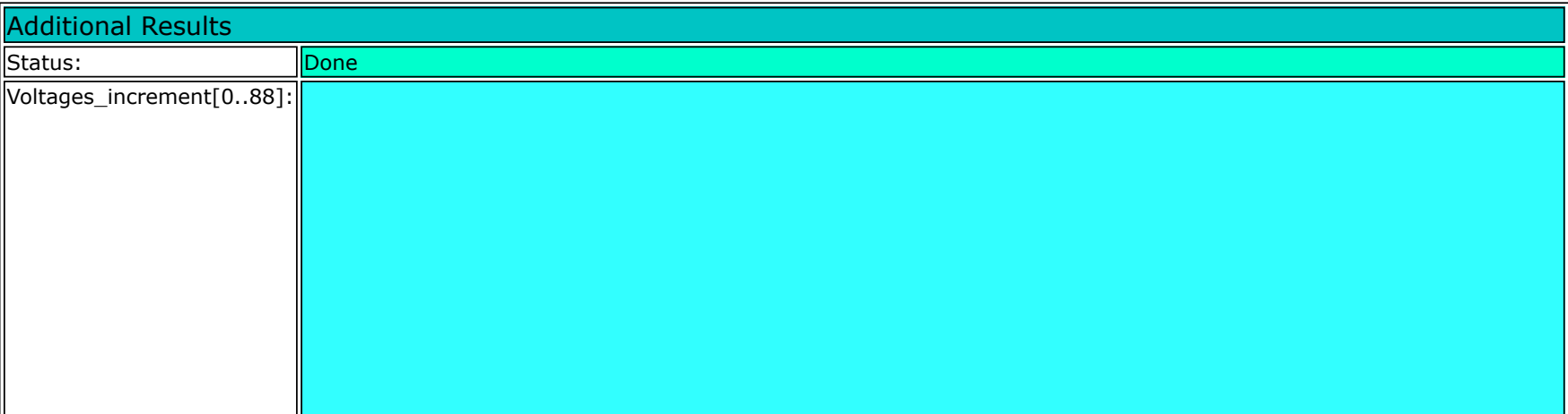
Pass/Fail Test	
Status:	Passed

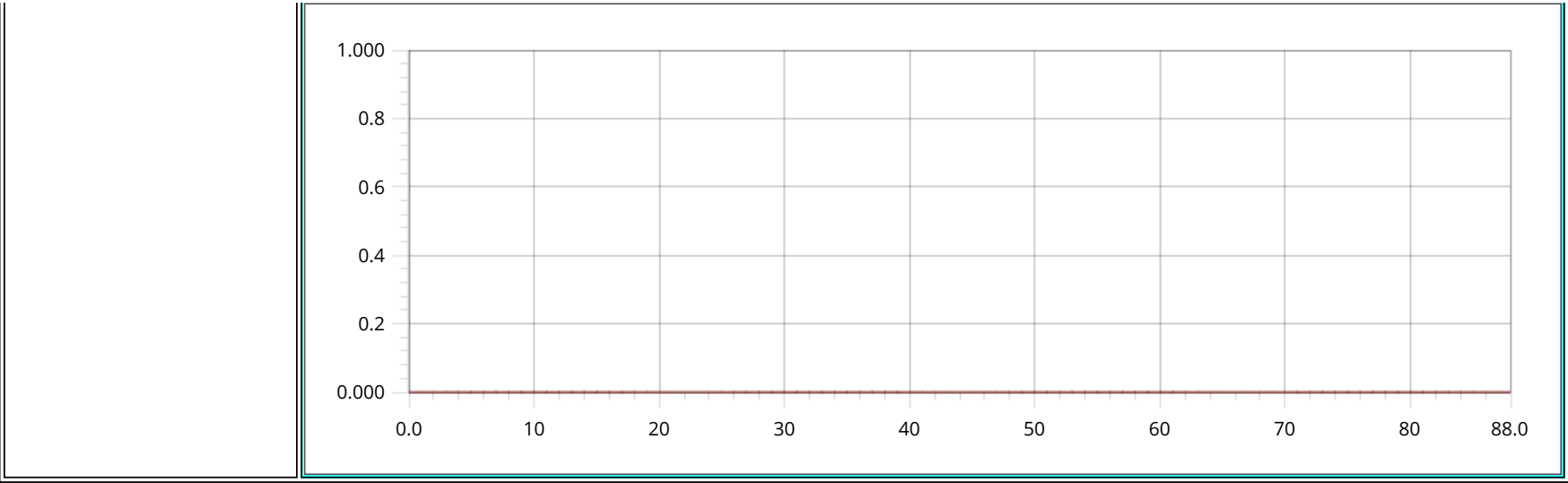


Pass/Fail Test	
Status:	Passed



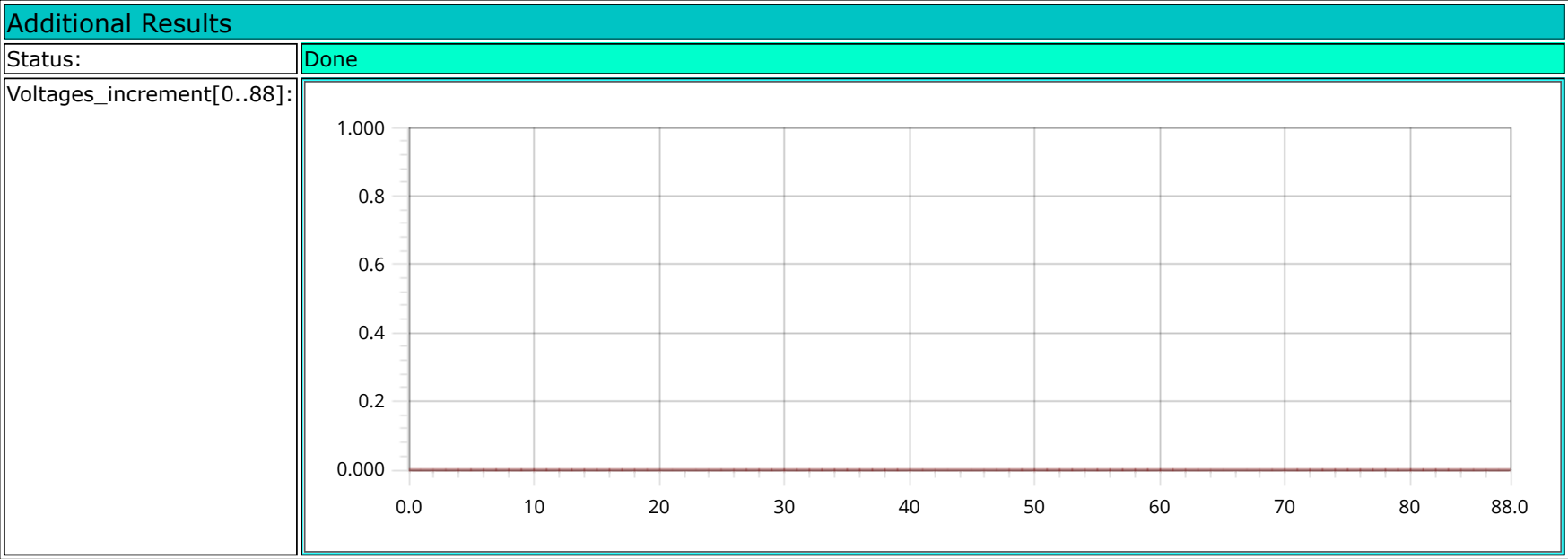
Pass/Fail Test	
Status:	Passed





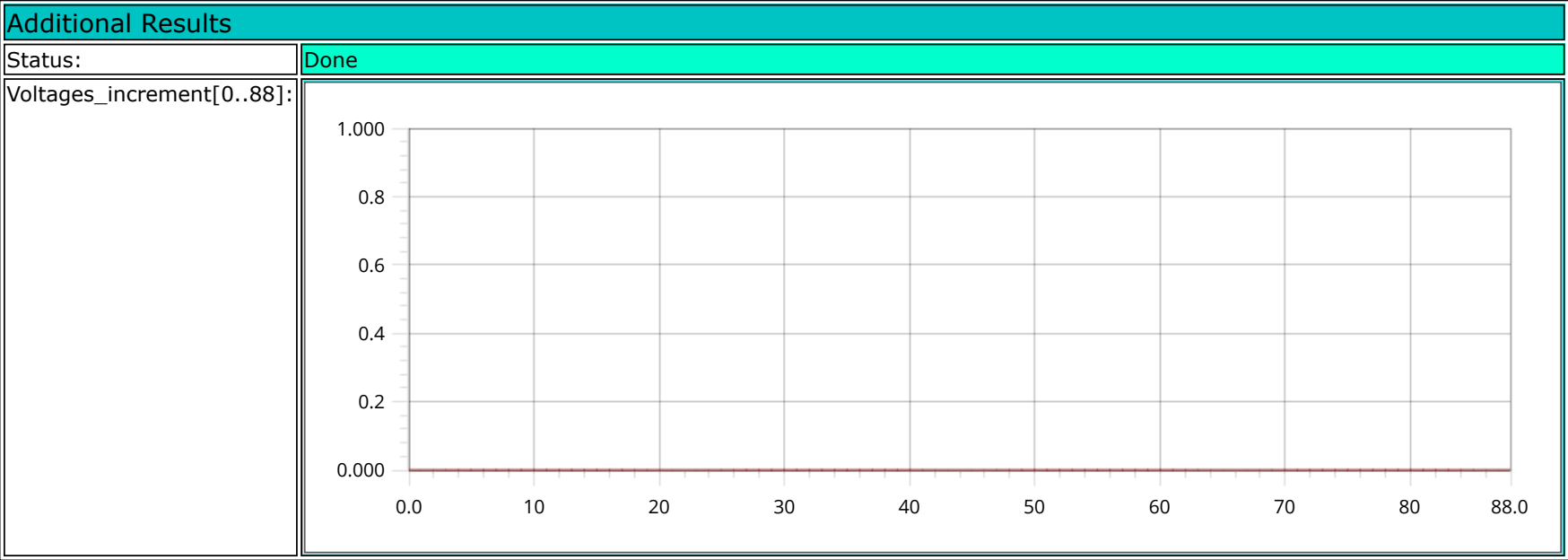
Pass/Fail Test

Status: Passed

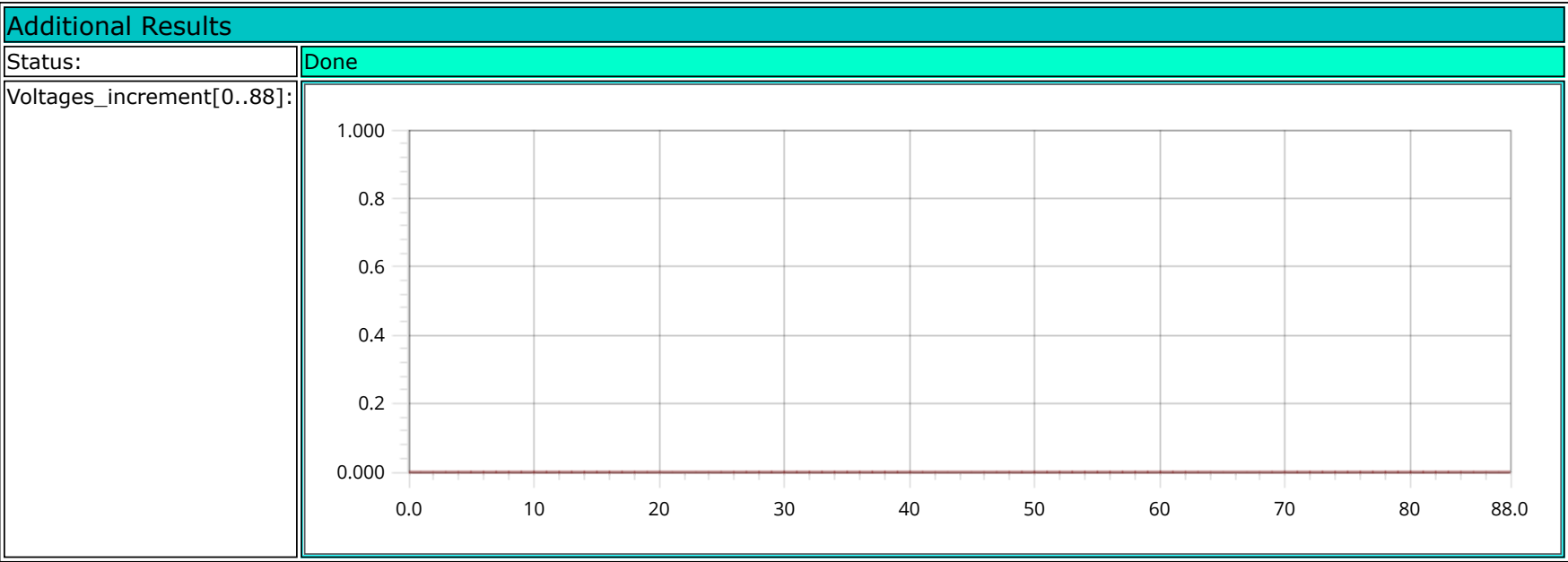


Pass/Fail Test

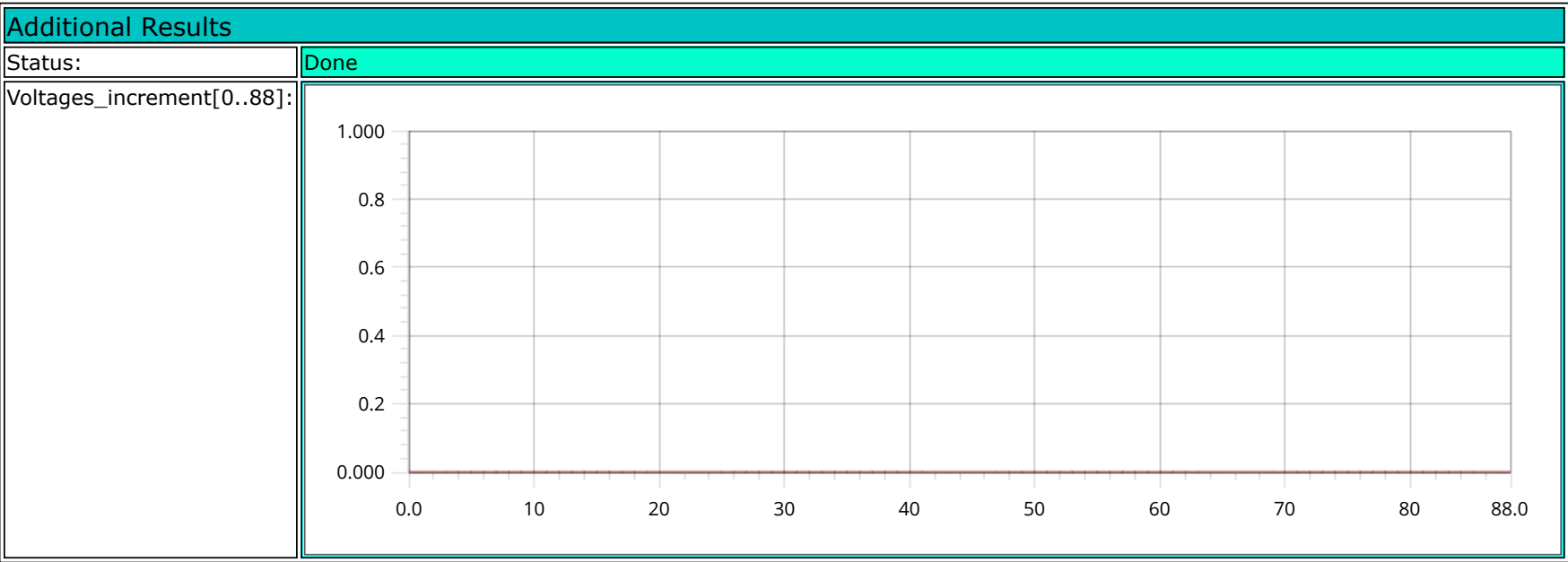
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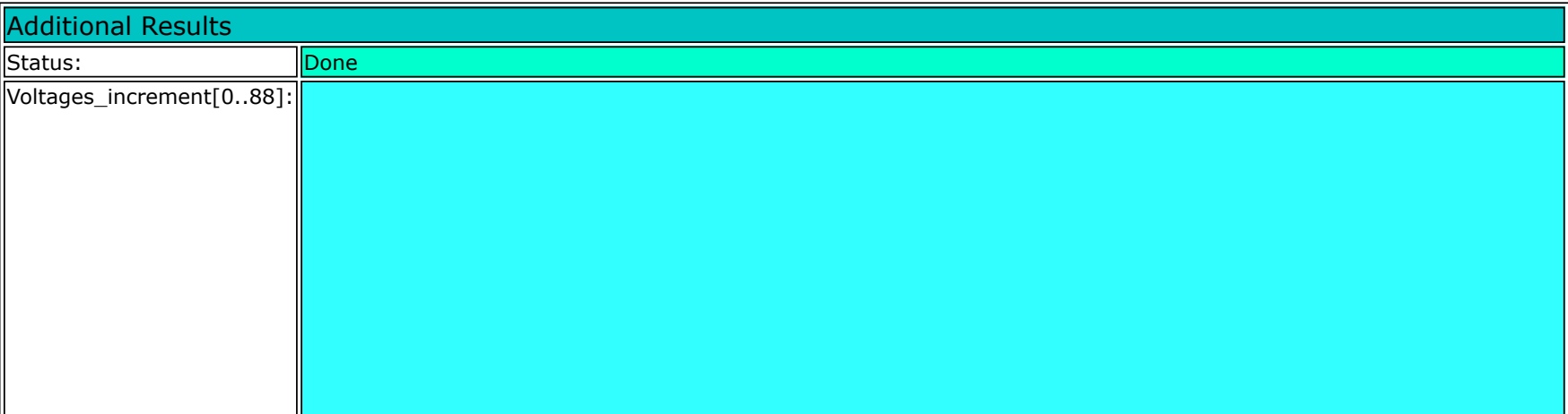
Pass/Fail Test	
Status:	Passed

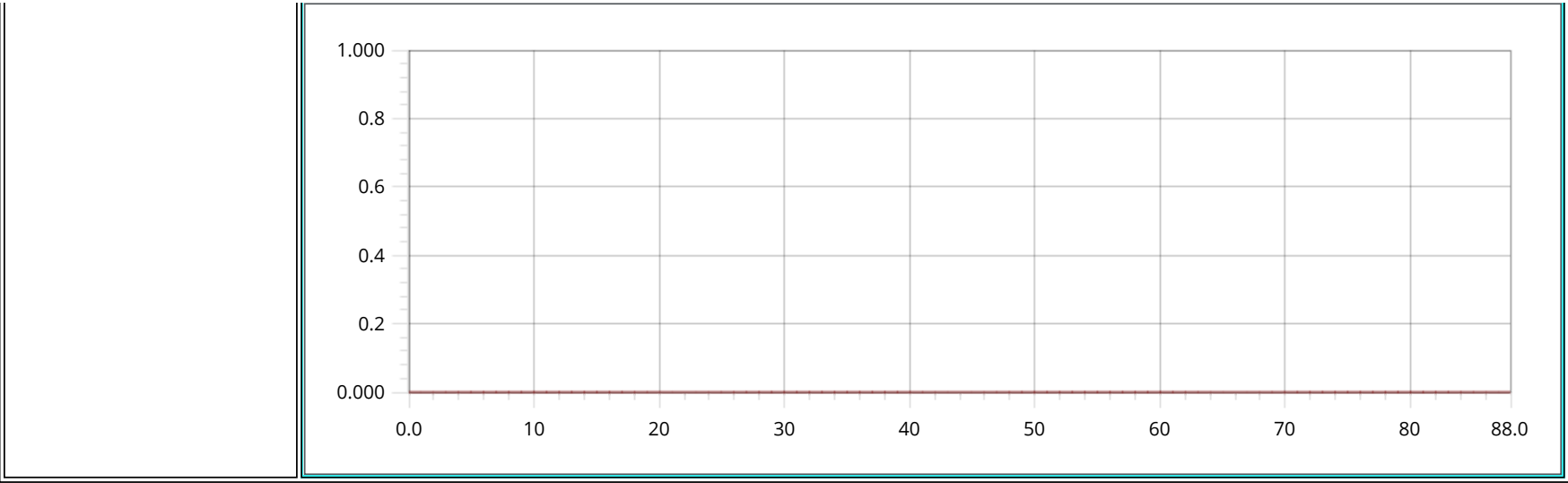


Pass/Fail Test	
Status:	Passed



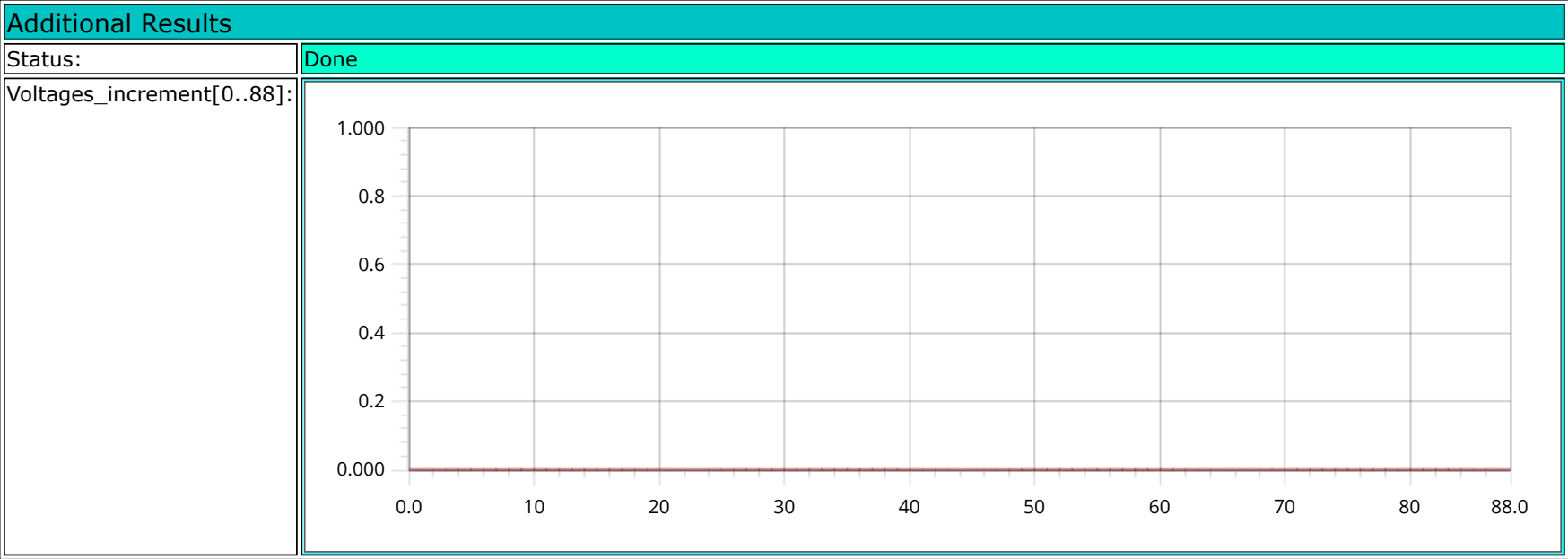
Pass/Fail Test	
Status:	Passed





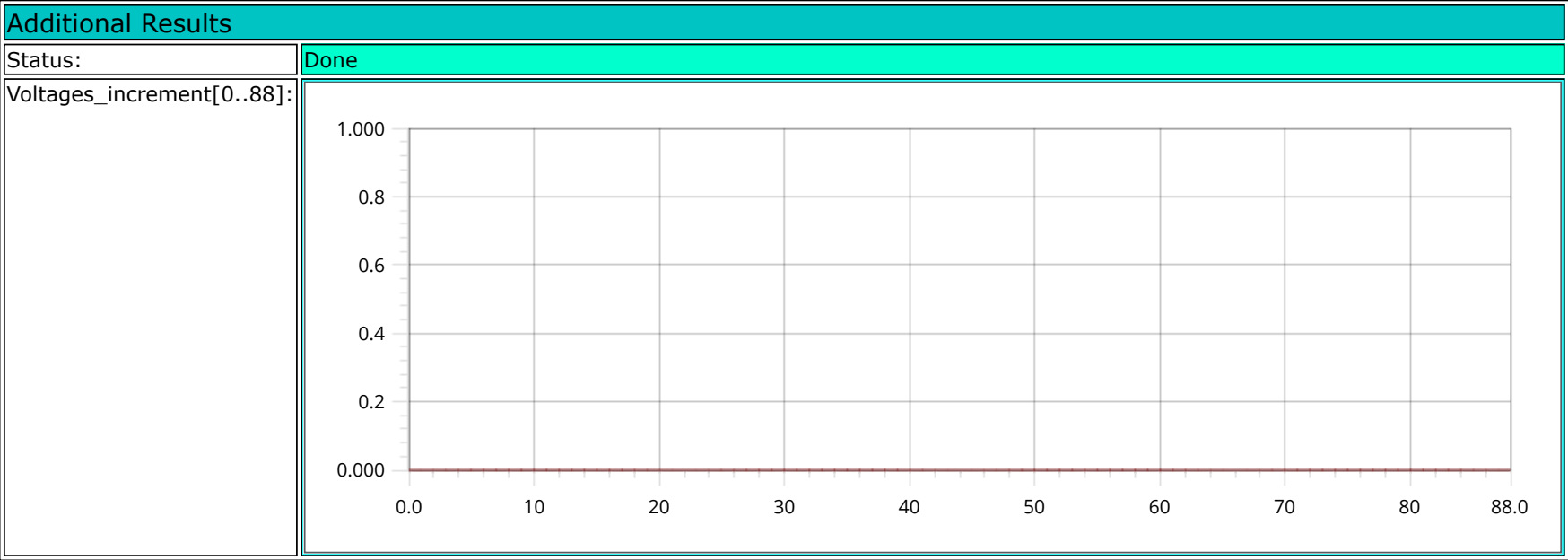
Pass/Fail Test

Status: Passed



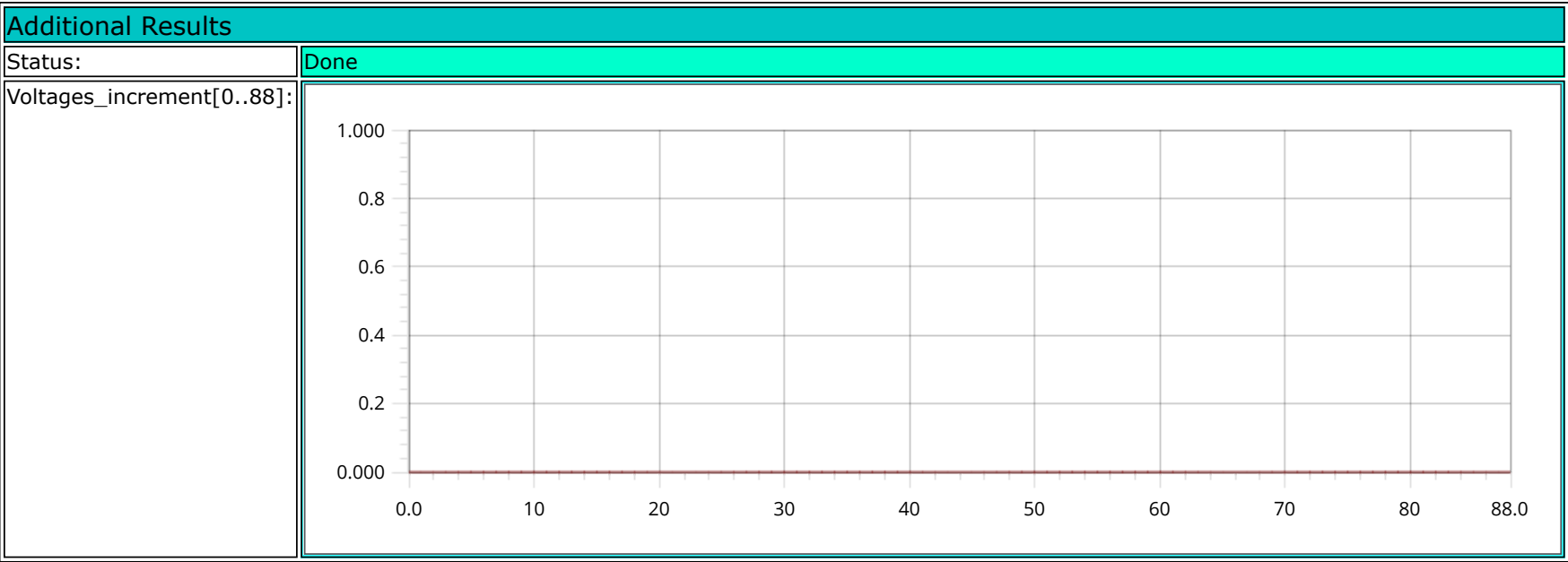
Pass/Fail Test

Status: Passed

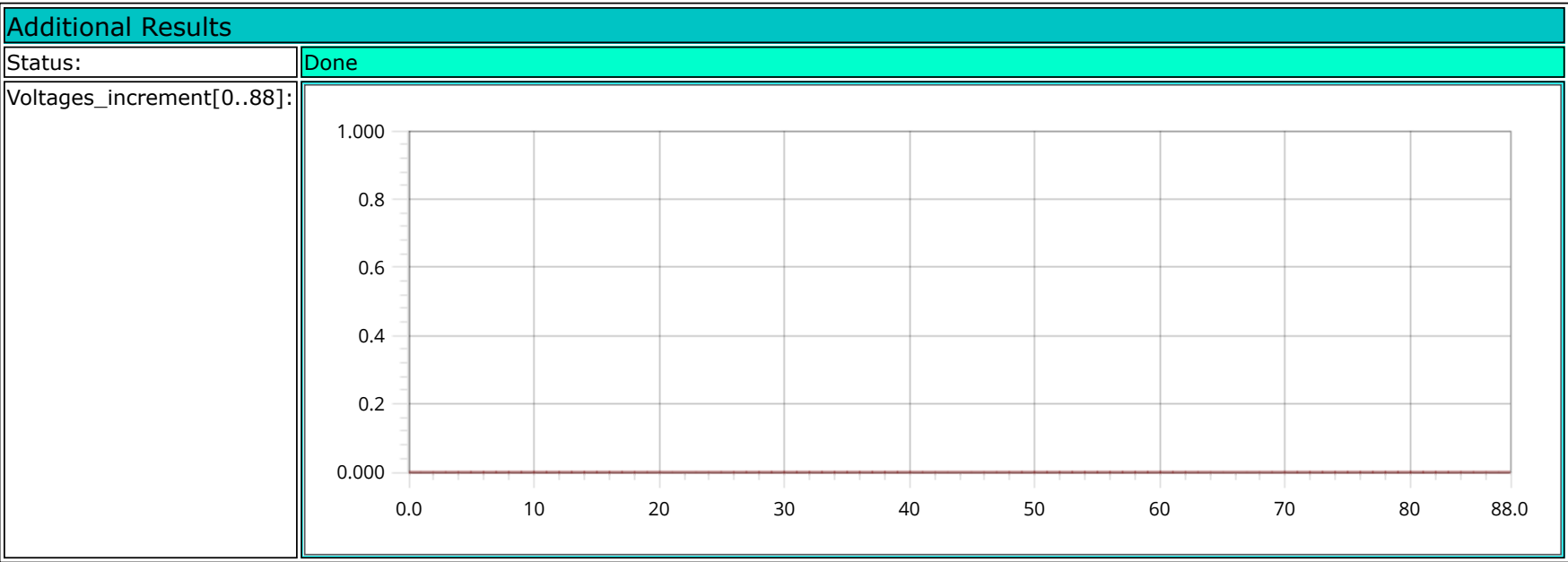




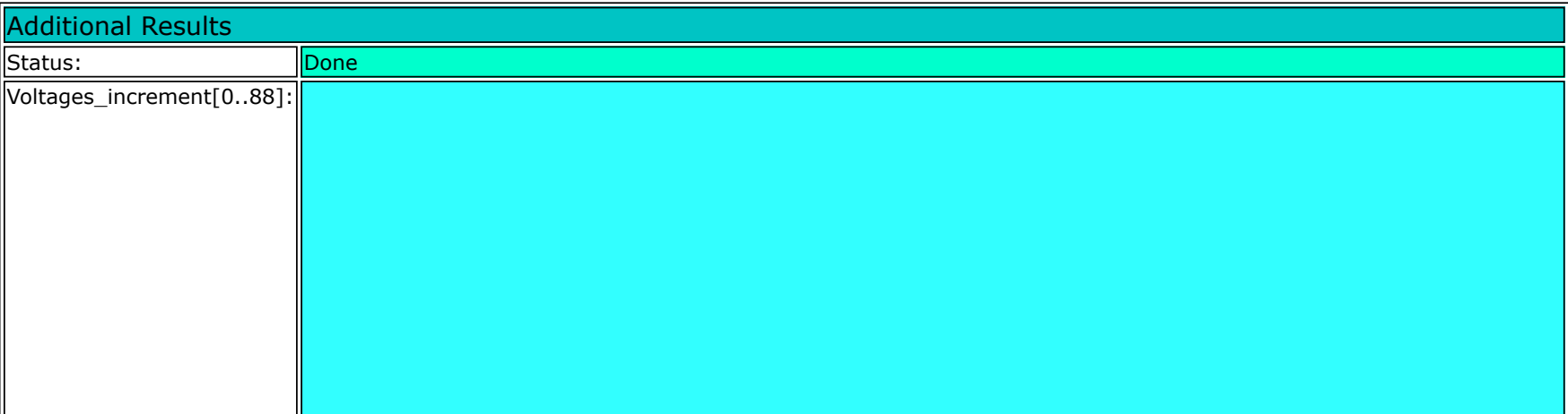
Pass/Fail Test	
Status:	Passed

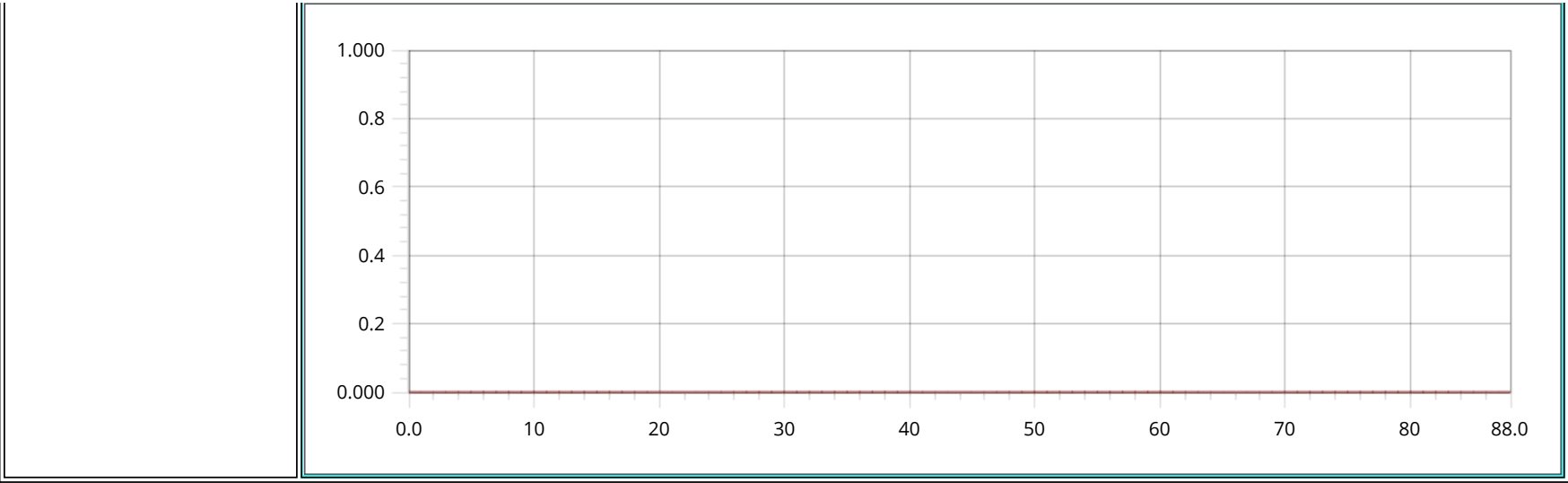


Pass/Fail Test	
Status:	Passed



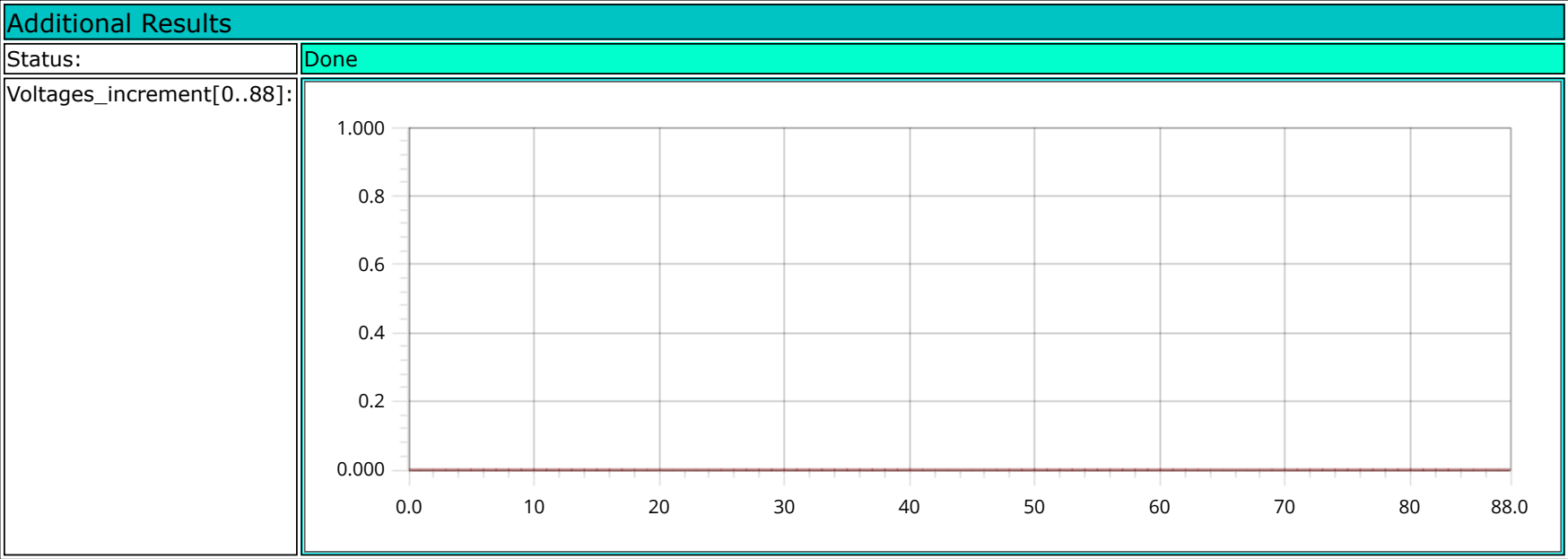
Pass/Fail Test	
Status:	Passed





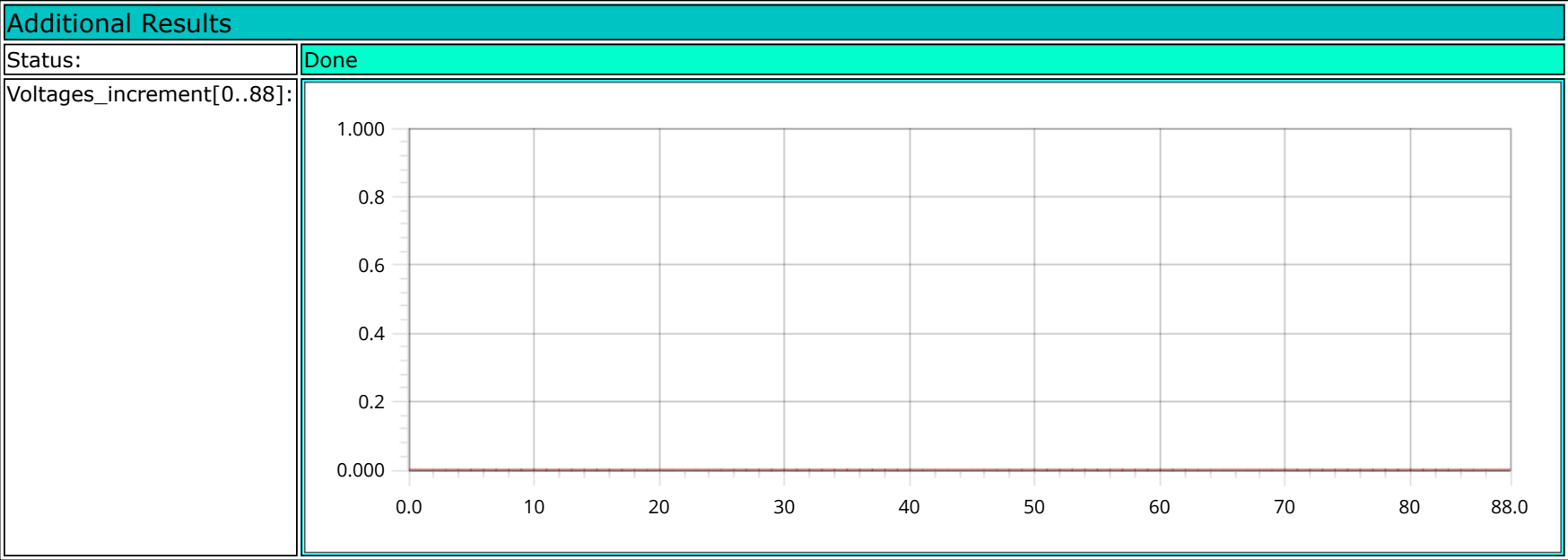
Pass/Fail Test

Status: Passed

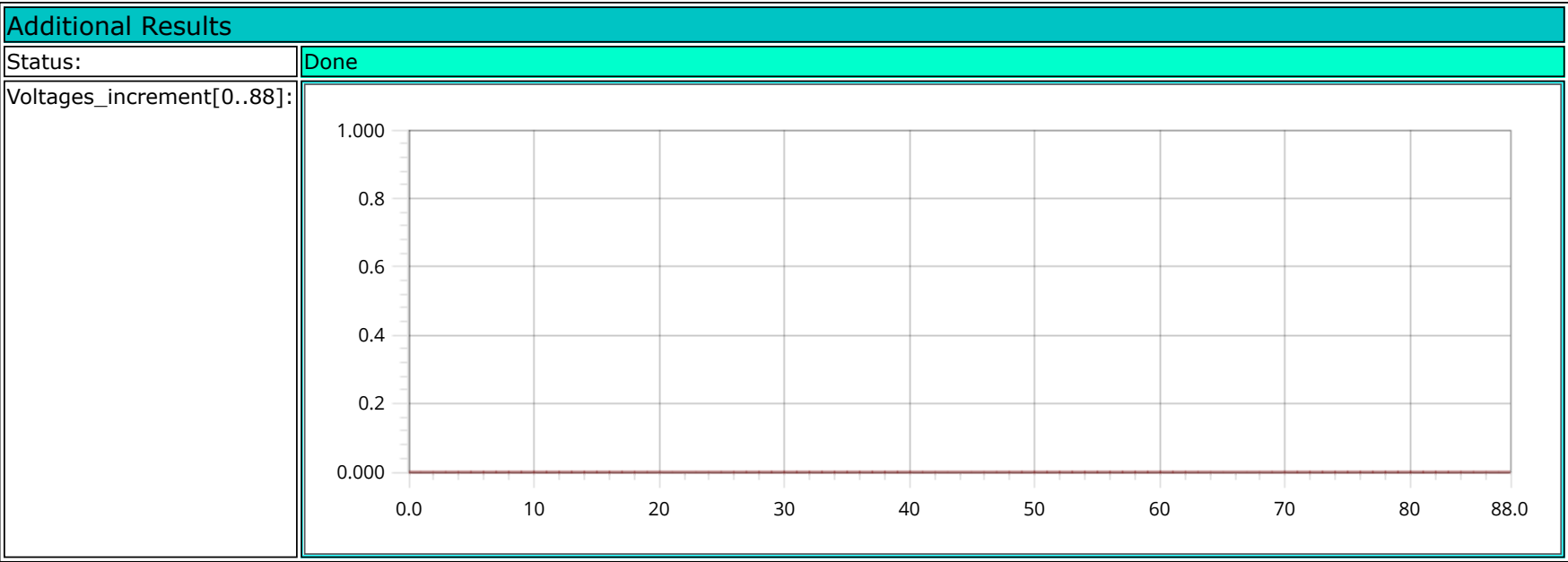


Pass/Fail Test

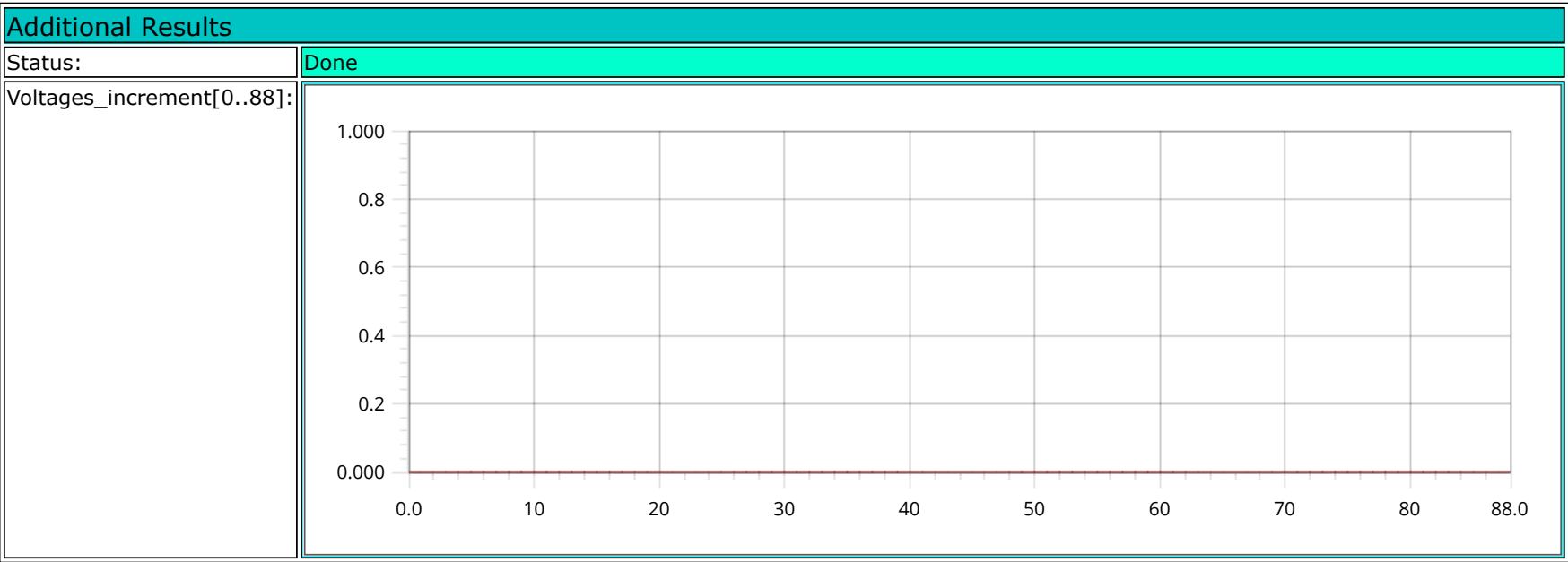
Status: Passed



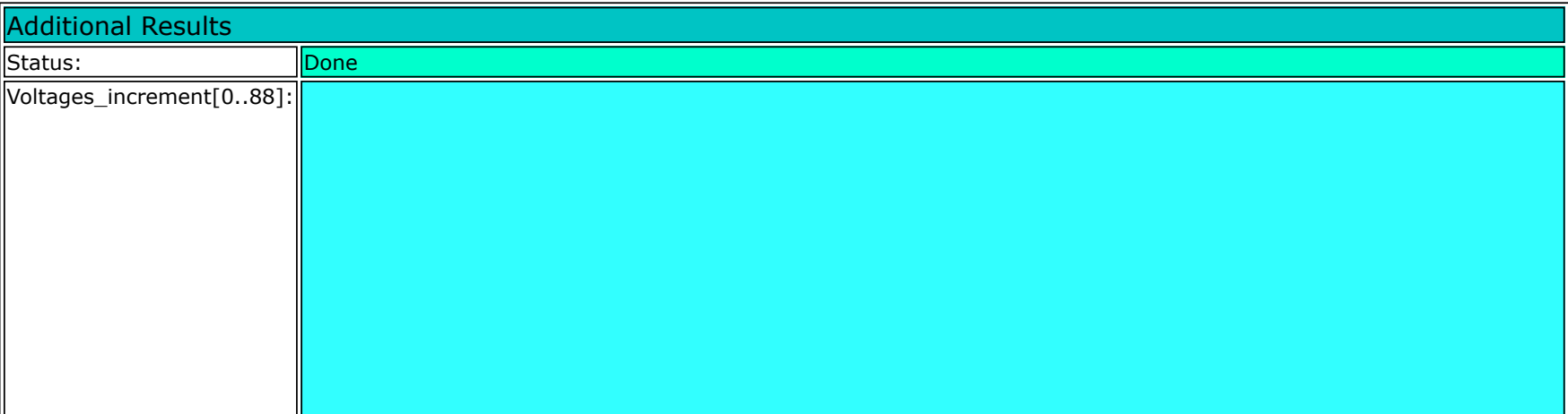
Pass/Fail Test	
Status:	Passed

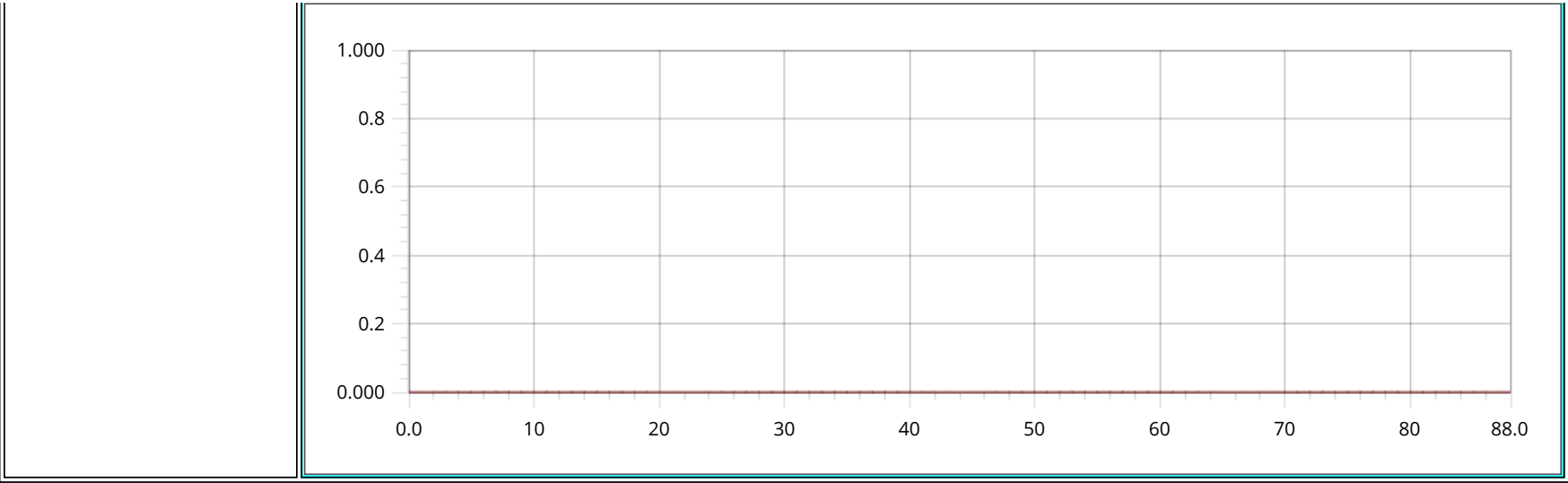


Pass/Fail Test	
Status:	Passed



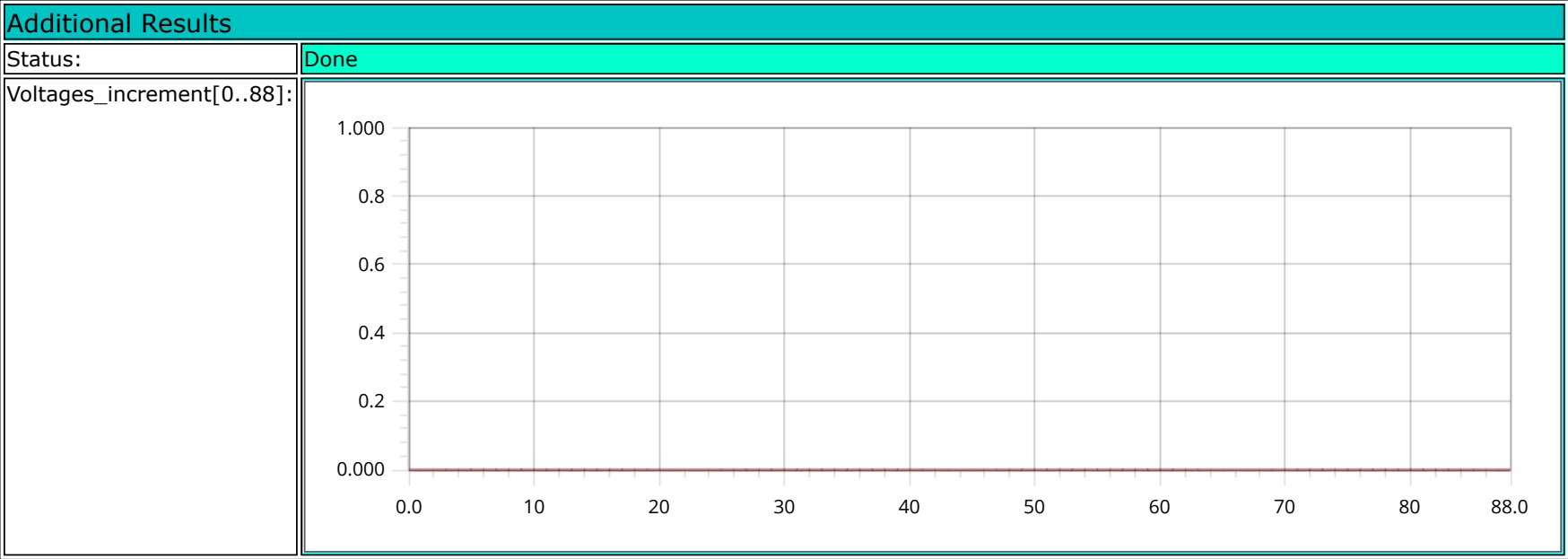
Pass/Fail Test	
Status:	Passed





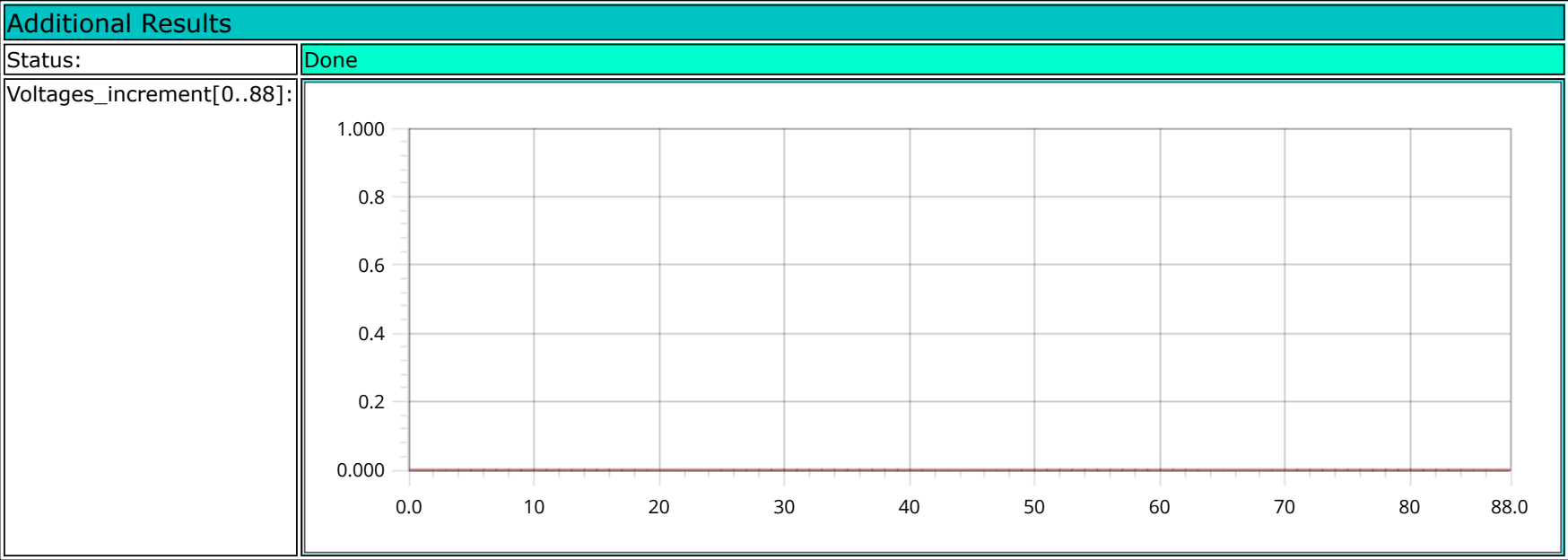
Pass/Fail Test

Status: Passed

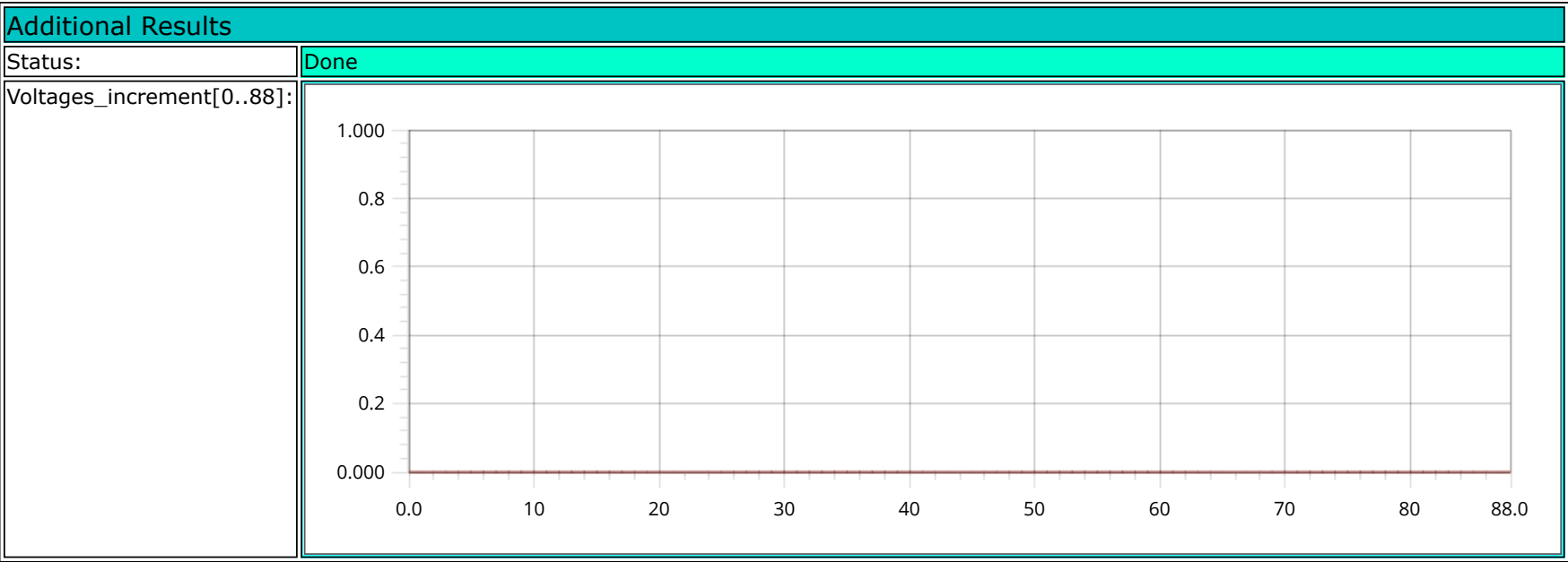


Pass/Fail Test

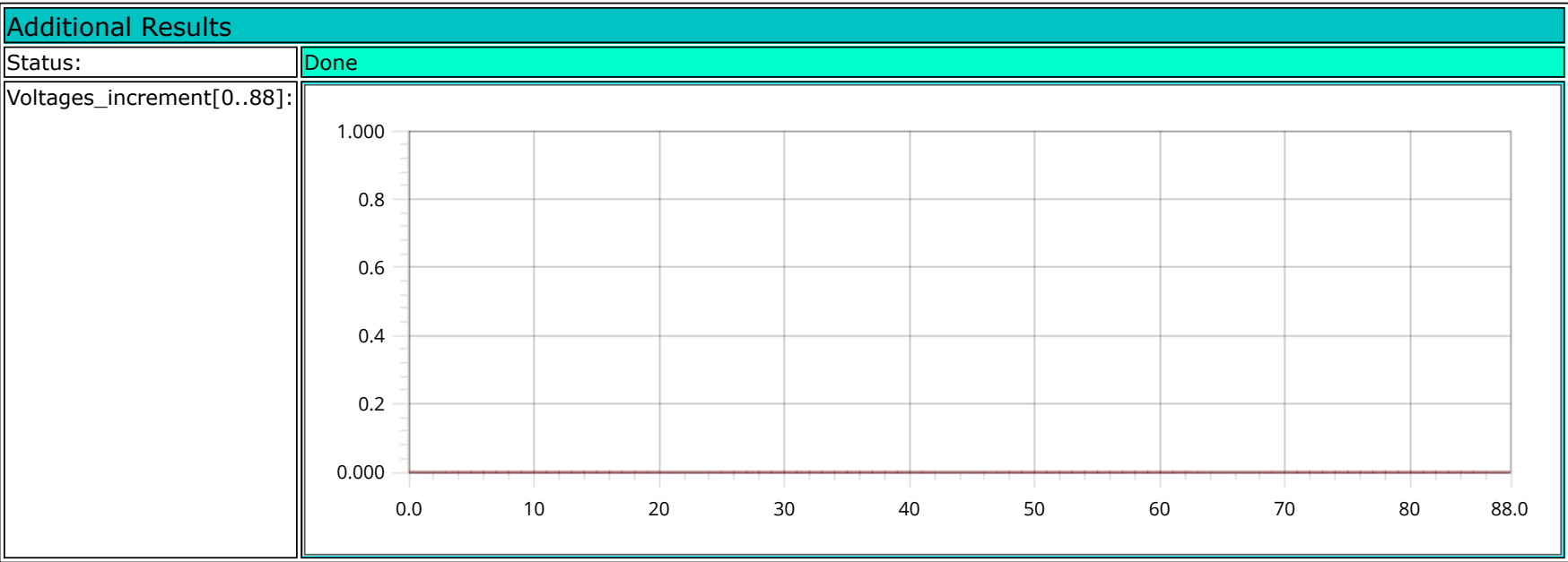
Status: Passed



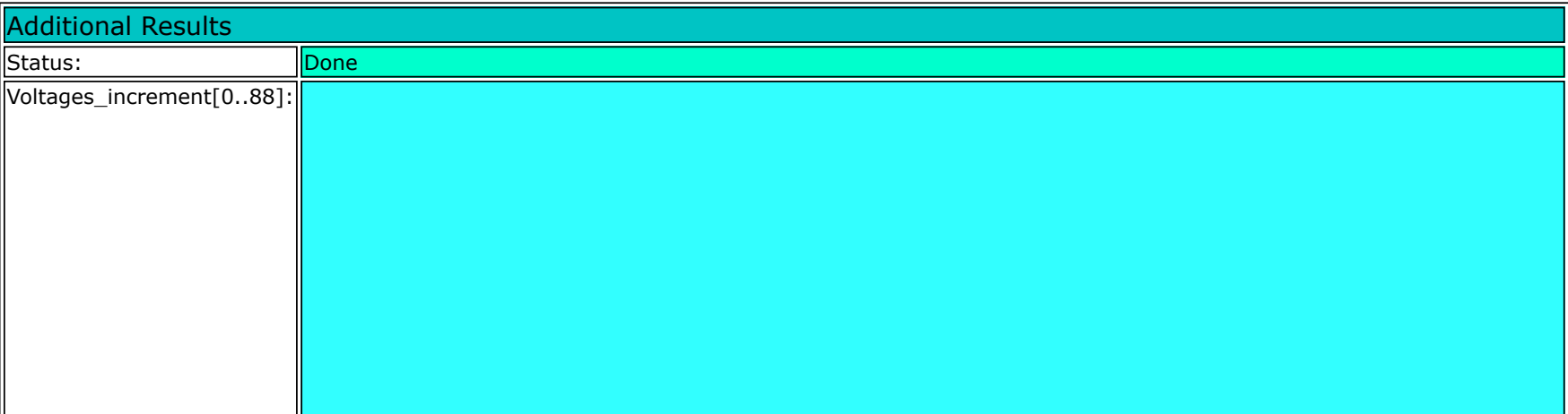
Pass/Fail Test	
Status:	Passed

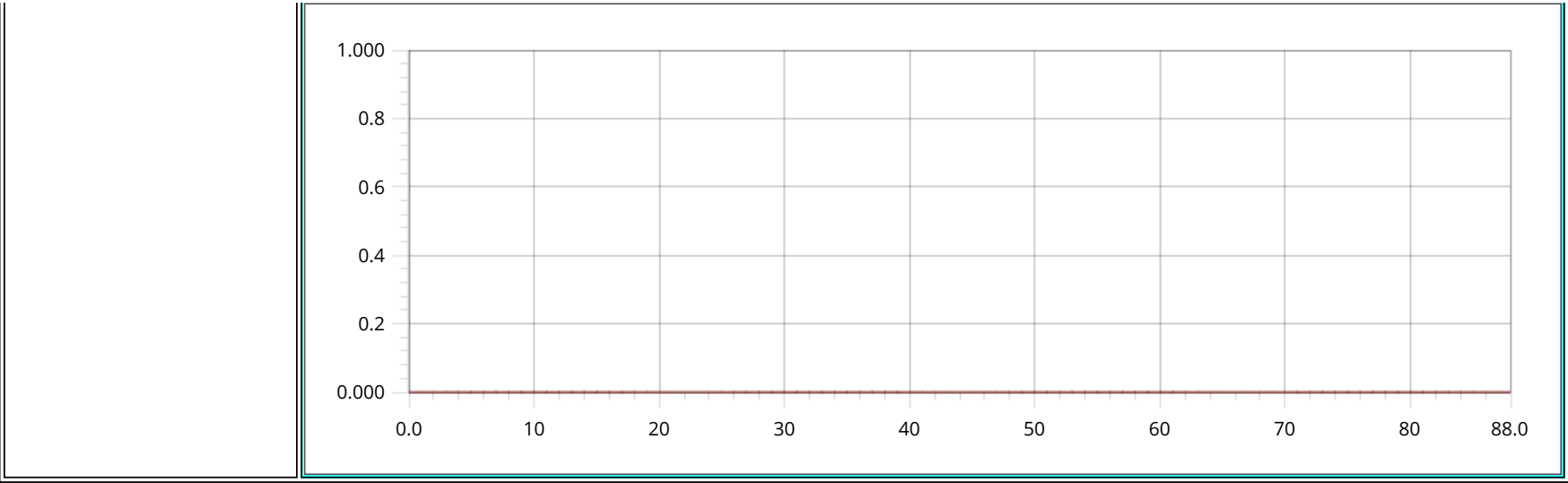


Pass/Fail Test	
Status:	Passed



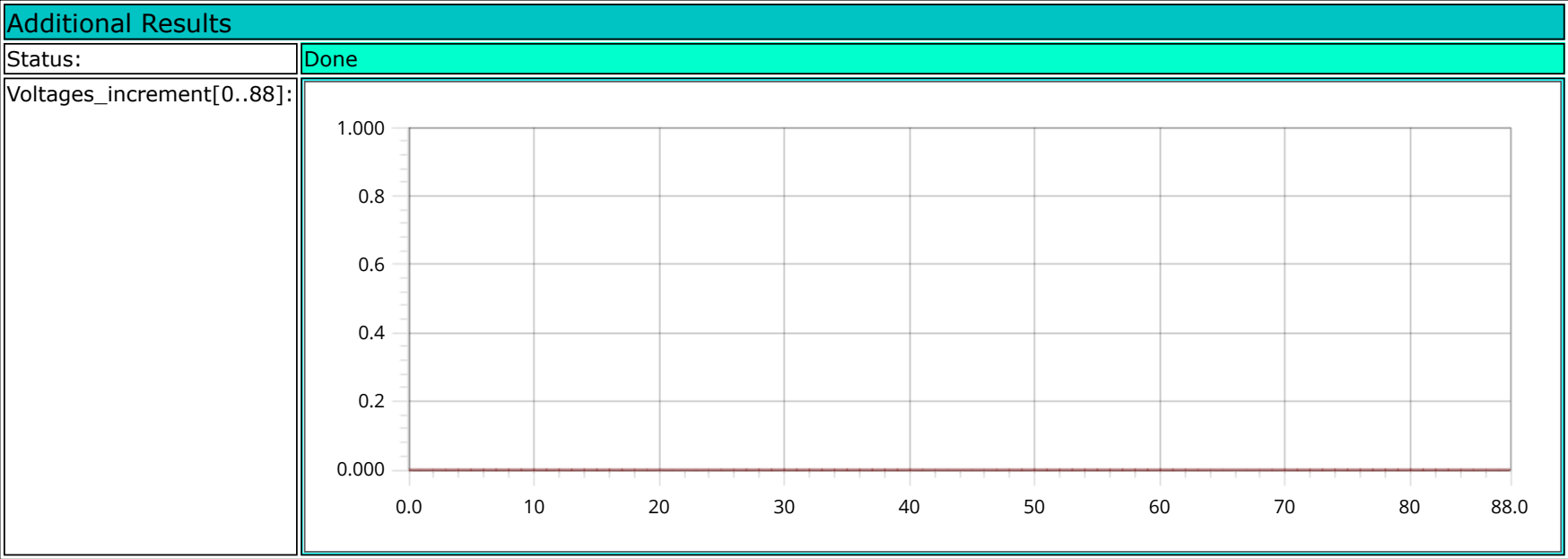
Pass/Fail Test	
Status:	Passed





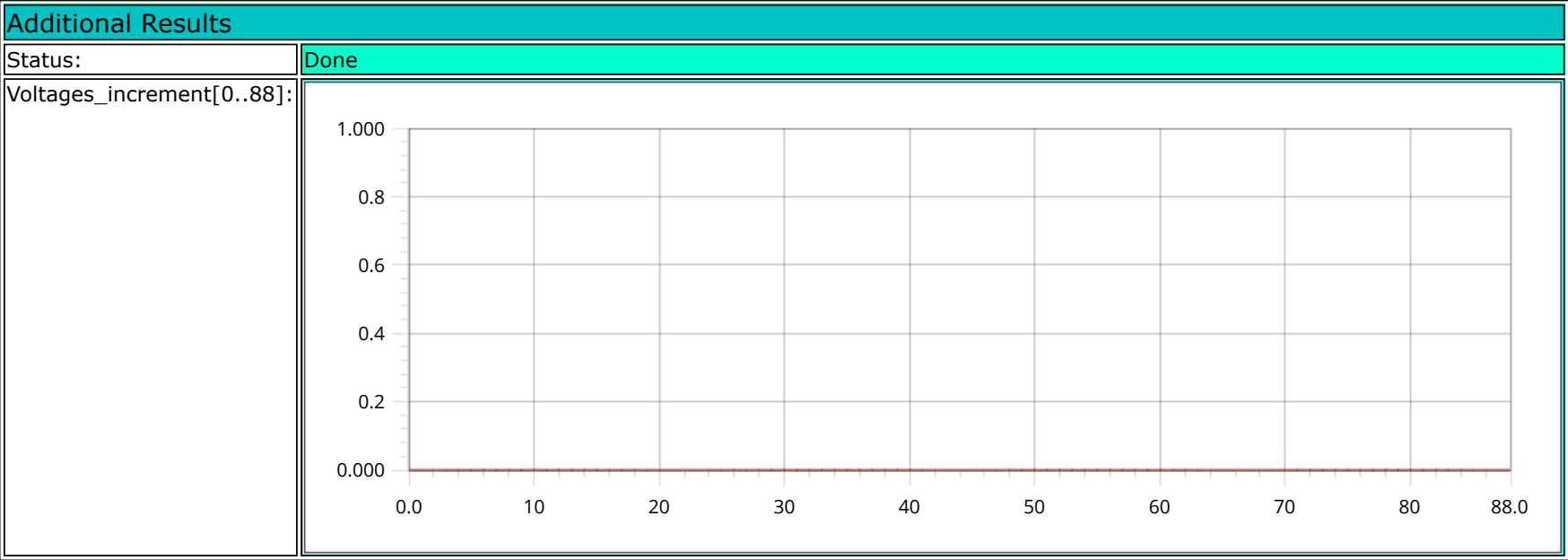
Pass/Fail Test

Status: Passed

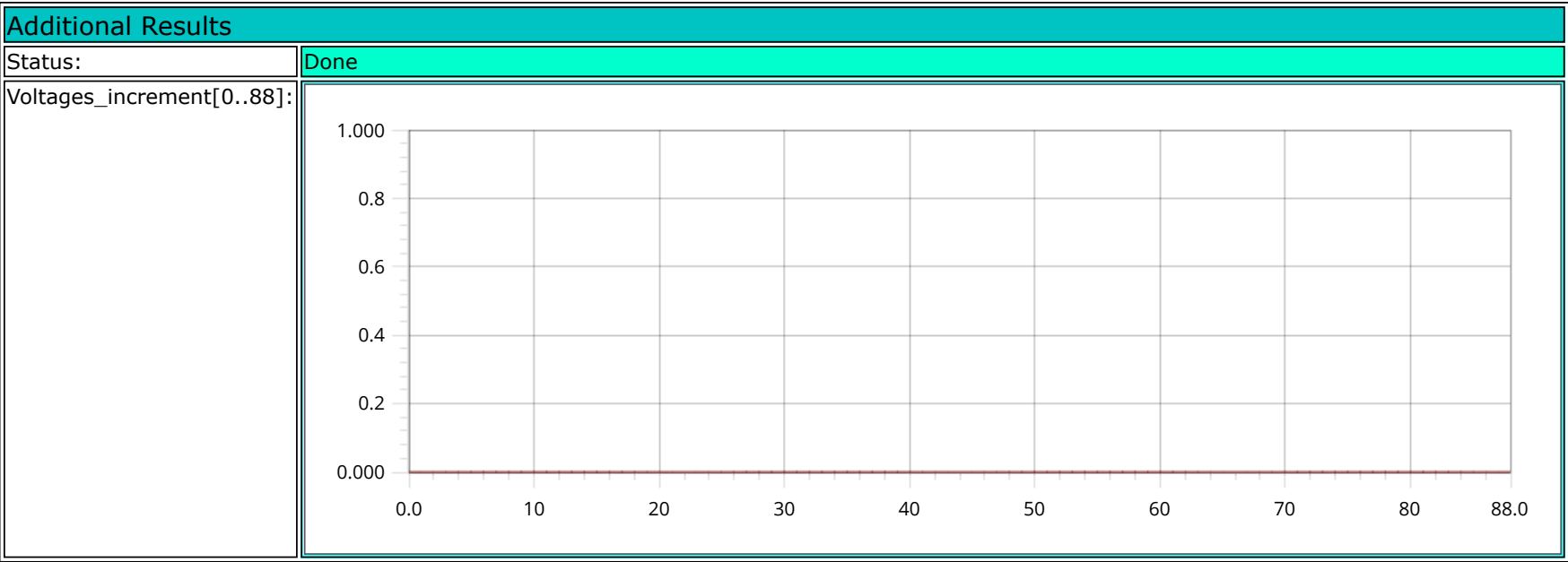


Pass/Fail Test

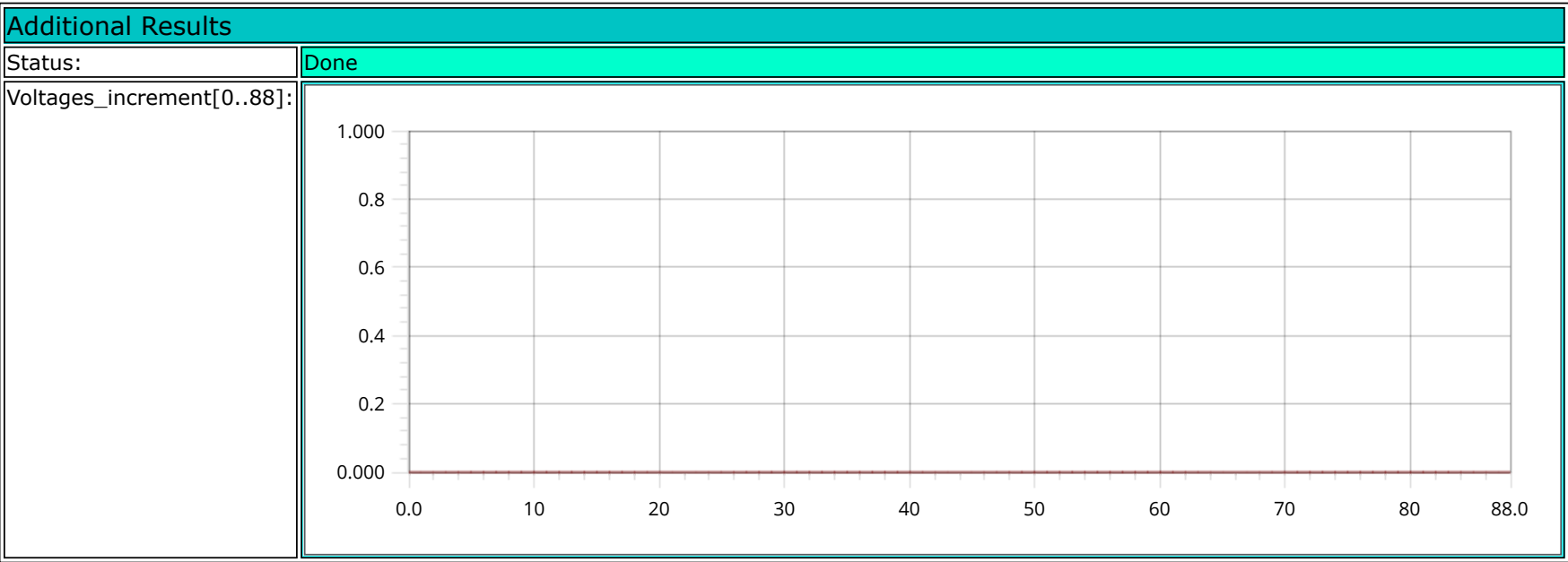
Status: Passed



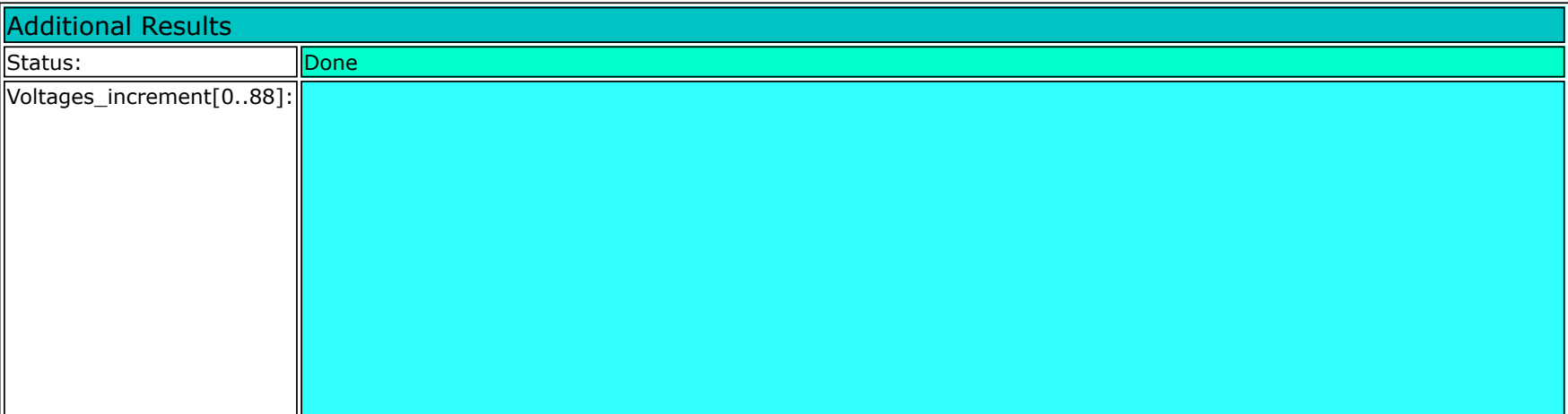
Pass/Fail Test	
Status:	Passed

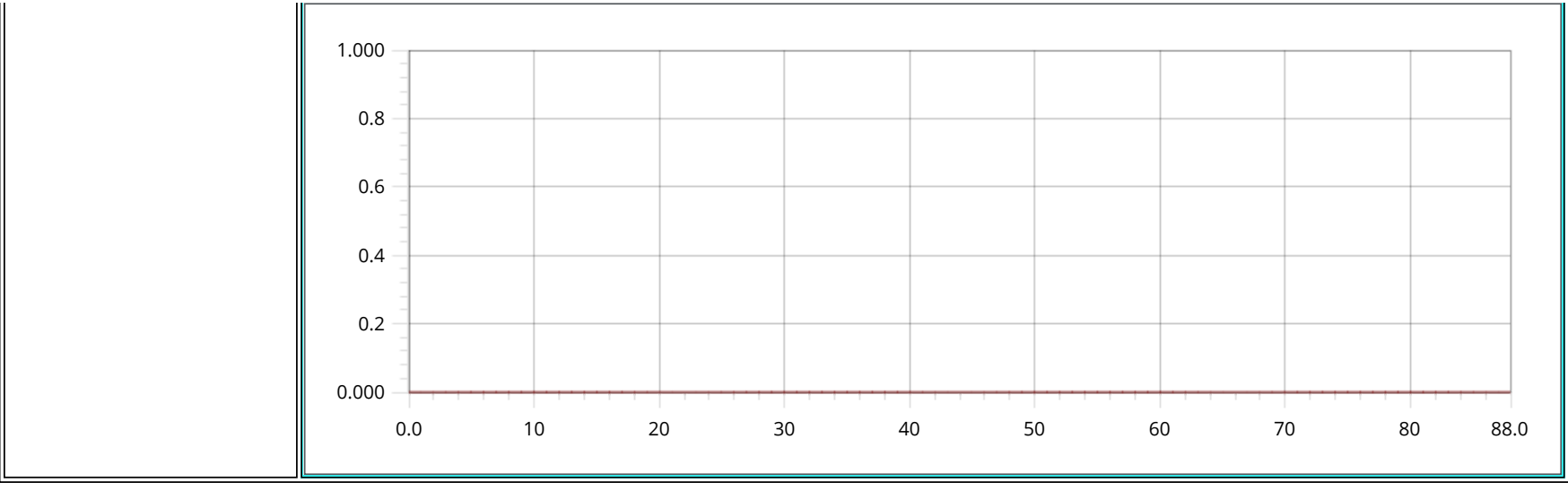


Pass/Fail Test	
Status:	Passed



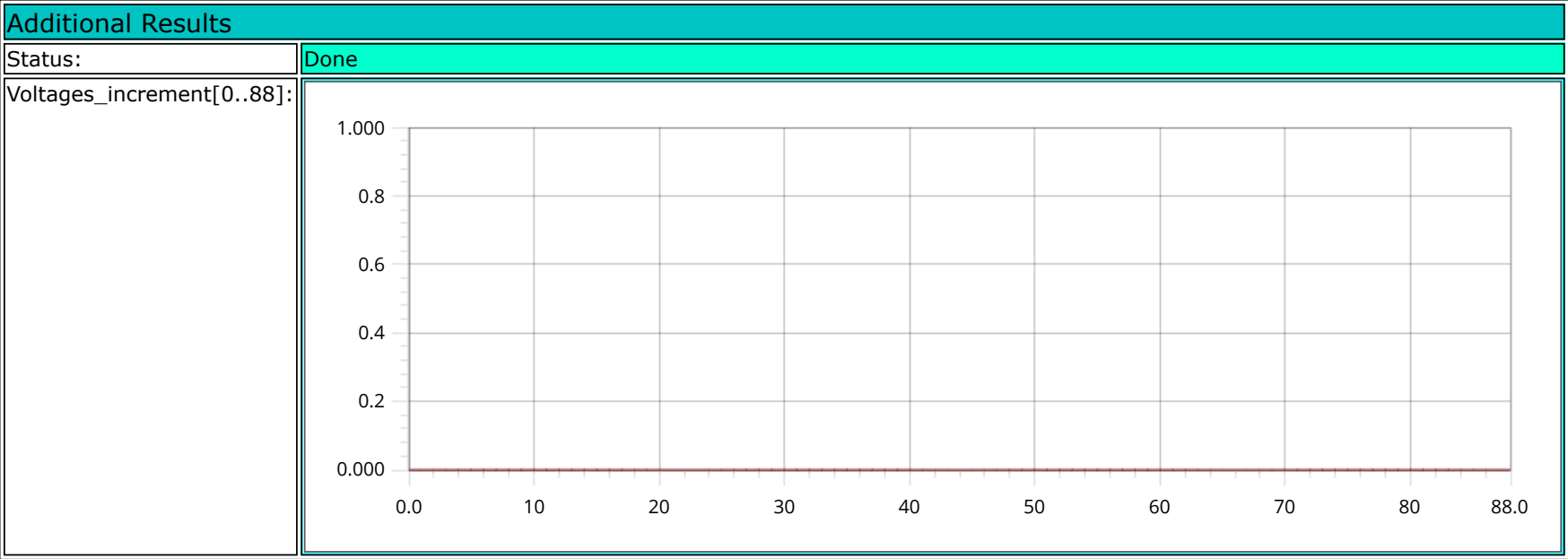
Pass/Fail Test	
Status:	Passed





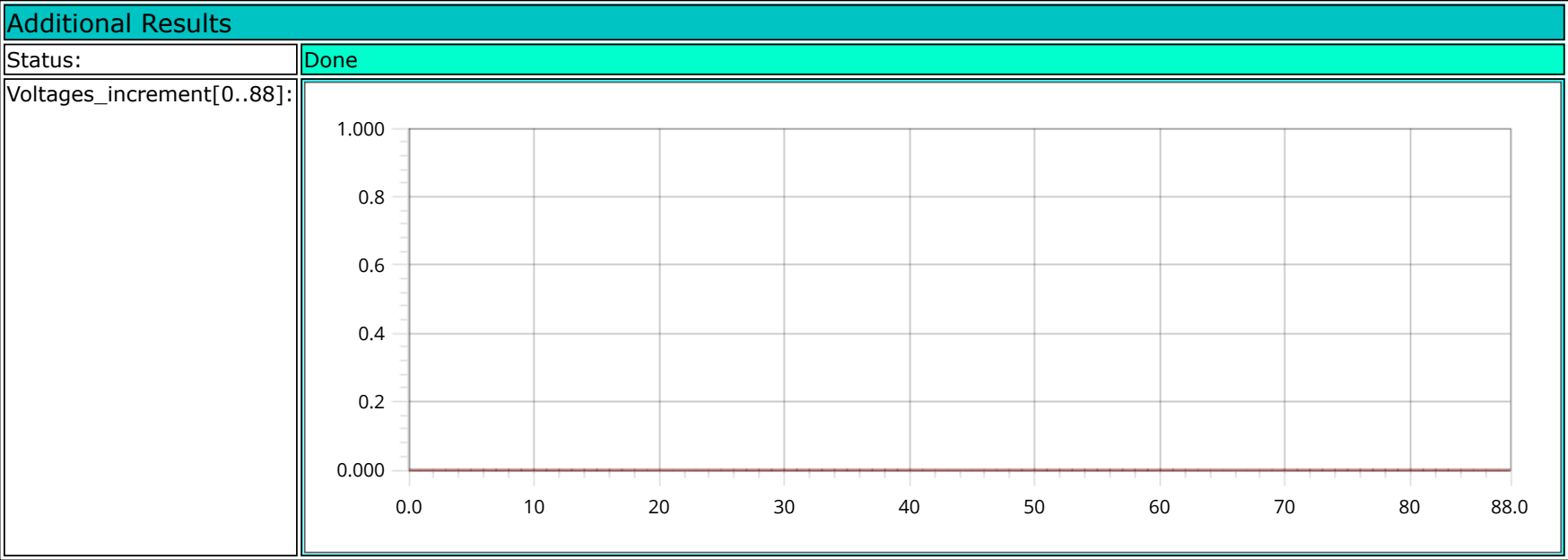
Pass/Fail Test

Status: Passed



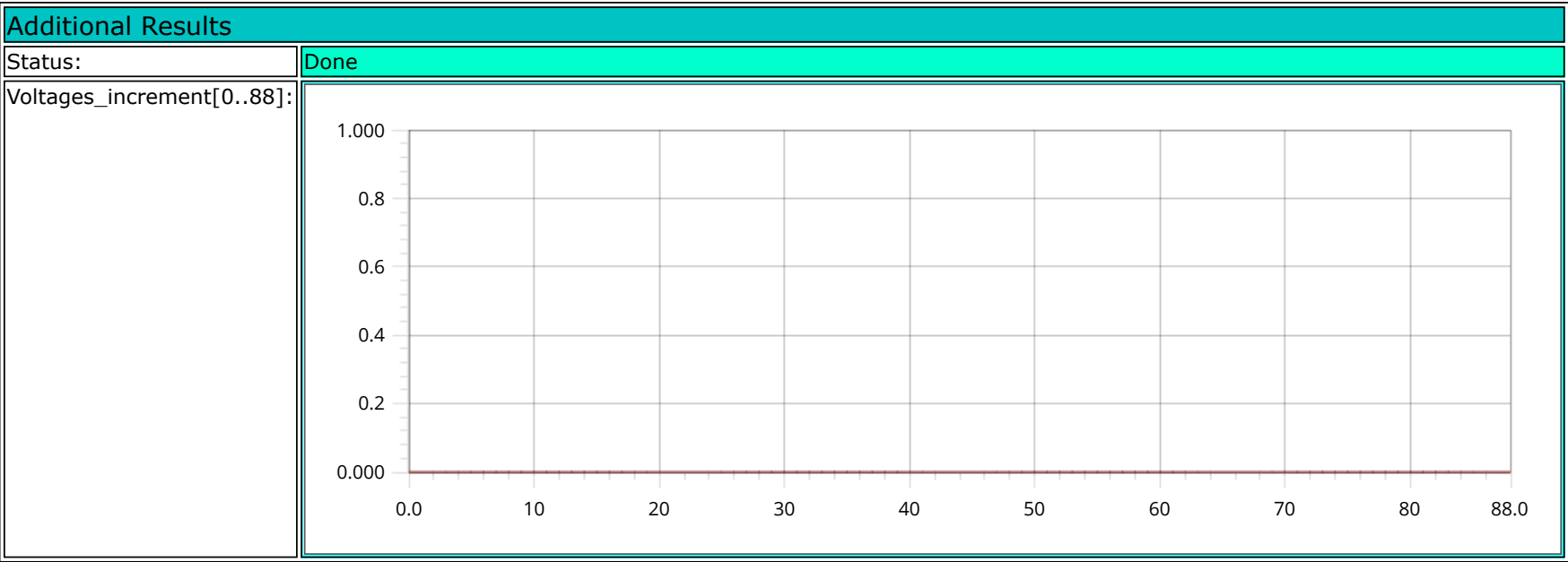
Pass/Fail Test

Status: Passed

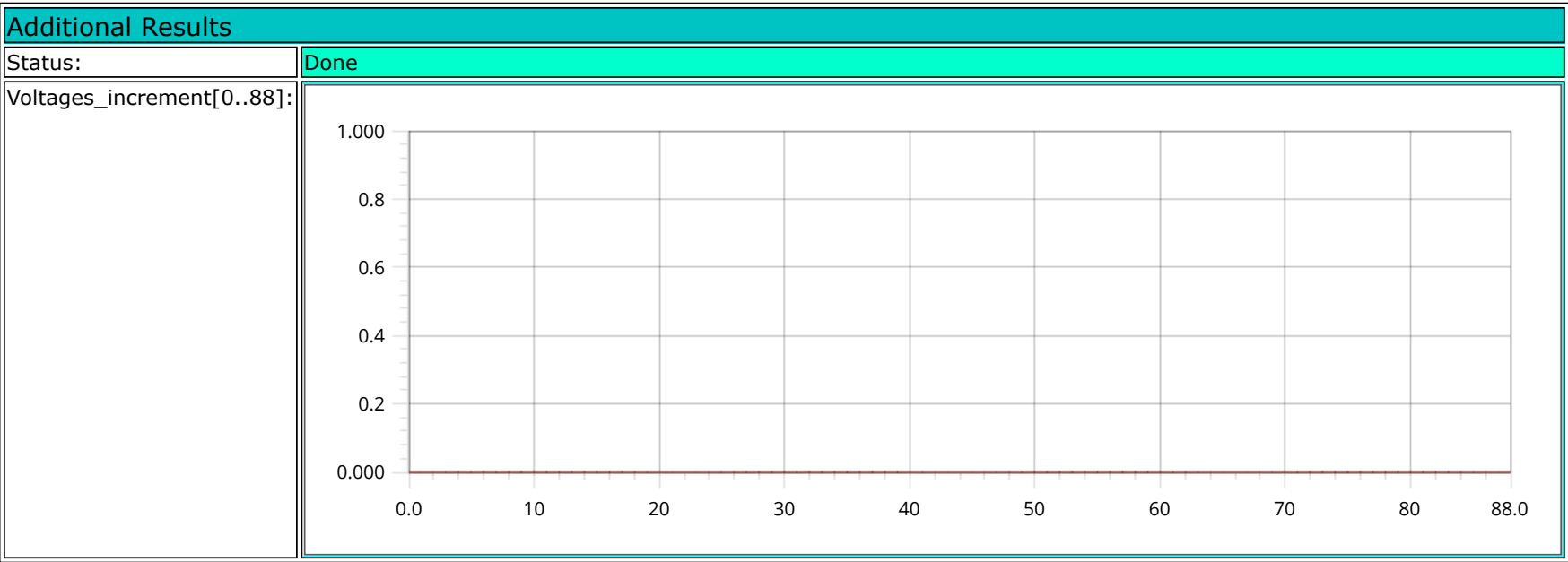




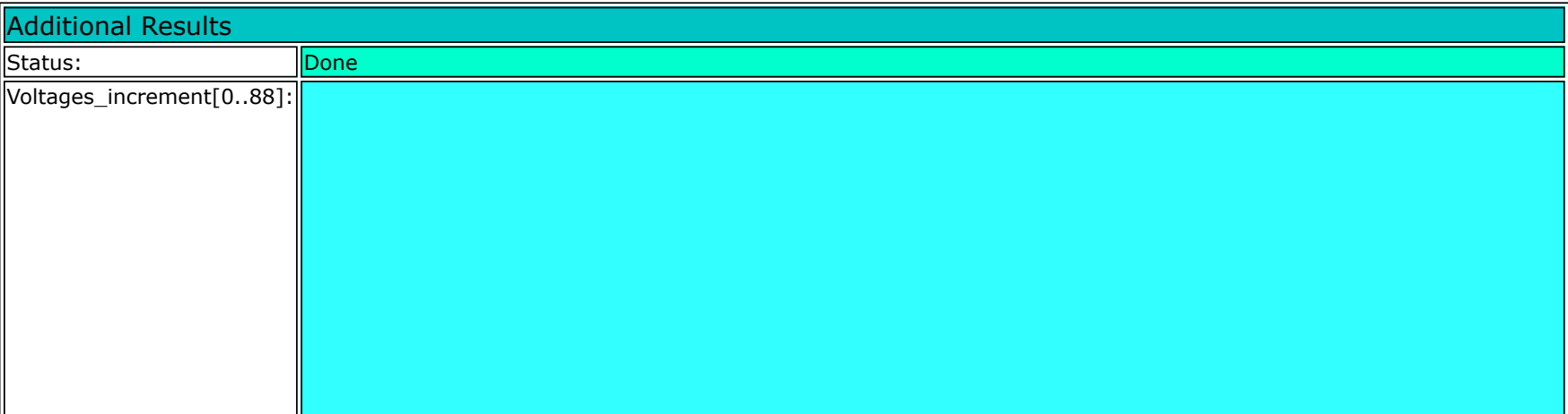
Pass/Fail Test	
Status:	Passed

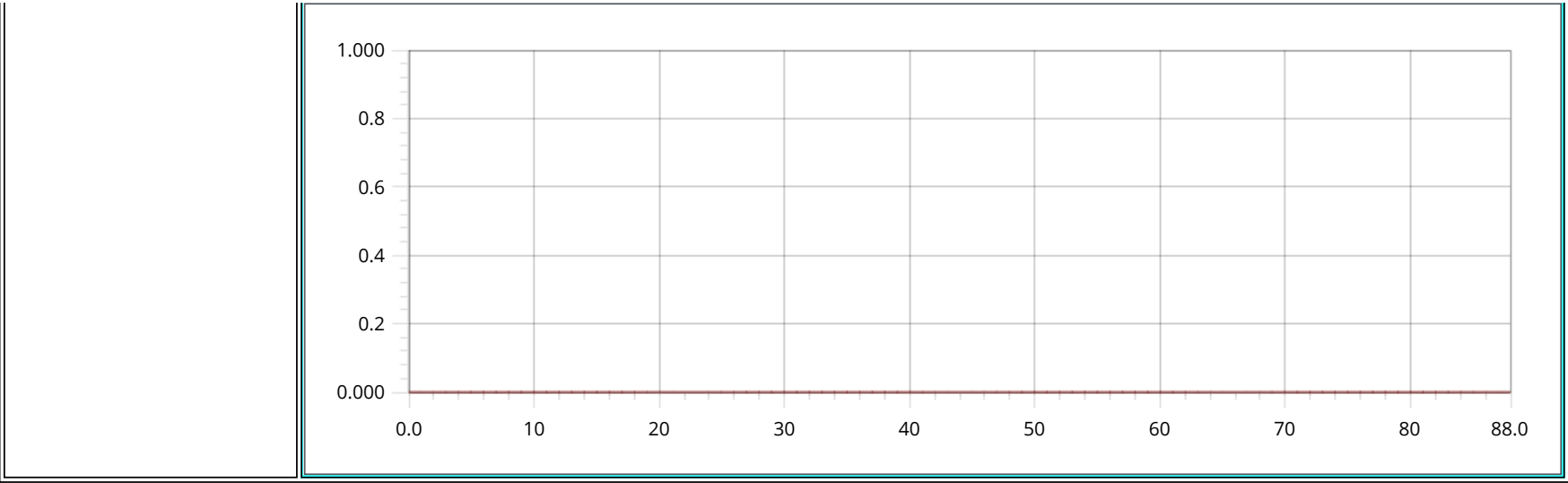


Pass/Fail Test	
Status:	Passed



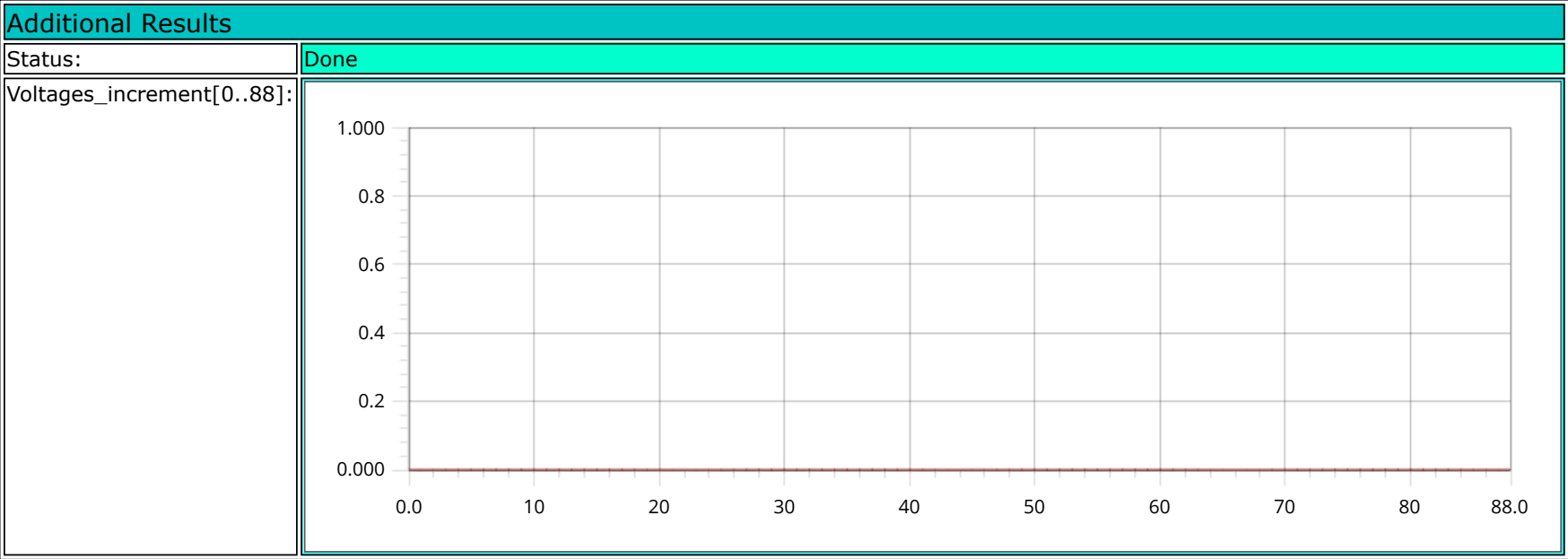
Pass/Fail Test	
Status:	Passed





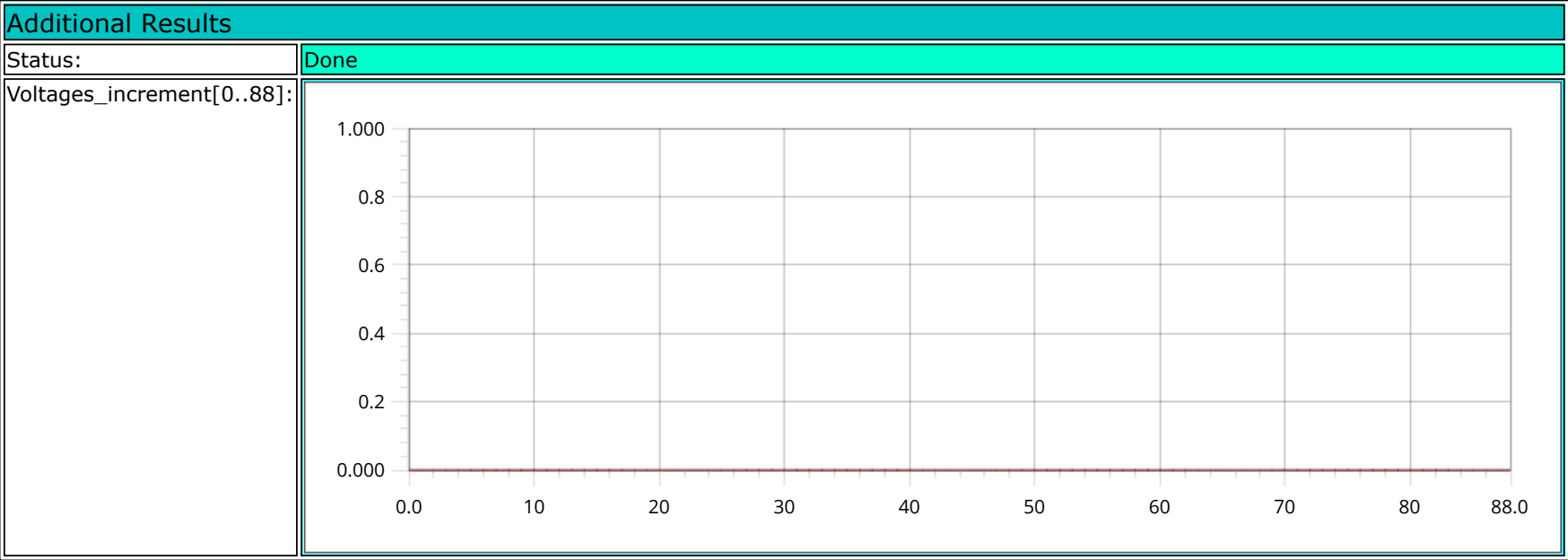
Pass/Fail Test

Status: Passed

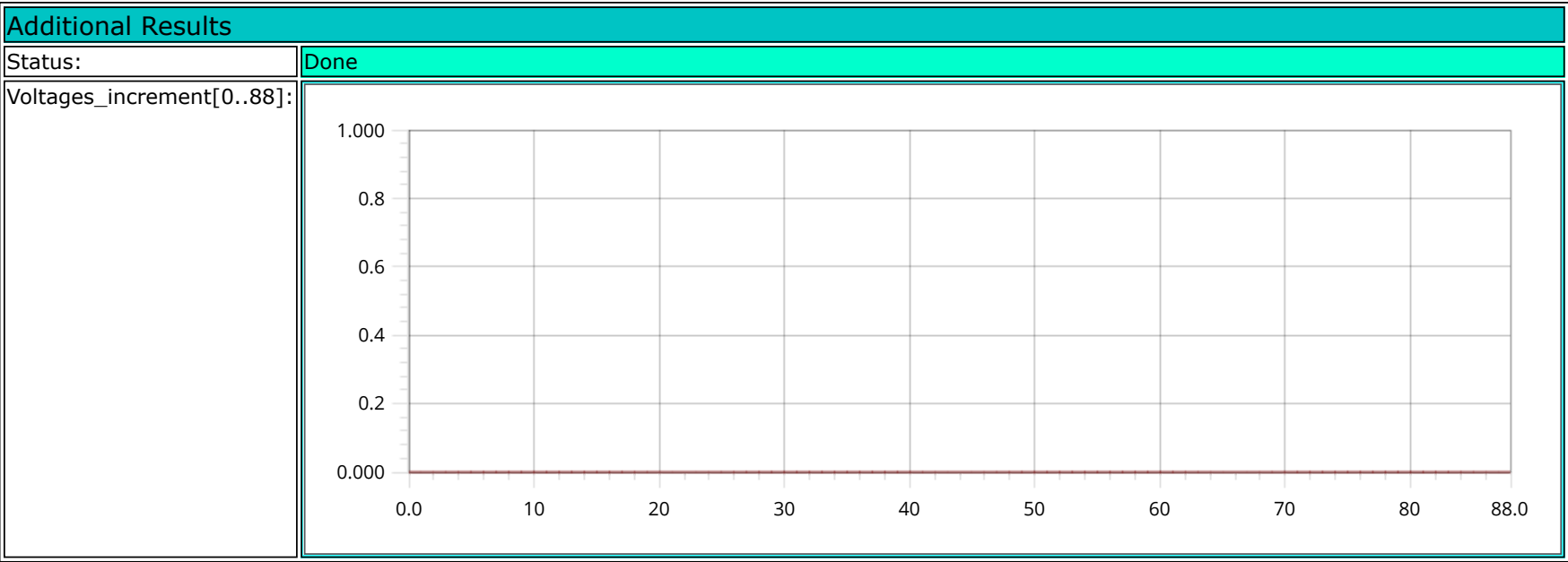


Pass/Fail Test

Status: Passed



Pass/Fail Test	
Status:	Passed



Pass/Fail Test	
Status:	Passed

End Sequence: MainSequence

End UUT Report

