UUT Report

Station ID: IT-W-7303101

Serial Number: NONE

Date: mercoledì 8 novembre 2023

Time: 16:07:55
Operator: administrator

Execution Time: 232.4189095 seconds

Number of Results: 551
UUT Result: Passed
Product: ARI LV
Product type: ARI LV

Comm type: COMM1_RS485

Product FW Version: 56010807 Comm FW Version: 56010000

Begin Sequence: MainSequence

(C:\Users\ITLUBAR5\ABB\LABRND VITTUONE - LabRnD_Shared\TestStand\Sequences\MOD\General_test.seq)

Double_trip_ARI_blocked	
Status:	Skipped
Simultaneous trip_open	
Status:	Skipped
General and Architecture and LED	
Status:	Skipped
Input-Output	
	Skipped
	Skipped
Status: Power Outage	Skipped Skipped
Status: Power Outage	

Modbus Register check	
Status:	Skipped
Modbus settings check	
Status:	Skipped
COMM LED & Button	
Status:	Skipped
Eprom test	
Status:	Passed
Module Time:	218.3741552

Begin Sequence: Eprom writing

(C:\Users\ITLUBAR5\ABB\LABRND VITTUONE - LabRnD_Shared\TestStand\Sequences\MOD\General_test.seq)

Additional Results	
Status:	Done
PopupString:	Check that all the funcionalities of the Device are still working.
Status:	Passed

Pass/Fail Test	
Status:	Skipped
Pass/Fail Test	
Status:	Skipped

	Pass/Fail Test	
	Status:	Skipped
-		
Pass	s/Fail Test	
Statı	us:	Skipped
Pass	s/Fail Test	
Statı		Skipped
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
Additiona	al Results	
Status:		Skipped
Additiona	al Results	
Status:		Skipped
Pass	s/Fail Test	
Statu		Skipped
<u> </u>		
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
	otatas.	ј олирреа
Additiona	al Results	
Status:		Skipped
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
		Jonepea
	Pass/Fail Test	Skipped
	Status:	ј <mark>окирреи</mark>
	Pass/Fail Test	
	Status:	Skipped
	Pass/Fail Test	
	Status:	Skipped
	s/Fail Test	
Statu	JS:	Passed
	Pass/Fail Test	
	Status:	Passed
	Pass/Fail Test	
	Status:	Passed
	Statusi	
	Pass/Fail Test	

Pass/Fail Test	
Status:	Passed
(T. II T	
Pass/Fail Test	Presed
Status:	Passed
Pass/Fail Test	los and
Status:	Passed
Pass/Fail Test	Passed
Status:	rasseu
Pass/Fail Test	
Status:	Passed
	<u> </u>
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
D (F. 1) T	
Pass/Fail Test	Percent
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	<u>'</u>
Status:	Passed
Pass/Fail Test	
Status:	Passed
D (5.1.7.)	
Pass/Fail Test	los and
Status:	Passed
Pass/Fail Test	Dagged
Status:	Passed
Pass/Fail Test Status:	Passed
Status.	Fasseu
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	

Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	, dosed
Status:	Passed
Pass/Fail Test	JI 2222
Status:	Passed
L	- 1
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	ller e
Status:	Passed
Pass/Fail Test	No.
Status:	Passed
Pass/Fail Test	
rass/raii lest	
	Passed
Status:	Passed
	Passed
Status:	Passed Passed
Status: Pass/Fail Test	
Status: Pass/Fail Test Status:	
Pass/Fail Test Status: Pass/Fail Test	Passed
Pass/Fail Test Status: Pass/Fail Test Status: Status:	Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Status: Status:	Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test	Passed Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Status:	Passed Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed Passed
Pass/Fail Test Status: Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status: Status: Pass/Fail Test Status: Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status: Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed Passed
Pass/Fail Test Status:	Passed Passed Passed Passed Passed Passed Passed Passed Passed

Passed

Status:

Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
	rasseu
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
	rdsseu
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
	<u> </u>
Pass/Fail Test	
Status:	Passed
	rasseu
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
<u> </u>	JI
Pass/Fail Test	
Status:	Passed
Julius.	li assen
Dece/Feil Test	
Pass/Fail Test	

Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
D (F. ') T	
Pass/Fail Test Status:	Passed
Status.	rasscu
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
() () () () () () () () () ()	
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed
Pass/Fail Test	
Status:	Passed

End Sequence: Eprom writing

| Firmware Upgrade | Skipped | Skipp

End Sequence: MainSequence

End UUT Report