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Team name:	A5		
Homework number:	HOMEWORK 5		
Due date:	29/10/2023		
Contribution	NO	Partial	Full
Luca Daidone			X
Leonardo Ritter			X
Luca Cordaro			X
Lorenzo Strechelli			X
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Notes:			

Project name	ADC Scan mode and LDR measurement		
Not done	Partially done (major problems)	Partially done (minor problems)	Completed
			X

### Explanation:

We successfully completed the homework.

### Excercise 3a - ADC scan using DMA:

#### CubelIDE Settings:

- We begin by configuring the potentiometer's output as an input for our STM32, specifically setting it as the first input to ADC1, with PA1 designated as ADC1\_IN.
- In the "Analog" section we activate the IN1 in the "Mode" panel. The configuration must be as follow:

ADCs_Common_Settings	
Mode	Independent mode
ADC_Settings	
Clock Prescaler	PCLK2 divided by 4
Resolution	12 bits (15 ADC Clock cycles)
Data Alignment	Right alignment
Scan Conversion Mode	Enabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion M...	Disabled
DMA Continuous Requests	Enabled
End Of Conversion Selection	EOC flag at the end of all conversions
ADC_Regular_ConversionMode	
Number Of Conversion	3
External Trigger Conversion ...	Regular Conversion launched by softw...
External Trigger Conversion ...	None
Rank	1
Channel	Channel 1
Sampling Time	3 Cycles
Rank	2
Channel	Channel Temperature Sensor
Sampling Time	480 Cycles
Rank	3
Channel	Channel Vrefint
Sampling Time	480 Cycles

We enable "Scan Conversion Mode" to allow the ADC to scan and convert multiple channels sequentially.

We enable "DMA continuous request" to ensure that the conversion results are sent by DMA to memory continuously.

We set the "Number of conversions" to 3, considering the 3 different channels to measure.

For the T and Vref channels, we set the sampling time to 480 cycles, as these signals change slowly.

We initiate the conversion by software.

- We employ two DMA controllers, one for ADC data and the other for the USART protocol. We configure the ADC DMA to operate in "circular" mode, which ensures it automatically restarts to write to the first memory cell when the last memory cell is written. We set the direction of the ADC DMA as "peripheral to memory" and the USART DMA as "memory to peripheral."
- USART2 is activated in asynchronous mode, with the following configurations:

▼ Basic Parameters	
Baud Rate	115200 Bits/s
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1
▼ Advanced Parameters	
Data Direction	Receive and Transmit
Over Sampling	16 Samples

- The NVIC table must be set as follow:

NVIC Interrupt Table	Enabled
Non maskable interrupt	<input checked="" type="checkbox"/>
Hard fault interrupt	<input checked="" type="checkbox"/>
Memory management fault	<input checked="" type="checkbox"/>
Pre-fetch fault, memory access fault	<input checked="" type="checkbox"/>
Undefined instruction or illegal state	<input checked="" type="checkbox"/>
System service call via SWI instruction	<input checked="" type="checkbox"/>
Debug monitor	<input checked="" type="checkbox"/>
Pendable request for system service	<input checked="" type="checkbox"/>
Time base: System tick timer	<input checked="" type="checkbox"/>
PVD interrupt through EXTI line 16	<input type="checkbox"/>
Flash global interrupt	<input type="checkbox"/>
RCC global interrupt	<input type="checkbox"/>
DMA1 stream6 global interrupt	<input checked="" type="checkbox"/>
ADC1 global interrupt	<input type="checkbox"/>
USART2 global interrupt	<input checked="" type="checkbox"/>
EXTI line[15:10] interrupts	<input type="checkbox"/>
DMA2 stream0 global interrupt	<input checked="" type="checkbox"/>
FPU global interrupt	<input type="checkbox"/>

The DMA interrupts are automatically set up when we enable the DMA peripherals.

We enable the global interrupt for USART2 to ensure that the USART DMA continues sending data.

There's no need to activate the ADC1 global interrupt since we've observed that the ADC DMA interrupt serves the same purpose.

### Code:

In the infinite loop, first the ADC is started in DMA mode with the "HAL\_ADC\_Start\_DMA()" function, and after that a simple 1s delay is introduced.

The HAL\_ADC\_Start\_DMA() requires as parameters: the ADC handler, an array of data where the DMA peripheral will store the conversion results, called adc\_val[3], and a parameter that specifies the size of the memory pointed by the DMA.

In the "void HAL\_ADC\_ConvCpltCallback()" routine, which is called after the ADC completes all three conversions and the DMA fills the memory, we handle all the conversions as follows:

```
//convert the ADC value from the potentiometer to voltage
float pot = (adc_val[0]*VMAX)/4095;
//convert the ADC value from the T sensor to voltage
float temp = 25 + ( ( ( (adc_val[1]*VMAX)/4095) - V25 ) / AVG_SLOPE);
//convert the ADC value from the internal reference to voltage
float ref = (adc_val[2]*VMAX)/4095;
```

- for the potentiometer, the ADC value is simply converted into voltage
- for the Temperature, the following formula is used:

$$Temperature(in\ ^\circ C) = \frac{V_{sense} - V_{25}}{Avg\_Slope} + 25$$

- for the reference voltage, again the value is simply converted into voltage

All the float results are then written into a buffer string with the “sprintf” function and sent over UART through the HAL-function “HAL\_UART\_Transmit\_DMA”. The program counter will go back to the infinite loop, where the ADC will be started again after the 1s delay.

### Excercise 3b - Light dependent resistor:

#### CubeIDE Settings:

- From the “Green PCB Board” schematic we found that LDR is connected to the PA0 of the Nucleo board, therefore we begin by setting PA0 as an analog input in order to read the LDR. The PA0 pin is connected to the ADC1\_IN0 channel
- In the “Analog” section we activate IN0 in the “mode” pannel. The configuration must be as follow:

ADCs_Common_Settings	
Mode	Independent mode
ADC_Settings	
Clock Prescaler	PCLK2 divided by 4
Resolution	12 bits (15 ADC Clock cycles)
Data Alignment	Right alignment
Scan Conversion Mode	Disabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled
DMA Continuous Requests	Enabled
End Of Conversion Selection	EOC flag at the end of all conversions
ADC_Regular_ConversionMode	
Number Of Conversion	1
External Trigger Conversion Source	Timer 2 Trigger Out event
External Trigger Conversion Edge	Trigger detection on the rising edge
Rank	1
Channel	Channel 0
Sampling Time	3 Cycles
ADC_Injected_ConversionMode	
Number Of Conversions	0
WatchDog	
Enable Analog WatchDog Mode	<input type="checkbox"/>

12 bits resolution is used, with right alignment, and DMA Continuous request must be enabled.

The number of conversions must be set to 1 since we read only one channel.

External trigger source must be the Timer 2.

- We employ two DMA controllers, one for ADC data and the other for the USART protocol. We configure the ADC DMA to operate in "circular" mode, which ensures it automatically restarts to write to the first memory cell when the last memory cell is written. We set the direction of the ADC DMA as "peripheral to memory" and the USART DMA as "memory to peripheral."
- USART2 is activated in asynchronous mode, with the following configurations:

▼ Basic Parameters	
Baud Rate	115200 Bits/s
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1
▼ Advanced Parameters	
Data Direction	Receive and Transmit
Over Sampling	16 Samples

- In the Timer section, TIM2 must be activated by setting its channel 1 to "PWM generation no output". By setting the Prescaler equal to 84-1 and the Counter Period equal to 1000-1 the total period is 1ms. The "Trigger Event Selection" must be set to "Update Event" such as every time a period elapses, the ADC is started.

▼ Counter Settings	
Prescaler (PSC - 16 bits value)	84-1
Counter Mode	Up
Counter Period (AutoReload Register - ...)	1000-1
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable
▼ Trigger Output (TRGO) Parameters	
Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Update Event
▼ PWM Generation Channel 1	
Mode	PWM mode 1
Pulse (32 bits value)	0
Output compare preload	Enable
Fast Mode	Disable
CH Polarity	High

- In the NVIC settings, the ADC1 Global interrupt and the USART2 Global Interrupt must be enabled.

### Code:

In the main(), before the infinite loop, first the ADC is started in DMA mode with the "HAL\_ADC\_Start\_DMA()" function, and then the timer is started with the function "HAL\_TIM\_PWM\_Start()". The HAL\_ADC\_Start\_DMA() requires as parameters: the ADC handler, an array of data where the DMA peripheral will store the conversion results, called adc\_val[1000], and a parameter that specifies the size of the memory pointed by the DMA.

In the "void HAL\_ADC\_ConvCpltCallback()" routine, which is called after the ADC completes all 1000 conversions and the DMA fills the memory, we first calculate the average of the 1000 samples. The average is also converted into voltage.

```
//sum in a variable all the 1000 samples
for(uint16_t i = 0 ; i<SAMPLE_SIZE; i++)
    sum += adc_val[i];

//calculate the average and convert it into voltage
float adc_avg = ((sum / SAMPLE_SIZE)*VDD)/4095.0;
```

The voltage is then converted into resistance following the formula:

$$\text{LDR} = (V_{\text{ADC}} \times 100 \text{ k}\Omega) / (3.3 \text{ V} - V_{\text{ADC}})$$

And the resistance is converted into LUX following:

$$\text{LUX} \approx 10 \times (100 \text{ k}\Omega / \text{LDR})^{1.25}$$

The code is:

```
//convert the voltage into resistance value
float ldr = (adc_avg * LDR_CONV) / (VDD - adc_avg);
//convert the resistance value into LUX value
float lux = 10 * pow((LDR_CONV / ldr), 1.25);
```

note how the pow() function from the math library had to be employed to calculate the power of 1.25.

All the float results are then written into a buffer string with the “snprintf” function and sent over UART through the HAL-function “HAL\_UART\_Transmit\_DMA”. The ADC ISR will be called every 1s, since the DMA waits for 1000 samples and the timer trigger a conversion each 1ms.

Professor comments: