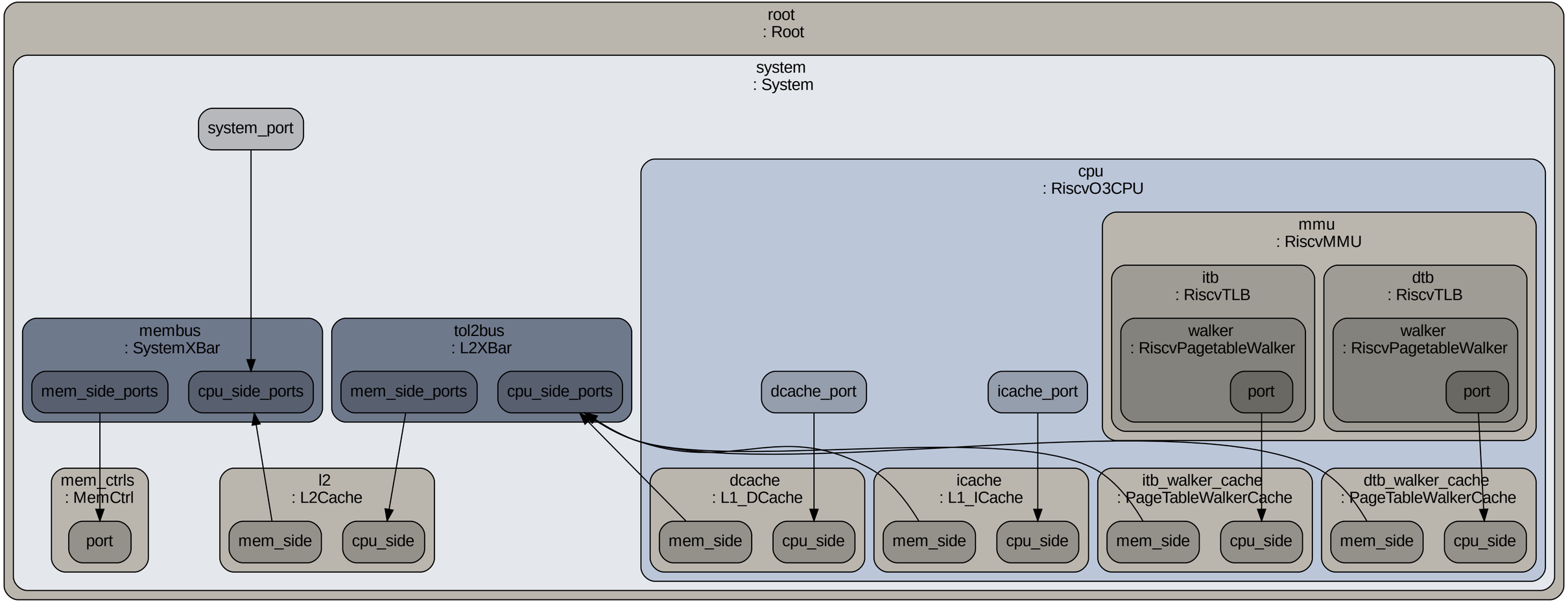
root
: Root
system
: System
system_port
cpu
: RiscvO3CPU
mmu
: RiscvMMU
itb
: RiscvTLB
walker
: RiscvPagetableWalker
port
dtb
: RiscvTLB
walker
: RiscvPagetableWalker
port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
tol2bus
: L2XBar
mem_side_ports
cpu_side_ports
dcache_port
icache_port
mem_ctrls
: MemCtrl
port
l2
: L2Cache
mem_side
cpu_side
dcache
: L1_DCache
mem_side
cpu_side
icache
: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side