CONFERENCE SPEECH "TWO CHALLENGES OF STEALTHY HYPERVISORS DETECTION: TIME

CHEATING AND DATA FLUCTUATIONS"

Hello! I'm Igor from Moscow. I work as a programmer and teacher at university. I'm interested in hidden software research, new security and spyware technologies.

And today I'll tell you about one of these technologies.

Slide 1 Hello

Hello!

Today I'll tell you about a new security system which detects hidden software.

TWO CHALLENGES OF STEALTHY HYPERVISORS DETECTION: TIME CHEATING & DATA FLUCTUATIONS

lgor Korkin

CDFSL 2015

Slide 2 "Agenda"

This talk consists of three parts. First, I'll describe a hypervisor as a "real cyber threat".

Next I'll focus on the existing hypervisor detection methods and their drawbacks.

Finally I'll present a new hypervisor detection system, which improved time based detection method.

Agenda

- Hypervisor (or HYP) as a security threat
- Ways of HYPs detection & their drawbacks
- Time-based detection methods improvements & its challenges



Slide 3 "Any PC can be compared with a big ship"

The idea is that any computer can be compared with a big ship.

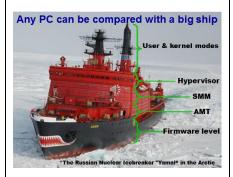
Because decks of a ship look like computers modes.



Slide 4 "Any PC can be compared with a big ship"

To protect a ship we need to make sure that there is no spy or intruder all over the ship from the hold to the top of the mast.

In terms of computer the situation is absolutely the same, we need to make sure that there is no spyware in each computer mode.



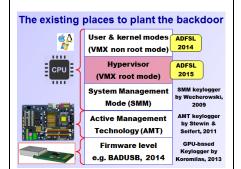
Slide 5 "The existing places to plant the backdoor"

Here you can see the details of different computer modes. Each of these modes can have backdoor.

At the last conference in Virginia one year ago, I discussed kernel mode rootkits; it was the upper deck.

Now we are going deeper.

Today I'll tell you about the detection of new suspicious software... about hypervisors.



Slide 6 "What & where is a hypervisor?" What & where is a hypervisor? All right, we all have laptops and {u-Z-e} use them for various purposes. Of course, any laptop could be infected. Slide 7 "What & where is a hypervisor?" What & where is a hypervisor? To remove malware we use an antivirus and we expect that this antivirus works properly. Slide 8 "What & where is a hypervisor?" What & where is a hypervisor? This is the basic scheme of how computers work. All these software apps are run by the operating system, which is above {E-B-A-V} the hardware.

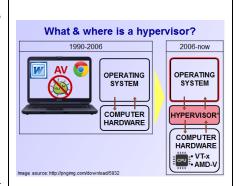
Slide 9 "What & where is a hypervisor?"

But the situation is changing: the vast majority of computers which have been sold in the last 10 years definitely have a CPU — a processor with "hardware virtualization technology {T-E-K-nologi}".

For Intel processors this technology is called VT-X, and for AMD processors it's called AMD-V.

Today we will focus on Intel processors, but all my ideas work well on AMD CPU as well.

Hardware virtualization technology provides a mechanism to put <u>an</u> intermediary between the operating system and the hardware.

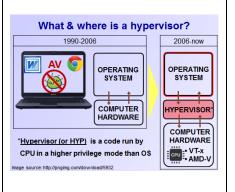


Slide 10 "What & where is a hypervisor?"

This sóftware is called a hypervisor and it runs directly on the hardware.

A hypervisor is a program that operates at a more privileged level than the operating system; it is just below the operating system. In a nutshell, the hardware virtualization is the CPU feature.

Does any computer system support hardware virtualization?



Slide 11 "Hardware virtualization in our computers"

On the left you can see the computers without hardware virtualization, and on the right you can see the computers that support hardware virtualization technology.

If you want to check whether your computer supports this technology or not go to this website or use tools like {Cee-Pee-Uuu-Z} CPU-Z to receive system information.

Now let's go through some features of a hypervisor. We will focus on 5 of them.

CPU without VT-x low performance computers Metbook & no Ultrabook Ultrabook Tablet PC no Does your CPU support Hardware virtualization? Check on ark intel com or use CPU-7

Slide 12 "Five features of HYP & the area of its application "

The first one is that a hypervisor can control all interaction between the operating system and the hardware.

A hypervisor has an unrestricted access to any hardware compónents, like hard disks and network cards.

Second, it's impossible to stop or block a hypervisor by using the operating system, because a <u>hypervisor</u> runs at a more privileged level.

Third, there is no **built-in** tool to detect a hypervisor. Current approaches cannót solve the detection problem in an appropriate way.

Next is that a hypervisor can <u>avoid its detection</u> <u>using a variety of countermeasures</u>, today we will focus on the time cheating.

Five features of HYP & the area of its application

- . HYP can control access to memory, HDD etc
- 2 Impossible to block or delete HYP from OS
- HYP can <u>prevent its detection</u> = stealthy HYP e.g. by using time cheating
- 5. HYP installs invisibly for both users & AVs

leas

What feels ominous is that a hypervisor can be installed invisibly both {BO-UTH} for users and antiviruses. Slide 13 "Five features of HYP & the area of its application " By using the first two advantages, we can create an advanced {ADVA-ANSD} and powerful anti-virus, Five features of HYP & the area of its application which enhances the level of security, or for example a tool to {i-S-o} isolate internet banking from malware. It sounds practical! Obviously, this is a plus of "hardware virtualization". 5. HYP installs invisibly for both users & AVs Of course, we can use all these advantages, but what will happen if hackers use all of them? 1 + 2 + 3 + 4 + 5 =for backdoor Surely, on the dark side, hackers can create the most powerful "backdoor". Any doubts {DA-U-T-S} that this can happen? Overview of a backdoor HYP facilities Slide 14 "Backdoor hypervisor can" record keystrokes using OS Before answering this question let's look at what kind of backdoor a hypervisor can provide. vulnerabilities to load a driver-based HYP This backdoor can record keystrokes, steal all your data or even block your computers. using BIOS-based approach to infect motherboard chip Also a hypervisor can be installed by a driver or it can sit in the BIOS.

Slide 15 "Stealthy hypervisor & well-known examples"

Moreover, the problem with a stealthy hypervisor detection becomes even worse when "several nested hypervisors" sit on your computer.

This is an example of two nested hypervisors which are running at the same time.

In this case, a legítimate hypervisor allóws {ELAUS} us to run two operating systems at the same time,

like Windows and Mac operating systems simultaneously. It sounds great!

At the same time, the malicious hypervisor is loaded first and therefore it is located between the trusted hypervisor and the hardware. That is why it can compromise the trusted one and steal all our data.

Slide 16 "Stealthy hypervisor & well-known examples"

This situation is entirely factual.

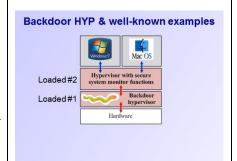
Look at the first two lines. Here you can see two examples of hypervisors, which are loaded by drivers.

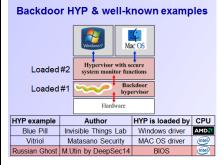
And also at the last DeepSec conference, the investigation about undócumented suspicious hypervisor was

presented. Look at the last line. This ghost {GO-UST} hypervisor uses BIOS to hide its start-up.

All these examples prove that an illegal hypervisor is a "real cyber threat" to our computers,

... to all our data, ... to our business.





We have to meet this challenge; we have to detect a backdoor hypervisor.	
Of course, researchers are trying to solve the hypervisor detection problem.	
Slide 17 "Hypervisor detection tools"	Analysis of hypervisor detection tools
Shue 17 Hypervisor detection tools	Tool Detection Resi- method lient? distribute?
Look at this comparison table with well-known hypervisor detection tools.	Copilot 2004 Deep Watch 2008 Signature based + —
The first column includes the tool titles. The next one holds the implemented detection methods.	Symantec EndPoint Protection 2012 McAfee Deep Defender 2012 Symantec EndPoint Based on the trusted HYP trusted HYP
And the last two columns show if the tools are resilient and portable.	Actaeon 2013 Signature based Proof of Concepts 2008 - 2015 Behavior based New proposal tool Time based + +
	New proposal tool Time based T
Slide 18 "Hypervisor detection tools"	Analysis of hypervisor detection tools
	Tool Detection Resilent? Easy to distribute?
Look at the first two lines. You can see that only hardware tools are resilient to hypervisor	Copilot 2004 Deep Watch 2008 Signature based + —
countermeasures, but they are not portable so they are good only for laboratories.	Symantec EndPoint Protection 2012 McAfee Deep Defender 2012 Based on the trusted HYP +
That is why further we will not consider the hardware tools.	Actaeon 2013 Signature based Proof of Concepts 2008 - 2015 Behavior based New proposal tool Time based + +
	The proposal test Time seeds Time seeds
Slide 19 "Hypervisor detection tools"	Analysis of hypervisor detection tools
	Tool Detection Resi- Easy to method lient? distribute?
In terms of portability {porta-BÍ-LITY}, the software tools are better, but they are unable to detect stealthy	Copilot 2004 T Deep Watch 2008 Signature based + —
hypervisors, <u>hypervisors which are applying countermeasures</u> .	Symantec EndPoint Protection 2012 McAfee Deep trusted HYP Defender 2012 Access 2012 Scientific based +
So far, you can see that there is no tool, which is both {BO-UTH} portable and resilient.	Actaeon 2013 Signature based

Slide 20 "Hypervisor detection tools"	Analysis of hypervisor detection tools
Shae 20 Hypervisor detection tools	Tool Detection Resi- method lient? distribute?
But today I'll present you a detection tool which is both {BO-UT}	Copilot 2004 Deep Watch 2008 Signature based + -
portable and resilient.	Symantec EndPoint Protection 2012 McAfee Deep Defender 2012 Actaeon 2013 Signature based Proof of Concepts 2008 - 2015 Behavior based 2008 - 2015 Time based New proposal tool Time based + +
Slide 21 "What are the principles of these detection methods"	
Now let's focus on the principles of these software detection tools.	What are the principles of these software detection
How do they work and why are they vulnerable?	tools?
Slide 22 "Hypervisor Detection Methods"	Hypervisor detection methods
There are four hypervisor detection methods.	Signature based Based on the trusted HYP
Let's start with signature-based detection.	Behavior based Time based

Slide 23 "Hypervisor Detection Methods"

How does it <u>work</u>? After the installation of a hypervisor, its dispatcher or binary code and corresponding data structures are <u>loaded in the memory</u>. <u>Look at the red rectangles</u>.

We can reliably detect a hypervisor by finding its memory fragments.

But a hypervisor can hide its memory pages and prevent its detection.

For example, a hypervisor can use Extended Page Tables for Intel CPU.

You can see that this detection method is vulnerable to hypervisor countermeasures.

Physical memory Physical memory HYP code HYP is loaded to memory We can detect a HYP using a search in the mem dump Without HYP Stealthy HYP Physical memory HYP physical memory HYP prevents acquiring a real memory dump from OS

Signature based detection

Slide 24 "Hypervisor Detection Methods"

Another method is a detection based on a trusted hypervisor.

In this case, a trusted hypervisor is loaded first and it's close to hardware.

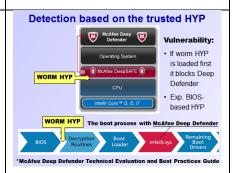
That is why it can prevent any installation of a suspicious one. It's a trusted hypervisor.

Detection based on the trusted HYP W McAfee Deep Detender Operating System OR McAfee DeepSAFE (B) CPU Intel® Care ** 13, 15, 17 The boot process with McAfee Deep Defender BIOS Decryption Routines Boot Loader Melb sys Remaining Dorivers *McAfee Deep Defender Technical Evaluation and Best Practices Guide

Slide 25 "Hypervisor Detection Methods"

Everything looks okay, but what will happen if a suspicious, ... a worm hypervisor installs before the trusted one? What will happen if the worm gains the control first?

Of course, in this situation, the worm hypervisor can prevent the installation of the trusted one; it can



compromise the trusted one. As a result our computers will be unsafe, will be in danger.

Unfortunately, this method is vulnerable to typical man in the middle attack.

Slide 26 "Hypervisor Detection Methods"

The next method is named behavior-based detection.

This method relies on the fact that the result of specific instructions execution depends on whether a hypervisor is present or not. For example if a hypervisor is running by some old AMD processor, the execution of instruction VMSAVE with this parameter causes the system to freeze. Or it stops the system.

But in the same computer without a hypervisor, such execution does not freeze the system.

You see the difference in the behavior.

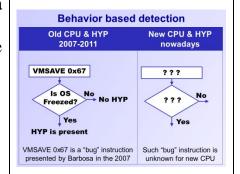
Unfortunately, this method works only for old CPUs and there are no such "freezing" instructions for new hardware. This is a serious drawback for the detection.

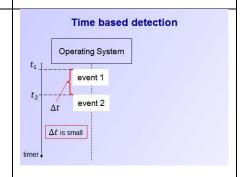
Slide 27 "Time based detection"

The last method for today is time-based detection.

Now I'm going to demonstrate an interaction between an operating system and a hypervisor.

Without a hypervisor all events occur in the operating system without long delays.





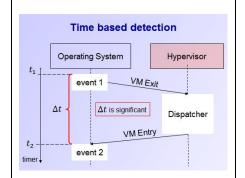
Slide 28 "Time based detection"

But if a hypervisor is present, it intercepts this event.

As a result the hypervisor code starts to run and it needs some time, ... a delay to process this event.

Therefore, the duration of this event becomes longer, in comparison with no hypervisor case.

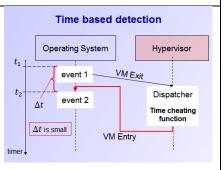
We can precisely detect a hypervisor by measuring this time delay.



Slide 29 "Time based detection"

Everything looks ok, but a hypervisor can compensate this time overhead, ... this delta time by using time cheating function. As a result, this delta time is decreased and becomes the same as in no hypervisor case.

We can see that this method is vulnerable to time-cheating.



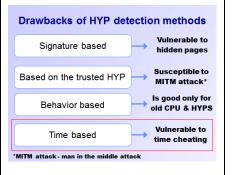
Slide 30 "Conclusion about detection method"

So far we can see that all software detection methods are vulnerable to hypervisor countermeasures.

For my further analysis, I chose the time-based detection. Why?

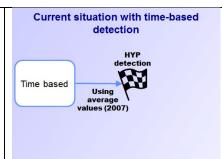
I focused on the fact that a hypervisor always spends time to process an event and this time needs to be hidden. It's impossible to process an event without spending time. That was my idea.

Let's focus on the road map of current situation with time-based detection and mark my contribution on it.



Slide 31 "Current situation with time-based detection"

We know that by using the time values or average time values we can precisely detect only a non-hidden hypervisor.



Slide 32 "Current situation with time-based detection"

But if a hypervisor is using time-cheating it's impossible to detect it using average values, because they are the same. So, a stealthy hypervisor avoids time-based detection.

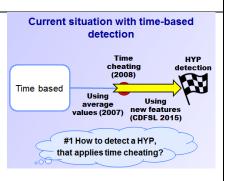
For this situation there are no appropriate time-based detection methods.

Current situation with time-based detection Time cheating (2008) Time based Using average values (2007) #1 How to detect a HYP, that applies time cheating?

Slide 33 "Current situation with time-based detection"

And today, I'll describe how to improve it. Look at the yellow arrow.

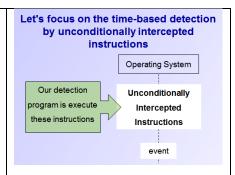
This new method works well even if a hypervisor applies time-cheating.



Slide 34 "Let's focus on the time-based detection"

To detect a hypervisor as "an event" I chose unconditionally intercepted instructions.

Let's focus on them.



Slide 35 "Time-based Detection by Unconditionally Intercepted Instructions"

First of all, what instructions do we call unconditionally intercepted?

Execution of each of these instructions is always intercepted by any hypervisor. Or this event causes a hypervisor dispatcher to start. An example of such instructions is "Cee Pee Uu I Dee".

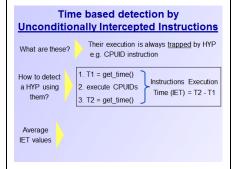
Time based detection by Unconditionally Intercepted Instructions What are these? Their execution is always trapped by HYP e.g. CPUID instruction How to detect a HYP using them? Average IET values

Slide 36 "Time-based Detection by Unconditionally Intercepted Instructions"

How to use them for detection? There are three steps:

Read a time counter, execute a block of these instructions and read the time counter repeatedly.

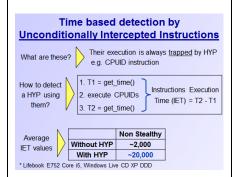
I calculate Instructions Execution Time — "I-Ee-Tee" or just the duration, ... the delta time, ... by subtracting the first value from the second one.



Slide 37 "Time-based Detection by Unconditionally Intercepted Instructions"

To make it clear I'll show how this scheme works. For example, I measured ten (10) "Cee-Pee-Uu-I-Dee" instructions with the help of Time Stamp Counter in two situations: without a hypervisor and with it.

You can see a ten times difference between "I-Ee-Tee" averages without a hypervisor and with a non-stealthy hypervisor.

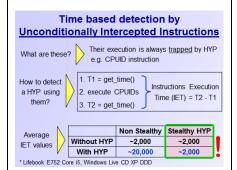


In other words, there is no problem to detect a non-stealthy hypervisor.

Slide 38 "Time-based Detection by Unconditionally Intercepted Instructions"

However, if a hypervisor uses time cheating these average values are the same, ... are absolutely the same. Their values are often not the same but really close to each other and that is why we cannot be sure about whether a hypervisor is present or not.

This is the most complicated situation, that hasn't been taken into account before. What should we do?



Slide 39 "How do we want to detect a HYP?"

Let's think.

What steps can be used to detect a hypervisor?



Slide 40 "What are the steps for time-based detection?"

First of all we need a computer without any hypervisor.

We will measure the time in the following cases: without a hypervisor and with it.

Next we calculate some statistic value and by comparing their values we can find threshold values.

It's similar to the previous situation. But you know that because of time-cheating attack the average values are not applicable. Now we just have to invent the appropriate statistic to replace an average.

Slide 41 "How to find the appropriate statistic?"

To do this to invent the statistic value let's move on to the question.

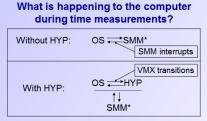
What is happening to the computer during time measurements?

What is happening to the PC during time measurements?

Slide 42 "What is happening to the computer during time measurements?"

Look at the interaction between the operating system and other modes during the measurements of "I-Ee-Tee".

Without a hypervisor, the operating system interacts only with System Management Mode — S-M-M, which is the most privileged mode and its interruptions occur randomly. S-M-M suspends the work of the



SMM — System Management Mode, works lower than HYP & OS SMM interrupts — occur randomly & suspend PC for a short time VMX transitions — catch execution of every CPUID instruction

operating system only for a short time.

If a hypervisor is loaded, such interruptions to S-M-M suspend both {BO-UTH} of them, ... the operating system and the hypervisor.

In a nutshell, the S-M-M is the most privileged mode and its interruptions occur randomly.

Slide 43 "Different switching between modes"

Let's specify the process of modes switching during "I Ee Tee" measurements.

Without a hypervisor, instructions are executed in the operating system.

This process is indicated by a small blue loop.

Randomly generated {jenerated} interruptions to the S-M-M are indicated as dashed blue arrows {ARO-UZ}.

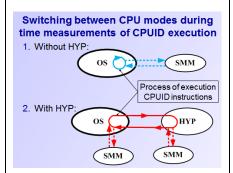
A scheme with a hypervisor is shown below. The hypervisor catches {CAT-CHIZ} the execution of every

"Cee Pee Uu I Dee" instruction from the operating system. This execution is indicated by a red oval loop.

The S-M-M is duplicated {DUPLICATID} for better clarity. Of course, there is only one mode.

S-M-M interruptions occur either from the operating system or from the hypervisor in a random way.

Now let's compare these cases and think about "I Ee Tee" peculiarities here.



Slide 44 "Theoretical analysis of switches between modes"

First due to the random nature of the interruptions to the S-M-M, the "I Ee Tee" is a random variable.

Another point is that "I Ee Tee" is randomly distributed in several fixed layers.

This layered nature is also caused by the interruptions to the System Management Mode.

Let's focus on the following "I Ee Tee" characteristics: average value and variability indexes {INDISIZ}, such as the <u>number of layers</u>, <u>variance</u> and <u>fourth order moment</u>.

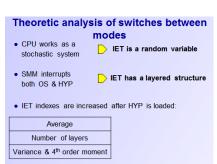
After a hypervisor is loaded all these statistics are increased in comparison with a no-hypervisor case.

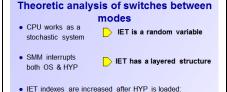
Slide 45 "Theoretical analysis of switches between modes"

We remember that a hypervisor can reduce the average value by time cheating.

In this situation, we have to apply only variability indexes as a possible way for detection.

Let's check these three ideas about random nature with layers and variability indexes.





Average Time-cheating by HYP

Number of layers

Variance & 4th order moment

Stealth HYP detection

Slide 46 "Let's check these three ideas by experiment"

The experiment was done in the following way.

Let's check these three ideas by experiment



Slide 47 "Scheme of the present experiment"

First, I use a tiny {TA-INY} hypervisor with minimum functionality. It has <u>basic functions</u> and <u>time</u> <u>cheating</u>. This is the most complicated case for detection.

Next, you can see the source code fragment for measuring "I Ee Tee".

The inner loop runs one thousand (1000) times and measures time, after that I suspended this driver for a two-second delay to make observations {ABZERVATIONZ} independent.

The outer loop runs ten (10) times. As a result, I got a matrix with one thousand (1000) by ten (10).

Slide 48 "Experimental matrix of IET values"

Here you can see the example of this matrix for no hypervisor case.

Such matrices were measured for both {BO-UTH} situations without a hypervisor and with a hidden one.

To make it clear let me show you fragments of columns with the help of a "scatter plot".

Instruction Execution Time in CPU ticks*						
	Number of outer loop interactions					tions
		1	2	3		10
	1	2004	2008	2048		2044
Number of inner loop interactions	2	2000	2008	2048		2048
ner	3	2012	2004	2048		2044
of in racti	4	2008	2000	2048		2048
ber	5	2008	2004	2044		2040
 						
_	1000	2008	2000	2040		2036
* without HYP Lifebook E752 Core i5 Windows Live CD XP DDD						

Slide 49 "Experimental results"

Here you can see the scatter plots for two mentioned cases.

This is the plot for the case with no hypervisor, and here is the plot with one hidden hypervisor.

Each point on the plot represents a value from the column. Numbers of experiments are on the x-axis.

The y-axis shows the execution {EX-SECUTION} time in CPU ticks or "I Ee Tee" values.

Let's compare the average values of these two plots.

Analysis of the experimental results No HYP Stealthy HYP Present Stealthy HYP Present

Slide 50 "Experimental results" {magnify average values}

We can see that due to time cheating the average values of these two cases are the same.

The average is about two thousand and seven (2007).

As a result, existing time based detection method does not work.

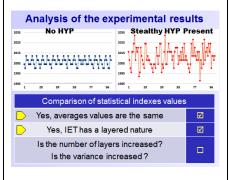
Analysis of the experimental results No HYP Stealthy HYP Present Stealthy HYP Present

Slide 51 "Experimental results" {magnify on the layers}

However, something is different. Let's focus on the structures of these plots.

Without a hypervisor, a set of points consists of few horizontal layers. There are just 4 layers and no outlies. With one hypervisor present, a set of points consists of several layers with a lot of outlies.

All these plots in both {BO-UTH} cases have a layered structure.



Slide 52 "Experimental results"

Moreover, we can see that after a hypervisor has been loaded, the number of layers has grown three-fold.

Also, a variance has increased {INCRI-ST} five-fold.

We see that all variability indexes are increased {INCRI-ST} so they can be used for detection.

Perfect!

Analysis of the experimental results No HYP Stealthy HYP Present Stealthy HYP Present

Slide 53 "Perfect! We've done it!"

We can conclude that all my theoretical {THIO-retical} principles have been confirmed by experiments.

In particular, "I Ee Tee" is a random variable, whose spread increases after the loading of a hypervisor.

Now we are focusing on three types of statistics: number of layers, variance and fourth (4th-) order moment.

To detect a hypervisor we need to compare samples in both {BO-UTH} cases without and with a hypervisor. But it isn't so easy! Let's look at the reasons.

Yeah! We've done it!

We've found the following "resilient" statistics:

- · number of horizontal layers
- variance
- $V = \frac{\sum (x_i \bar{X})^2}{n}$
- 4th order moment
- $\bar{M}_4 = \frac{\sum (x_i \bar{X})^4}{n}$

Let's use statistical tests to complete samples

Slide 54 "But also IET has the following anomalies:"

My experiments {I-KSPERiments} show that, apart from the layered {LAYYED} nature "I Ee Tee" samples also include partial deviation and other <u>anómalies</u>, like <u>noise</u> which significantly change variability indexes, so that they cease {SEASE} to be useful for detection.

In addition, our analysis shows that "I Ee Tee" is not normally distributed.

Let's think which of the statistical tests are applicable in our situation.

But also IET has the following anomalies:

- IET samples include noise
- · IET samples statistics fluctuate daily
- IET random variable is not normally distributed

What statistical tests are appropriate to compare these samples?

Slide 55 "Possible ways to compare samples"

First, we cannot use classical statistical or parametric tests, for example, Student's t-tests, because all of them require normal distribution.

Non-parametric tests are possible. But, our experiments show that these tests, for example a Wilcoxon test, are not precise enough for our experimental data.

Possible ways to compare the samples

Classical parametric tests
 Student's t-test
 ANOVA & ANCOVA

Require normal distribution

Give bad approximation

Slide 56 "Possible ways to compare samples"

To compare these samples I use <u>another non-classical method</u>, which was introduced by Mark Kornfeld in Russian and later by Johann {Yo-HANN} Strellen in English. Using this method the <u>confidence interval</u> is calculated {CAL-KJU-LATED} as the interval between the minimum and maximum values.

Possible ways to compare the samples

Classical parametric tests

Student's t-test

ANOVA & ANCOVA

Require normal distribution

Give bad approximation

Kornfeld (USSR'65) or Strellen (FRG'01) method:

Let $T_1, T_2, ... T_n$ is a sample, therefore confidence interval: (T_{MIN}, T_{MAX}) confidence level: $P = 1 - 0.5^{n-1}$

Here is the corresponding <u>confidence level</u> . Let's use this method.	
Slide 57 "Calculate statistics & variation intervals"	Calculate statistics & variation intervals 1. Calculate variances for each matrixes of IET values: No HYP HYP present
First, I measured "I Ee Tee" for two situations: without a hypervisor and with it.	1 2 10 1 1000 1 1 2 10
Then I calculated statistic value, for example, variance according to the matrixes columns {CALOMS}.	2. The result:
After that, I compared these variation intervals with each other: blue and red intervals.	
Slide 58 "Calculate statistics & variation intervals" As a result, I <u>found out</u> that the variation intervals overlap so much, that I could not get the threshold value to detect a hypervisor.	1. Calculate variances for each matrixes of IET values: No HYP 1 2 10 1 1000 V1 V2 V10 2. The result: instability of statistics values No HYP V1 V2 V10 V1 V2 V10 V1 V2 V10 V1 V2 V10 Overlap too much
Slide 59 "Data fluctuation: instability of statistics"	Data fluctuation: instability of statistics
What are the reasons for this data fluctuation?	What are the reasons for
We need to understand why the calculated statistics are so different.	the instability of statistics?

Slide 60 "Reasons of data instability or data fluctuations are outliers & jumps"

To answer this question I repeatedly made scatter plots and now show you typical anomalies.

{magnify up-left} We see that this scatter plot includes a high outlier {OUTLAE} apart from basic layers.

Also, the plot includes one jump.

{magnify up-right} This is an example of high outliers.

{magnify left-down} And this is an example of low frequency values.

All these anomalies considerably influence the variance and the other statistics.

As a result, I cannot calculate the appropriate {APRO-UPRIAT} threshold values.

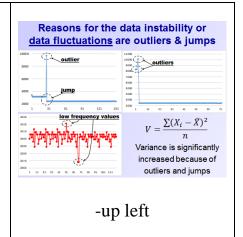
Slide 61 "How to overcome negative influence of these jumps & outliers"

Let's look at how to reduce the influence of these anomalies.

To remove outliers and low frequency values I applied filtration.

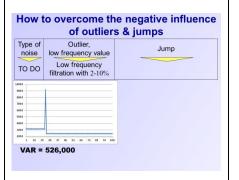
As a result, all values whose frequency is lower than 2, 5 or 10% are deleted.

Here is the original plot with one outlier and jump. The corresponding variance is about <u>half a million</u>.



-up-right

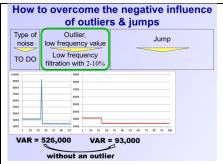
-left-down



Slide 62 "How to overcome negative influence of these jumps & outliers"

After the filtration, one outlier {OUTLI-YA} was deleted, and variance became about one hundred thousand. You see the difference.

Let's move on to the jump.



Slide 63 "How to overcome negative influence of these jumps & outliers"

To correct each jump I calculated the statistic values, for example variance, before and after the jump.

Next, I added these values using their section lengths. In other words, I <u>averaged out</u> the statistic before <u>and after the jump</u>.

Finally, the variance is a bit more than a hundred.

These measures significantly helped me to <u>stabilize</u> statistics and their <u>variation intervals</u>.

How to overcome the negative influence of outliers & jumps Type of noise TO DO Outlier, low frequency value Low frequency low frequency filtration with 2-10% $V = \frac{a}{a+b} * V_a + \frac{b}{a+b} * V_b$ VAR = 526,000 VAR = 93,000 VAR = 123 without an outlier without a jump

Slide 64 "Obtain different statistical values on different days"

I decided to test these ideas every day and do more experiments.

I decided to test these ideas & try to detect a HYP every day



Slide 65 "Obtain different statistical values on different days" Obtain different statistical values on different days Here you can see variation intervals for both cases: without and with a hypervisor, which were calculated during three days. Day first, second and third. Slide 66 "Obtain different statistical values on different days" Obtain different statistical values on different days The point is that I received different variation intervals on different days. So it seemed impossible to get the appropriate threshold values to detect a hypervisor. Slide 67 "Data fluctuation: lack of repeatability" Data fluctuation: lack of repeatability Now let's move on to the question – how to overcome this lack of repeatability? {RE'PETA'BELETE} How to overcome this data fluctuation every day?

Slide 68 "How to overcome lack of repeatability?"

I tackled this issue by using a two-step way to calculate new statistics. First, I calculated, for example, variance, for each column in the matrix and got a set of values: Vee-one (V1), Vee-two (V2) and so on Vee-ten (V10), which I considered {CONSIDED} as a "new sample".

Second step was to calculate the statistics of this new sample. Here "Vee-bar" was the average of this new sample. As a result overlapping of <u>new statistics intervals</u> was seriously reduced.

Another idea was to repeat measurements within 10 days.

This period was enough to stabilize variation intervals.

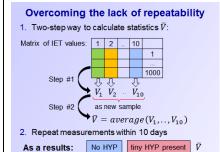
These two approaches were sufficient to get appropriate threshold values. It looked like a success.

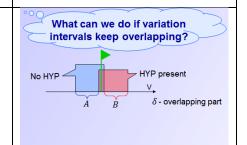
Slide 69 "What can we do if variation intervals keep overlapping?"

But, what can we do if variation intervals keep overlapping? Look at the green flag.

If the calculated value is in the overlapping part it is impossible to detect a hypervisor precisely.

What should we do in this situation?





Slide 70 "What can we do if variation intervals keep overlapping?"

The first idea was to repeat "I Eee Tee" measurements and recalculate statistics.

Here you can see the probabilities of errors of the first and second kinds {KA-INDS}.

The thing was that because this overlapping part is really small, the probability of repeating this negative situation is quite low. It is highly unlikely that the calculated value occurs in the overlapping part again because of the multiplication rule for independent events.

Let's join all the previous steps to calculate the threshold values.

What can we do if variation intervals keep overlapping? No HYP HYP present A B δ - overlapping part Type errors Decision Reality I HYP is present no NYP $\alpha = \delta / A$ II no NYP HYP is present $\beta = \delta / B$ Prepeat data acquisition & stats calculation

Slide 71 "Threshold values calculation"

The first step was to obtain the data for ten days. I got five matrixes every day for each case: without a

hypervisor and with my tiny {TA-INY} hypervisor. All in all I obtained fifty matrixes for each case.

Next was to <u>calculate</u> statistics using a two-step way.

The final step was to calculate threshold values. The results are in the table.

probability of type I and II errors comes to its min

Slide 72 "Example of threshold values"

This is the table with threshold values.

If the calculated number of layers is "5", so it is smaller than threshold "7" and <u>it means</u> there is <u>no hypervisor</u>. If the calculated number of layers is bigger than "8", it means that at least one hypervisor is present.

Exam	pie oi ti	II 6211010	values
	•		

Intel Core 2 Duo E6300 + Windows 7 x32

	Filtration Threshold values		Type I	Type II	
Statistics	level	No HYP	HYP is present	error, %	error,
Number of layers	0	≤7	≥8	4	0
Variance	0	≤14	≥ 18	2	0
Moment	0.1	< 679	> 947	2	0

The complete table with threshold values is in my paper.

Slide 73 "How to detect stealthy hypervisors?"

Let's sum up all previous ideas to formulate how to detect a stealthy hypervisor.

How to detect stealthy hypervisors?
Step by step method:



Slide 74 "How to detect a stealthy hypervisor?"

First is to guarantee that there is no hypervisor in BIOS by reflashing it.

Next is to reinstall operating system, which is downloaded from the reliable sources.

The first two steps look like performing a factory data reset in computers.

Next is to calculate threshold values from the no hypervisor case.

After that install supplementary software and monitor messages about a hypervisor presence.

How to detect stealthy hypervisors?

Stages	Stage description
	Flash BIOS with a trusted image or firmware
Preliminary (calculate	2. Install OS
thresholds)	Get threshold values in case where no HYP is present
	4. Check in a loop if a hypervisor is present
Operational	5. Install Office etc
(detect a	6. Monitor messages about a hypervisor presence
hypervisor)	7. Go to step 3 to adapt the tool to new legitimate HYP

Slide 75 "How to detect a stealthy hypervisor?"

You can say, all right Igor, but your head is in the clouds.

The first two steps are good only for crazy computer experts. They are not appropriate for everyday life.

Yes, you are absolutely right. Let's keep both {BO-UTH} feet on the ground.

How to make it userfriendly?

How to detect stealthy hypervisors? Stages 1. Flash BIOS with a trusted image or firmware 2. Install OS "Detached BIOS Chip, wikipedia.org/wiki/EIOS "Eprom programmer, batronix.com/versand/programmiergerace/BX22Pindex.htm"

Slide 76 "How to detect a stealthy hypervisor?"

Now I have the basis to guarantee no hypervisor presence in the running computer just by checking the structure of the scatter plot.

The details are {AR} in the paper.

How to detect stealthy hypervisors?

Stages	Stage description		
	1. Guarantee the absence of a HYP by checking		
Preliminary (calculate	a scatter plot (coming soon)		
thresholds)	2. Get threshold values in case where no HYP is		
	present		
	3. Check in a loop if a hypervisor is present		
Operational	4. Install Office etc		
(detection)	5. Monitor messages about a hypervisor presence		
	6. Go to step 3 to adapt the tool to new legitimate		
	HYP		

Slide 77 "Detection: architecture & source code"

Here you can see the architecture of this detection system.

At the preliminary stage, I use "measure IET" block, tiny hypervisor, block for calculating statistics and getting thresholds.

At the operational stage, I calculate statistics and compare their values with thresholds.

This detection system is developed using Cee + + and MATLAB, the source code is free & available here.

Preliminary Tiny HYP Measure IET

(calculate thresholds)

Operational (detection)

Calc stats & get thresholds

Calc stats & compare with thresholds

Source code components

Details

Source co	Details			
Tiny HYP Measure IET	Windows x32 drivers & their config tools	Visual Studio & WDK, C++ asm		
Calc stats 8	Matlab			
http://github.com/lgorl/orkin/HypopyjeoreDetection				

http://github.com/lgorKorkin/HypervisorsDetection

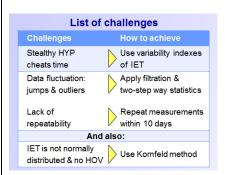
Slide 78 "Positive results on different PCs & HYPs"

Experiments {IkspErimens} proved that this system detects any hidden hypervisor, as well as 2 nested ones. Here you can see cases, which were considered. There are hypervisors based on drivers and BIOS-based ones. Detection system works well on both {BO-UTH} CPUs: on Intel and AMD.

Slide 79 "List of challenges and how I accepted them"

To conclude, I'd like to highlight all the challenges which I faced and how I dealt with them.

First, a stealthy hypervisor uses time cheating to prevent its detection. I have solved {SOLVD} this by using variability indexes. **Second** is the data fluctuation and noise values. I solved them by applying filtration, calculating statistics using two-step way and repeating measurements. **One more thing**, there were no appropriate methods to compare samples. I used the Kornfeld method for solving this task.

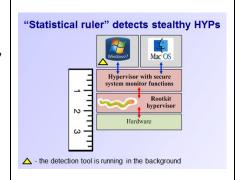


Slide 80 "Statistical ruler" detects stealthy HYPs"

In a nutshell, in this paper I presented a statistical ruler for hypervisors. This is the first software system, which can detect a stealthy hypervisor and calculate several nested ones even under countermeasures.

If you are interested I can show how it works, it will take about twenty minutes.

Thank you.



ADDITIONAL SLIDES

Slide 1 "Additional slides" Slide 2 "Почему Вы отказались от использования классических методов статистики?" Analysis of sample comparison methods (parametric stats) (non parametric stats) Слева классический метод Стьюдента, справа метод Корнфельда. Normal distribution X Homogeneity of variances > В нашем случае нарушаются условия применения параметрических тестов. Для сравнения $(\bar{x} - t * s_{\bar{x}}, \bar{x} + t * s_{\bar{x}}),$ $s_{\bar{x}} = s/\sqrt{n}$, s^2 -sample variance выборок с помощью классических параметрических критериев необходимо одновременное $1-0.5^{n-1}$ выполнение двух условий: 1. Закон распределения обоих сравниваемых выборок должен быть близок к нормальному закону распределения 2. Дисперсии сравниваемых выборок должны быть однородными. Только при соблюдении этих двух требований возможно использование классической мат статистики. В нашем случае оба условия не выполняются, поэтому применять классические методы мат статистики некорректно. Был применён непараметрический метод Корнфельда, который не требует выполнения ни

нормального закона распределения, ни однородности получаемых выборок.

Отмечу два благоприятных фактора для применения этого метода:

- 1. Возможность получать выборки больших объёмов, порядка 10 тысяч элементов.
- 2. Возможность повторять такие опыты.

Чего не наблюдается на практике в большинстве случаев параметрической статисики.

Also I got a huge amount of experimental data and repeated measurements as much as I needed, which is

not always possible to other statistical cases.

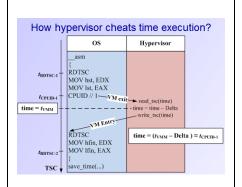
Указанные факторы способствовали получению приемлемых статистических результатов для обнаружения гипервизора.

Slide 3 "How hypervisor cheats time & duration"

This scheme presents the interaction between the operating system and the hypervisor during measuring the instructions execution time.

We measure time of CPUID execution using RDTSC which reads the value of time stamp counter.

Every time when our driver calls CPUID, the VM exit occurs. Each VM exit causes the hypervisor code



to run. After that, the hypervisor reads the time stamp counter, decreases its value by delta and writes the updated value back to counter. By trial and error we choose an appropriate delta value. As a result the registered duration is increased and is the same with the value without a hypervisor. Min-max confidence interval method by Slide 4 "Min-max confidence interval method by Strelen'04 or Kornfeld'65" Strelen'04 or Kornfeld'65 $x_1, x_2, ..., x_n$ - is a random sample Let X1,X2, ..., Xn be a random sample of variable X. The confidence interval for \bar{x} is (x_{min}, x_{max}) where $x_{min} = \min_{1 \le i \le n} x_i$ and $x_{max} = \max_{1 \le i \le n} x_i$ The confidence interval for average X is between Xmin and Xmax, with the following confidence level. with the confidence level $P\{x_{min} \le \bar{x} \le x_{max}\} = 1 - 0.5^{n-1}$ In other words, the probability that X is be between Xmin and Xmax is the following. Slide 5 "Comparison of the confidence interval methods" Comparison the confidence interval methods The confidence intervals method was introduced by Kornfeld in Russian and later by Strellen in English. This is just the comparison of Kornfeld and Strellen ideas. $p = 1 - 2(1/2)^n$. You can see the similar formulas and results: Kornfeld sorts sample values by ascending, that is why X1 Strelen method (Germany 2001) Now let $\bar{X}_1,...,\bar{X}_w$ be IID random variables with finite mean μ and finite median $\bar{\mu}$, hence $P\{\bar{X}_i < \bar{\mu}\} \le 0.5$ and $P\{\bar{X}_i > \bar{\mu}\} \le 0.5$. With $\bar{X}^{min} = \min_{1 \le i \le w} \bar{X}_i$ and $\bar{X}^{max} = \max_{1 \le i \le w} \bar{X}_i$ is the minimum and Xn is the maximum. Strellen gets straight the maximum and the minimum. Formulas with the confidence intervals are the same.

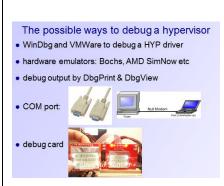
Slide 6 "How to debug HYPs"

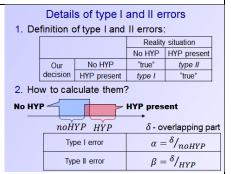
These are the ways how to debug a hypervisor.

- 1. To debug a hypervisor driver we can use Vmware with kernel debugger WinDbg.
- 2. We can also apply hardware emulator Bochs ("box") or AMD SimNow and their own debuggers.
- 3. It is possible to use classical approach with DbgPrint messages and DbgView to collect it. This approach is appropriate more for demonstration not for debugging.
- 4. Another approach which is used in BluePill is to send debug messages to COM port.
- 5. The last method is to use "Debug PCI Card" to see, for example POST BIOS messages.

Slide 7 "Details of type I and II errors"

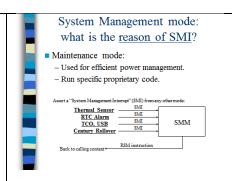
You can see the meaning of the type I and II errors and how to calculate them.





Slide 8 "Details of type I and II errors"

Here you can see the reasons of interruption to System Management Mode, there are signals from Thermal Sensor, RTC alarm, using USB devices instead of COM, like COM keyboard and mouse.



Slide 1 Hello

I'd been researching this topic for 2 years during my Ph.D. And I think my experience might be interesting and helpful.

First of all, I'd like to specify two points of this work. "Time Cheating" means that a hypervisor can prevent its own detection by using different countermeasures.

"Data Fluctuation" ... is the concept of statistics ... means that the data received from experiments are often not applicable for detection, because the data values differ and they often differ significantly.

TWO CHALLENGES OF STEALTHY HYPERVISORS DETECTION: TIME CHEATING & DATA FLUCTUATIONS Igor Korkin CDFSL 2015

Slide 2 "Agenda"

This talk consists of three parts. During the first part of my talk, I'd like to describe a hypervisor as a "sophisticated cyber threat". During the second part of my talk, I'd like to look at the existing "hypervisor detecting tools" and "approaches" and their "drawbacks". The third part covers a



"new hypervisor detecting method" and "challenges", which I dealt with during my work.

Scatter plots & frequency poligons

I repeatedly make scatter plots and a corresponding frequency polygon or relative frequency chart. Polygon dots correspond to the layers on the scatter plot. We see that scatter plot includes tall outliers, apart from basic layers. Similarly, the frequency polygon has a long tail or has a large skew.

First, it's important to remember that interruptions to S-M-M have a random nature; therefore, the I-E-T duration is a random variable as well. Moreover, we can find another feature of I-E-T duration. It is randomly distributed in several <u>fixed</u> layers. The number of S-M-M interruptions determines the number of layers.

And also, the Instructions Execution Time is a random variable, which is not normally distributed. At the same time non-parametric methods are not precise enough.

And the last case is the intersection of two variation intervals. My idea is repeat the measurement and calculation.

All these anomalies have a significant influence on the calculated statistical values; as a result we cannot use it for detection.