Atividade 02: Circuitos Digitais - Segunda fase

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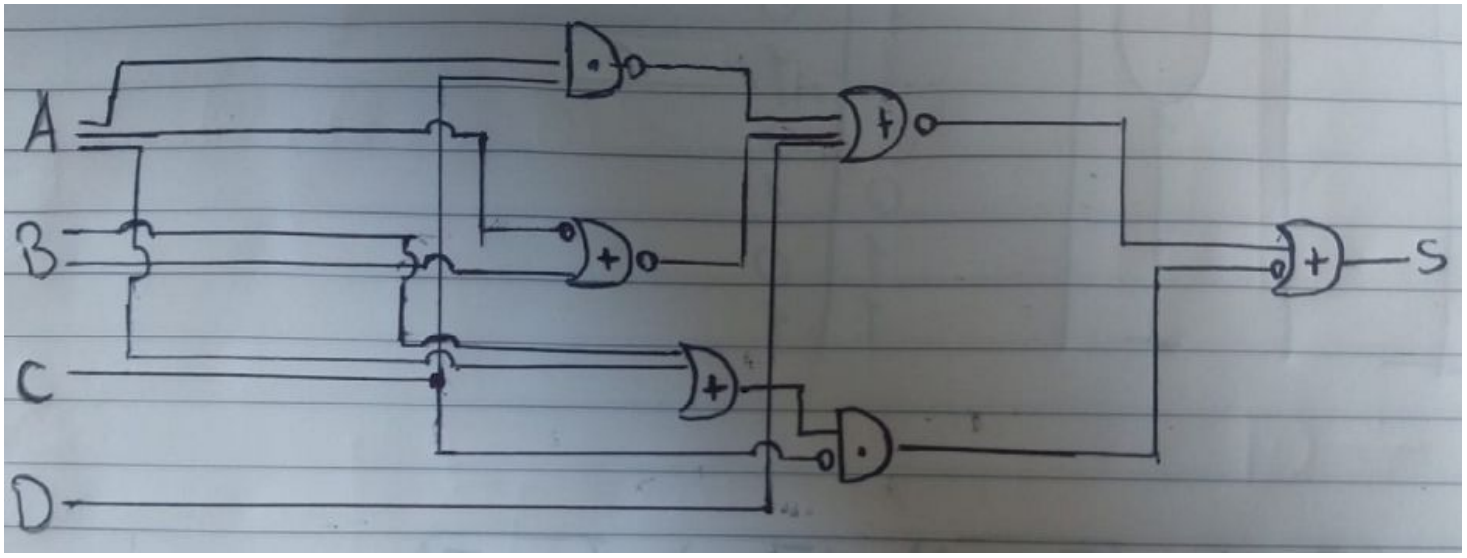
1.a)



1.b)

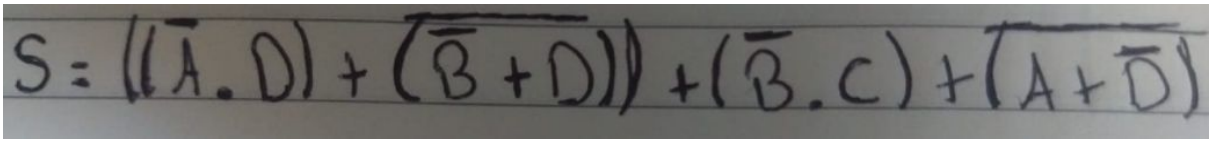
A	B	C	D	X=A.C ''	Y=A'+B''	Z=A+B	H=(X+D+Y)''	K= (C'.Z)''	S=H+K
0	0	0	0	1	0	0	0	1	1
0	0	0	1	1	0	0	0	1	1
0	0	1	0	1	0	0	0	1	1
0	0	1	1	1	0	0	0	1	1
0	1	0	0	1	0	1	0	0	0
0	1	0	1	1	0	1	0	0	0
0	1	1	0	1	0	1	0	1	1
0	1	1	1	1	0	1	0	1	1
1	0	0	0	1	1	1	0	0	0
1	0	0	1	1	1	1	0	0	0
1	0	1	0	0	1	1	0	1	1
1	0	1	1	0	1	1	0	1	1
1	1	0	0	1	0	1	0	0	0
1	1	0	1	1	0	1	0	0	0
1	1	1	0	0	0	1	1	1	1
1	1	1	1	0	0	1	0	1	1

$$S=\overline{\overline{(A.C)}+D+\overline{\overline{A}+B}+\overline{C}.(A+B)}$$

' = para negado

'' = para tudo negado

2.a)



2.b)

A	B	C	D	W=(A'.D)	X=(B'+D)''	Y=(B'.C)	Z=(A+D')''	T=W+X	S=T+Y+Z
0	0	0	0	0	0	0	0	0	0
0	0	0	1	1	0	0	1	1	1
0	0	1	0	0	0	1	0	0	1
0	0	1	1	1	0	1	1	1	1
0	1	0	0	0	1	0	0	1	1
0	1	0	1	1	0	0	1	1	1
0	1	1	0	0	1	0	0	1	1
0	1	1	1	1	0	0	1	1	1
1	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0
1	0	1	0	0	0	1	0	0	1
1	0	1	1	0	0	1	0	0	1
1	1	0	0	0	1	0	0	1	1
1	1	0	1	0	0	0	0	0	0
1	1	1	0	0	1	0	0	1	1
1	1	1	1	0	0	0	0	0	0

' = para negado
'' = para tudo negado