

Instr	RegDst	ALUSrc	Mem toReg	Reg Write	Mem Read	Mem Write	Branch	ALOUp 1	ALOUp 2	JAMPReg	LUItr
R-type	1	0	0	1	0	0	0	1	0	0	1
Lw	0	1	1	1	1	0	0	0	0	0	1
Sw	x	1	x	0	0	1	0	0	0	0	1
Beq	x	0	x	0	0	0	1	0	1	0	1
Jal	x	x	x	0	x	0	0	x	x	1	0
Lui	0	x	x	1	x	0	0	x	x	0	1