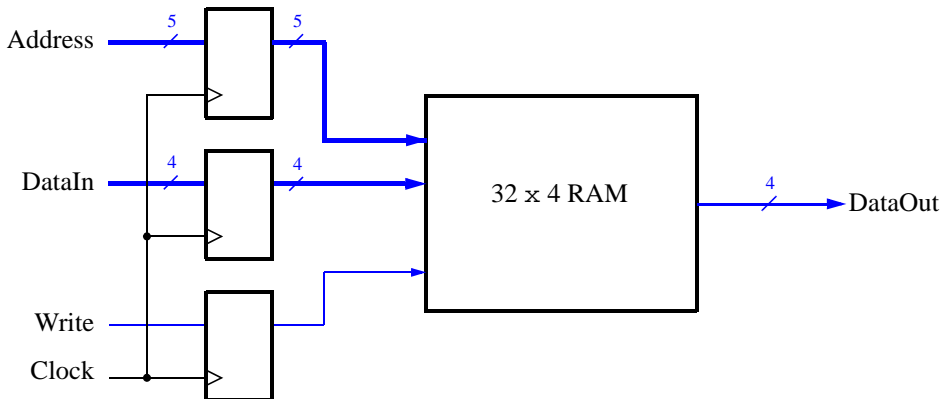


(a) RAM organization



(b) RAM implementation