## iCEBreaker Bitsy v1.1a

Open Source FPGA development board

icebreaker-fpga.org

Pin Name FPGA Package Pin Number **FPGA Pin Name** Differential Pair 3.3V (500mA total) IN (1.45 to 5.5 V) RGB[0-2] P19 P18 P17 P16 P15  $\mathsf{C}_{\mathsf{DONE}}$  $C_{\mathsf{RESET}}$ GND 3.3V ea EOB\_ 0B\_ OB OB

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Pin Name	FPGA Package Pin Number FPGA Pin Name								
CLK	35	IOT	_46b		USB_	DET	37	IOT_	_45a
FLASH_CS	16	IOT	_35b		USB_	DN	38	IOT_	_50b
RAM_CS	6	IOE	3_13b		USB_	DP	42	IOT_	_51a
1.3-+14									
FPGA SPI	$C_{D}$	ONE	00	CS	16	IOB.	_35b <sub>.</sub>	_SPI_	CSN
Header	$C_R$	ESET	00	SCK	15	IOB.	_34a <sub>.</sub>	_SPI_	SCK
	C	AND	00	<b>I01</b>	17	IOB.	_33b <sub>.</sub>	_SPI_	SI
	3	3.3V	00	100	14	IOB.	_32a	_SPI_	S0
I0B_31b	18	102	• •	103	19	IOB.	_29b		



1 BIT SQUARED