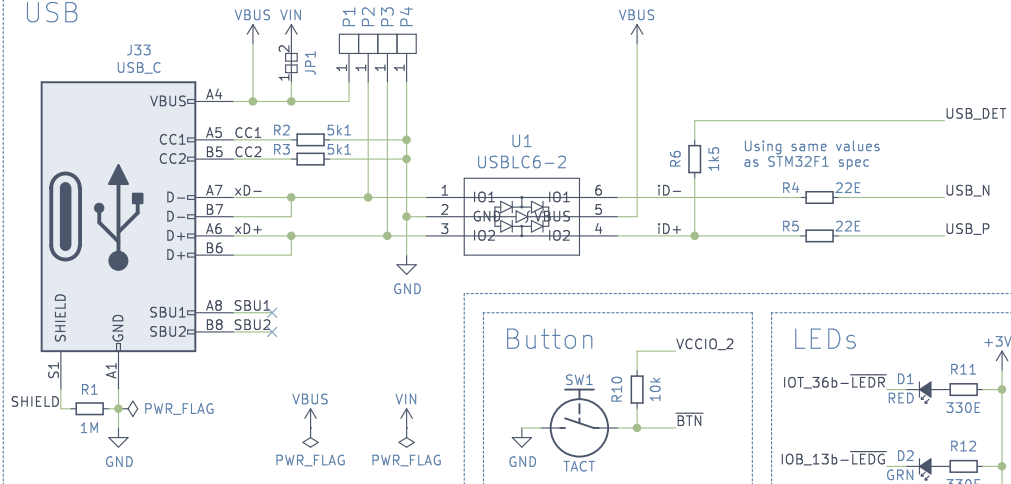
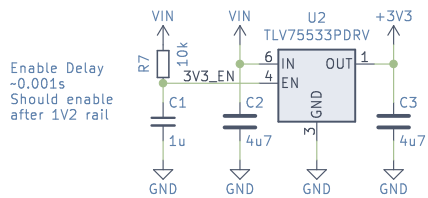


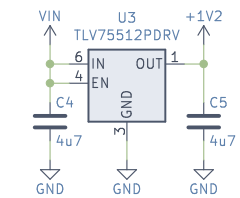
USB



3V3 VReg

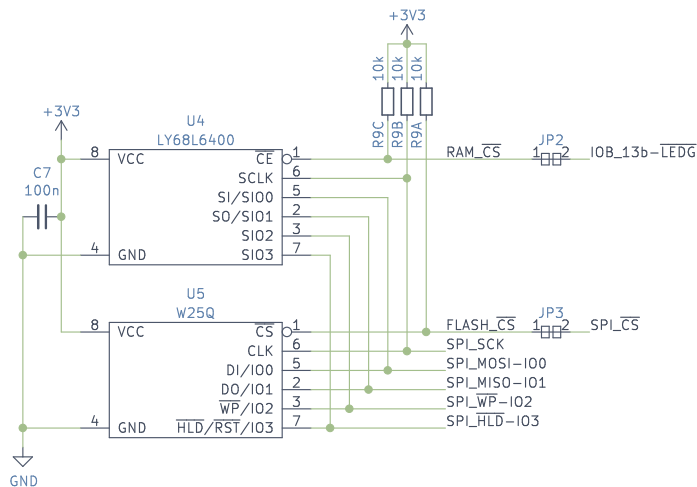


1V2 VReg

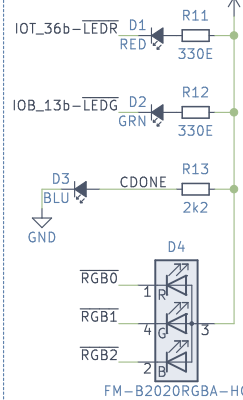


Flash/RAM

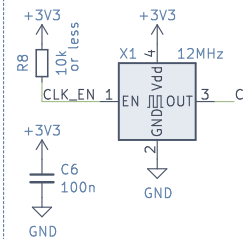
JP3: Remove jumper when only programming ICE
Replace jumper for programming Flash and for normal operation



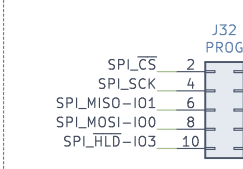
LEDs



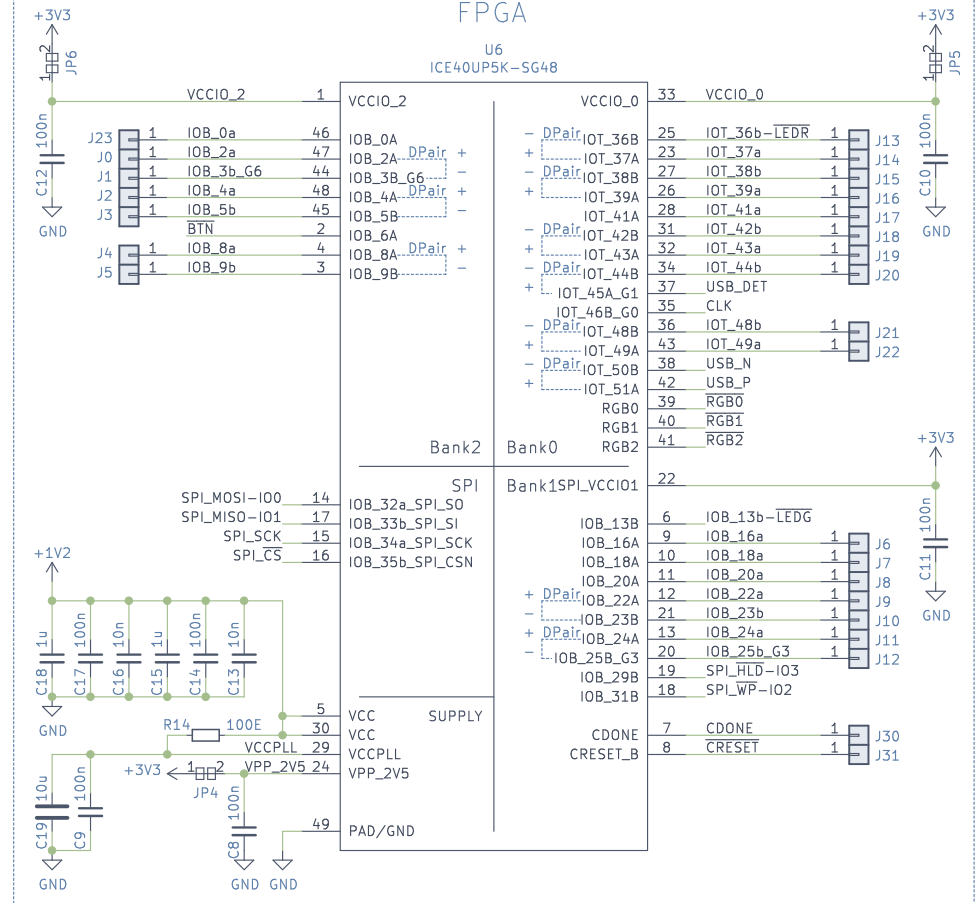
Clock



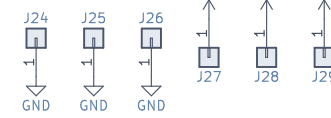
QSPI/Programming



FPGA



Power Pins



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