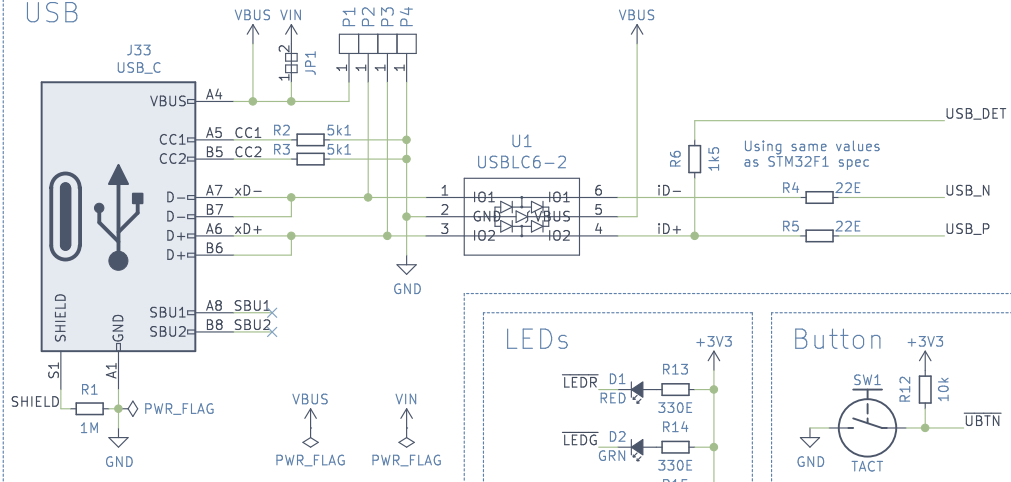
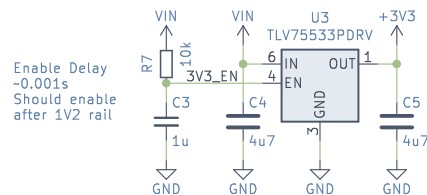


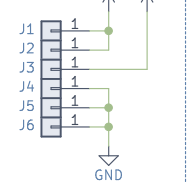
USB



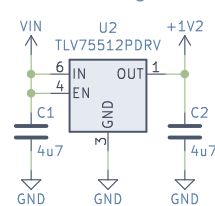
3V3 VReg



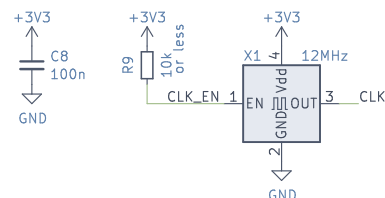
Power Pins



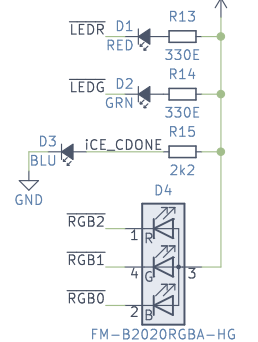
1V2 VReg



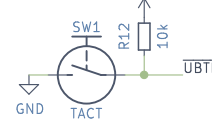
External Clock



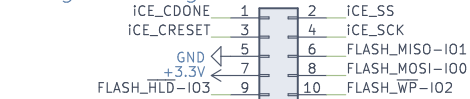
LEDs



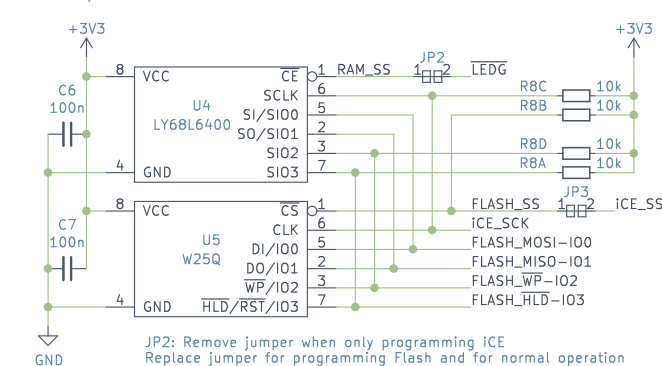
Button



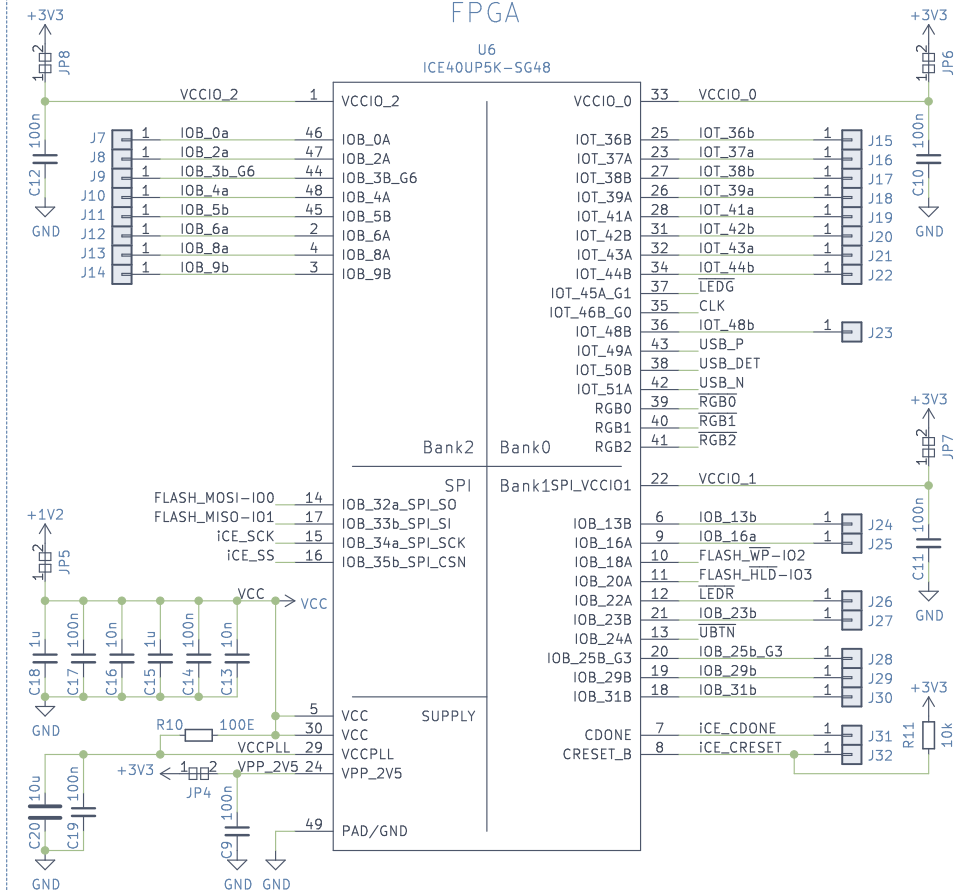
Programming



Flash/RAM



FPGA



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