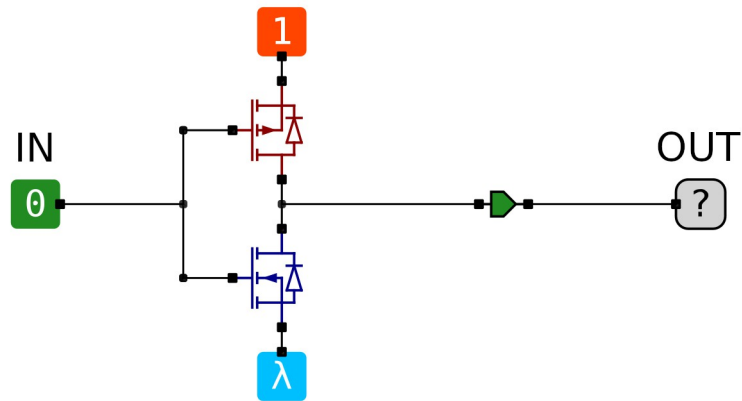


STI PCB specification

STI = Standard Ternary Inverter is probably the most primitive logical gate. In fact it plays not logical role but auxiliary - it is used as analogue amplifier much more frequently than digital gate.



Pins description

IN - input signal (trit on the picture above, tryte on the picture below)

OUT - output signal (the same dimension as “IN”)

All voltage sources correspond to power supply.

Designed behaviour

IN	OUT
λ	1
0	0
1	λ

PCB layout

