Semnale de control MIPS32

Instrucțiune	Opcode Instr[31-26]	Reg Dst	ExtOp	ALUSrc	Branch	Br_? (opțional)	Jump	JmpR (opțional)	Mem Write	Memto Reg	Reg Write	ALUOp[1:0]	function Instr[5-0]	ALUCtrl[2:0]
ADD	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000000	000 (+)
SUB	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000001	001 (-)
SLL	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000010	010 (<<)
SRL	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000011	011 (>>)
AND	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000100	100 (AND)
OR	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000101	101 (OR)
XOR	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000110	110 (XOR)
SLT	000000	1	X	0	0		0		0	0	1	00 (Tip R)	000111	111 (CMP)
ADDI	000001	0	1	1	0		0		0	0	1	01 (+)	XXXXXX	000 (+)
LW	000010	0	1	1	0		0		0	1	1	01 (+)	XXXXXX	000 (+)
SW	000011	X	1	1	0		0		1	X	0	01 (+)	XXXXXX	000 (+)
BEQ	000100	X	1	0	1		0		0	X	0	10 (-)	XXXXXX	001 (-)
ORI	000101	0	0	0	0		0		0	0	1	11 (|)	XXXXXX	101 (OR)
SLTI	000110	0	1	0	0		0		0	0	1	10 (-)	XXXXXX	001 (-)
J	000111	X	X	X	0		1		0	X	0	XX	XXXXXX	XXX