

1

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A

A

B

B

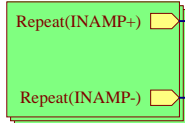
C

C

D

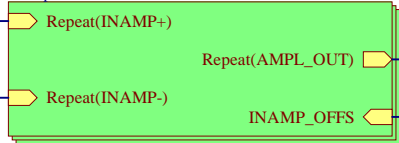
D

Repeat(U\_filter, 1, 4)  
filter.SchDoc



INAMP+

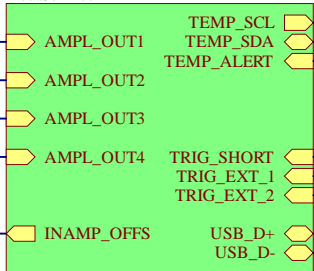
Repeat(U\_inamp, 1, 4)  
inamp.SchDoc



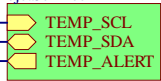
AMPL\_OUT



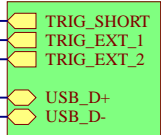
U\_mcu  
mcu.SchDoc



U\_cjc  
cjc.SchDoc



U\_external  
external.SchDoc



INAMP\_OFFS

U\_power  
power.SchDoc



U\_ProjectInfo  
ProjectInfo



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Nazev listu/sheet name/:

Top.SchDoc

DPS projekt/PCB project/:

V286-tclogger

Kreslil /drawn by/ :

Iliyan Antov

Kontroloval /checked by/:

Boris Botev

Datum /date/: 2024-05-30

List /sheet/: 1 / 10

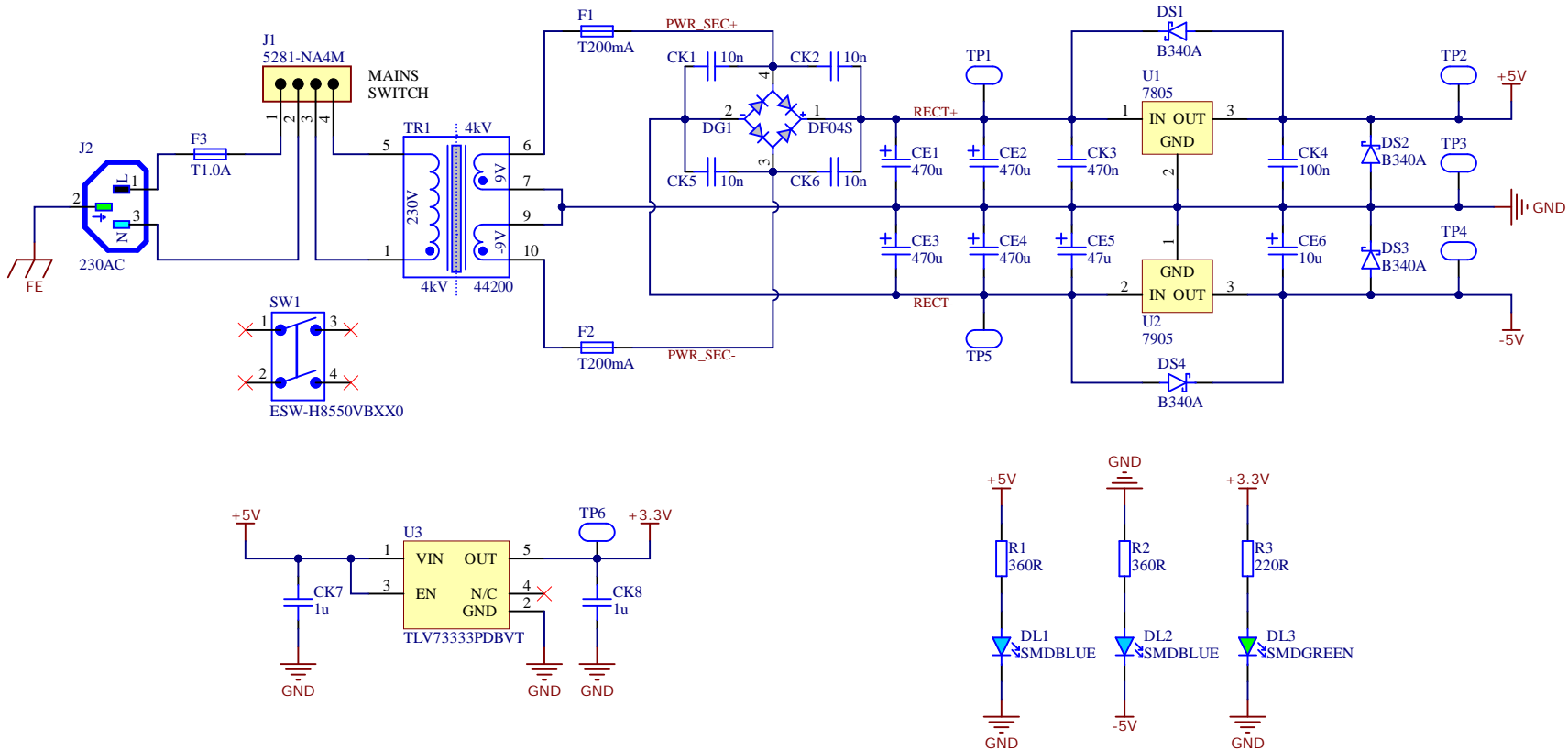
1


2

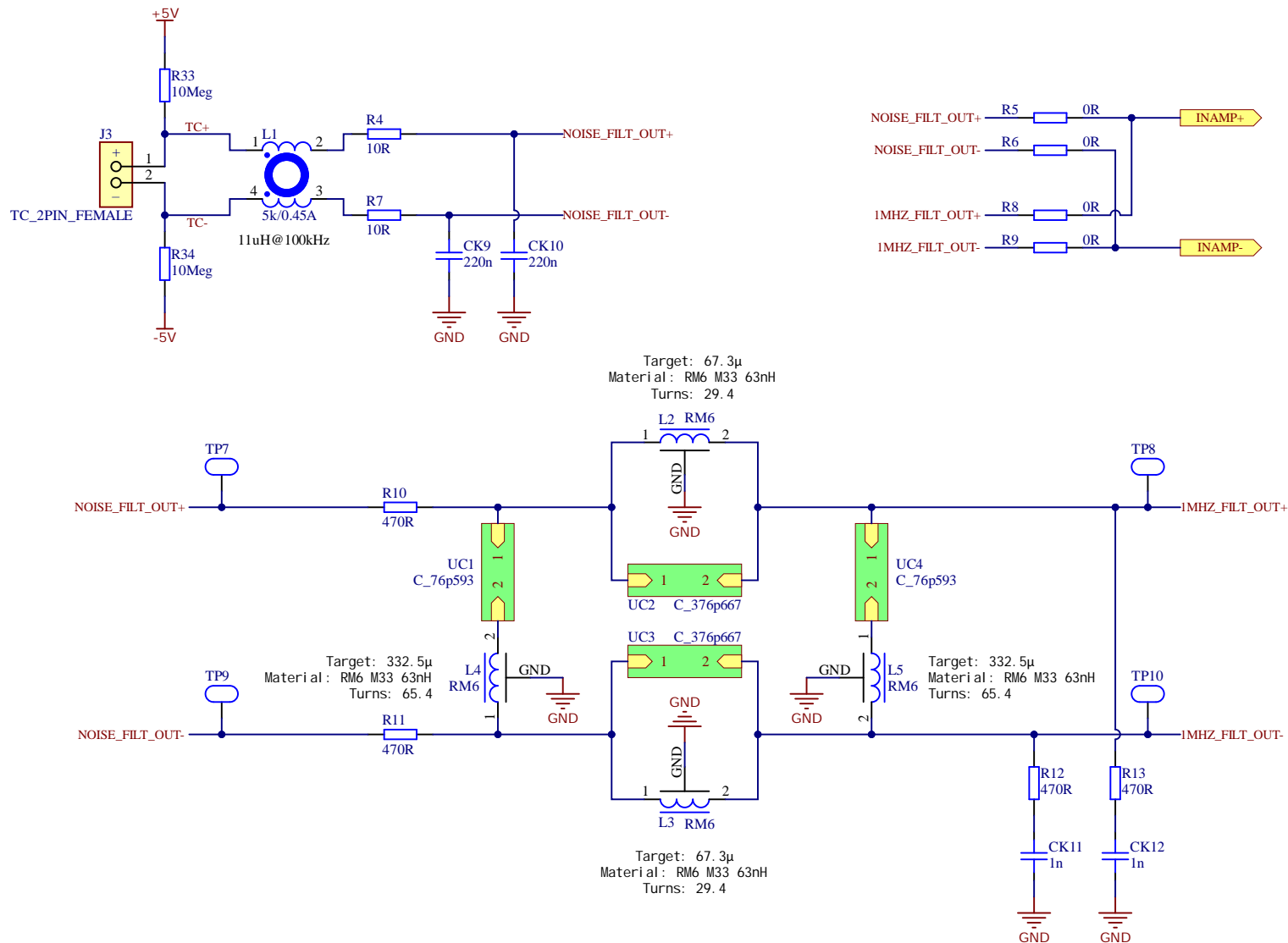
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1	2	3	4																					
A	<div>Revisions table</div> <table><thead><tr><th>Revisions:</th><th>Description of changes:</th><th>Drawn by:</th><th>Checked by:</th></tr></thead><tbody><tr><td>100</td><td>Initial version</td><td>Iliyan Antov</td><td>Boris Botev</td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr></tbody></table>				Revisions:	Description of changes:	Drawn by:	Checked by:	100	Initial version	Iliyan Antov	Boris Botev												
	Revisions:	Description of changes:	Drawn by:	Checked by:																				
	100	Initial version	Iliyan Antov	Boris Botev																				
B																								
C																								
D	<div>S1</div> <div>BTL</div>	<div>S2</div> <div>CROSSED</div> <div>BIN</div>	<div>LB1</div> <div>2D</div>	<div>FM1</div> <div>FM2</div> <div>FM3</div> <div>FM4</div>																				
	<div><div><div>Číslo výkresu /drawing number/: V286-53[No Variations]100</div><div><div><div><div><div><div></div></div></div><div><div><div></div></div></div><div><div><div></div></div></div></div></div><div><div><div>Nazev listu/sheet name/: ProjectInfo.SchDoc</div><div>DPS projekt/PCB project/: V286-tclogger</div></div><div><div><div>Kreslil /drawn by/: Iliyan Antov</div><div>Kontroloval /checked by/: Boris Botev</div></div><div><div><div>Datum /date/: 2024-05-30</div><div>List /sheet/: 2 / 10</div></div></div></div></div></div></div></div>																							
	1	2	3	4																				



Číslo výkresu /drawing number/: <b>V286-53[No Variations]100</b>		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	power.SchDoc	V286-tclogger
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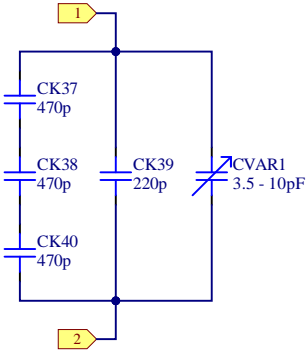


Číslo výkresu /drawing number/: <b>V286-53[No Variations]100</b>		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	filter.SchDoc	V286-tclogger
	Kreslil/drawn by/:	Kontroloval/checked by/:
Iliyan Antov		Boris Botev
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1	2	3	4										
A				A									
B	<div>Target: 76.190p Actual : 76.593p Di ff: +0.403p</div> <div><div>1</div><div>CK32 470p</div><div>CK33 470p</div><div>CK34 470p</div><div>CK35 470p</div><div>CK36 220p</div><div>2</div></div>			B									
C				C									
D	<table><tr><td colspan="2">Číslo výkresu /drawing number/: V286-53[No Variations]100</td></tr><tr><td rowspan="3"></td><td>Název listu/sheet name/: C_76p593.SchDoc</td><td>DPS projekt/PCB project/: V286-tclogger</td></tr><tr><td>Kreslil /drawn by/: Iliyan Antov</td><td>Kontroloval /checked by/: Boris Botev</td></tr><tr><td colspan="2">Datum /date/: 2024-05-30List /sheet/: 5 / 10</td></tr></table>			Číslo výkresu /drawing number/: V286-53[No Variations]100			Název listu/sheet name/: C_76p593.SchDoc	DPS projekt/PCB project/: V286-tclogger	Kreslil /drawn by/: Iliyan Antov	Kontroloval /checked by/: Boris Botev	Datum /date/: 2024-05-30List /sheet/: 5 / 10		D
Číslo výkresu /drawing number/: V286-53[No Variations]100													
	Název listu/sheet name/: C_76p593.SchDoc	DPS projekt/PCB project/: V286-tclogger											
	Kreslil /drawn by/: Iliyan Antov	Kontroloval /checked by/: Boris Botev											
	Datum /date/: 2024-05-30List /sheet/: 5 / 10												
1	2	3	4										

1	2	3	4
A			
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C			
D			

Target: 376.200p  
Actual : 376.667p  
Di ff: +0.467p



Číslo výkresu /drawing number/: <b>V286-53[No Variations]100</b>		
	Nazev listu/sheet name/: <b>C_376p667.SchDoc</b>	DPS projekt/PCB project/: <b>V286-tclogger</b>
	Kreslil /drawn by/ : Iliyan Antov	Kontroloval /checked by/: Boris Botev
	Datum /date/: 2024-05-30 <b>List /sheet/: 6 / 10</b>	

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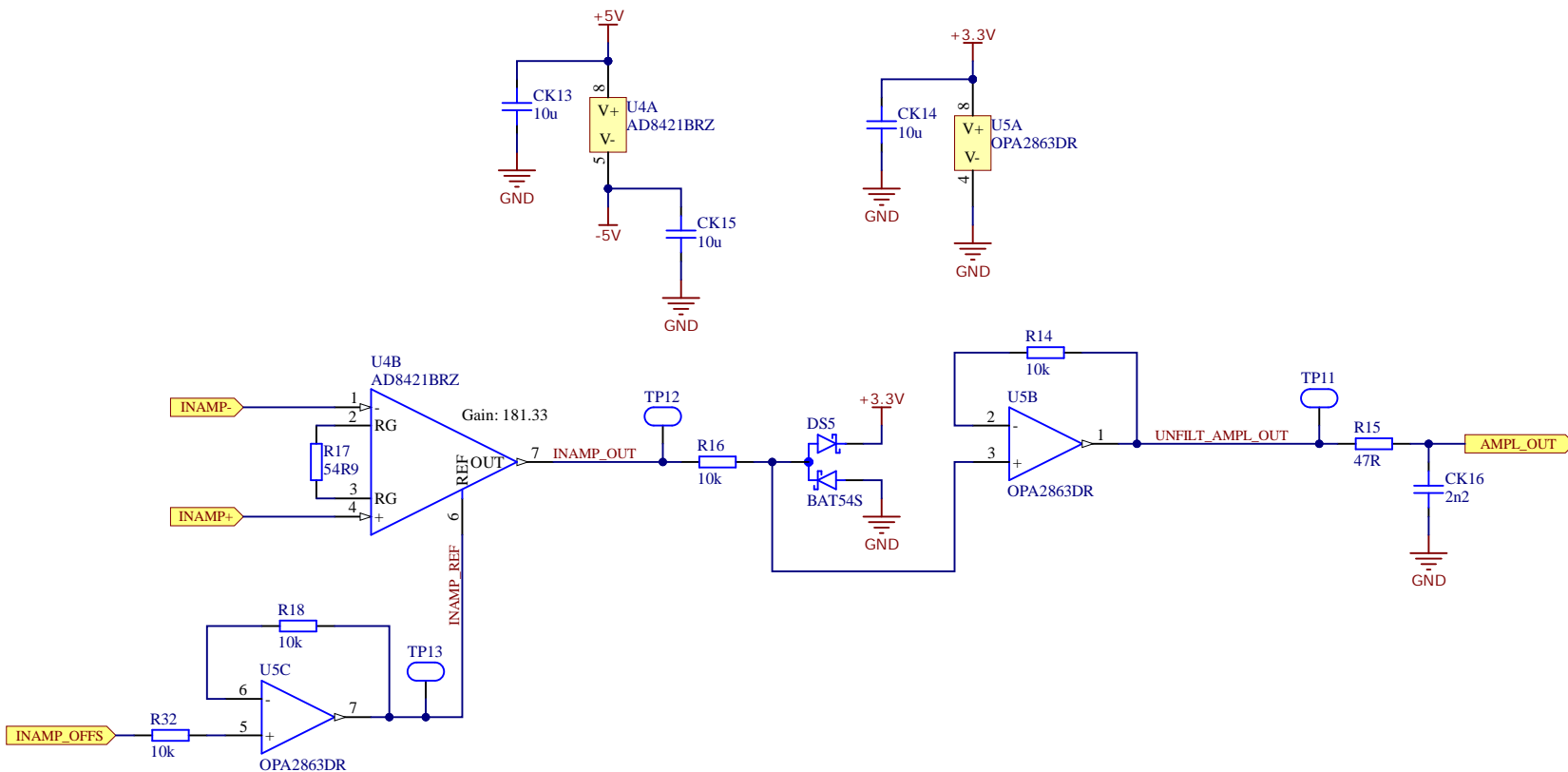
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D



Číslo výkresu /drawing number/: V286-53[No Variations]100

Název listu/sheet name/:  
inamp.SchDocKreslil /drawn by/:  
Iliyan Antov

Datum /date/: 2024-05-30

DPS projekt/PCB project/:  
V286-tcloggerKontroloval /checked by/:  
Boris Botev

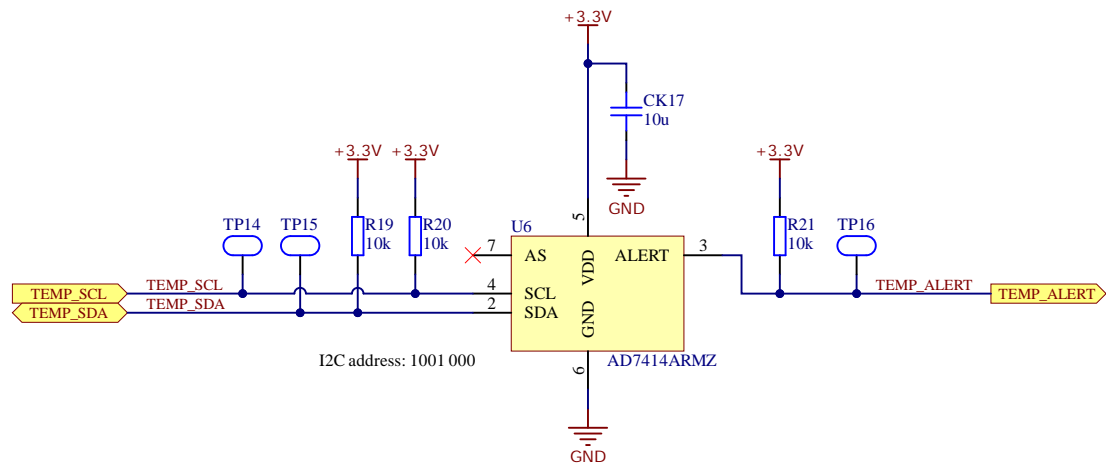
List /sheet/: 7 / 10

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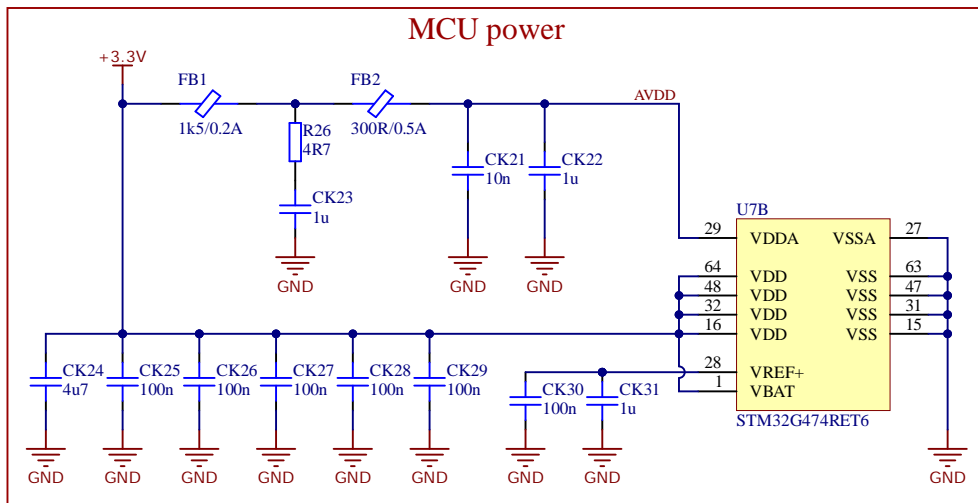
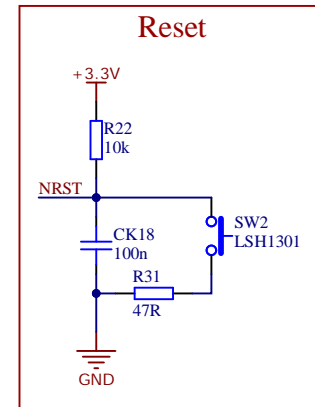
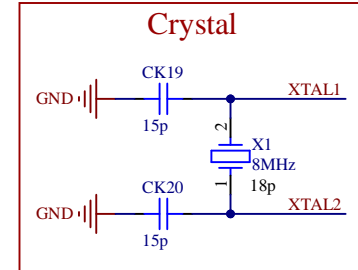
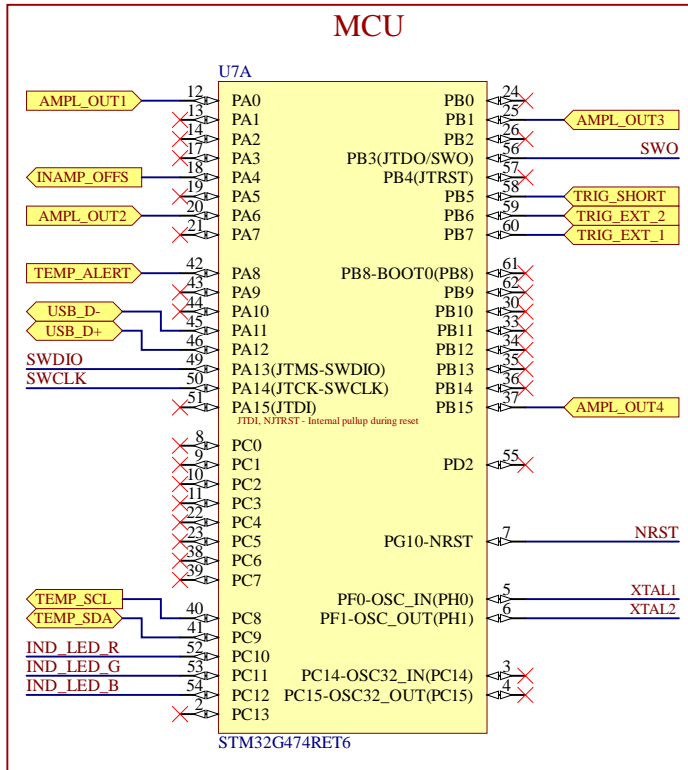
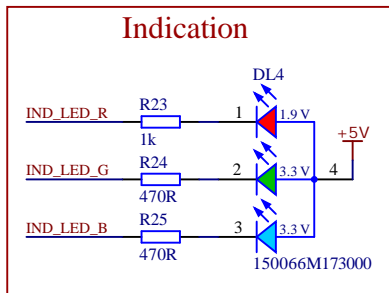
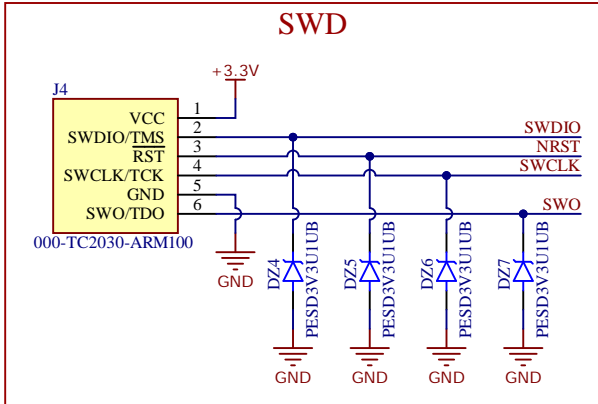
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4

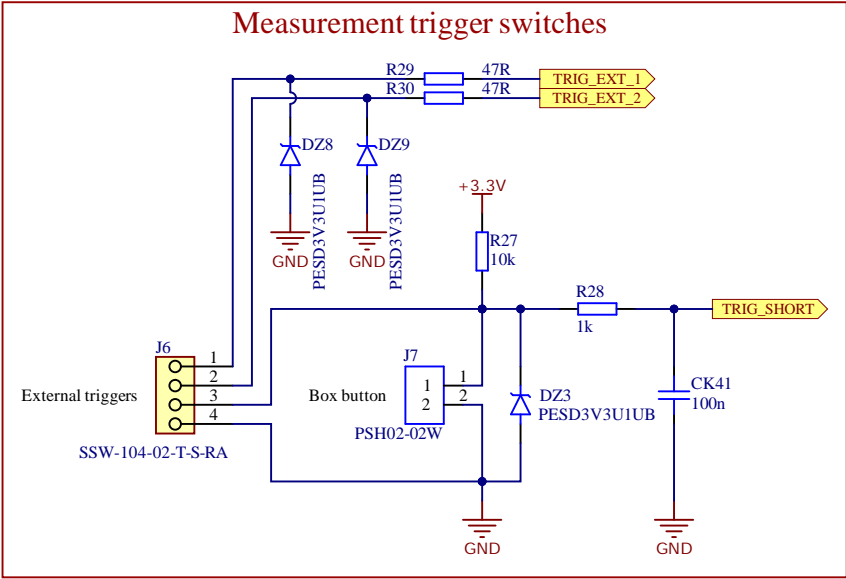
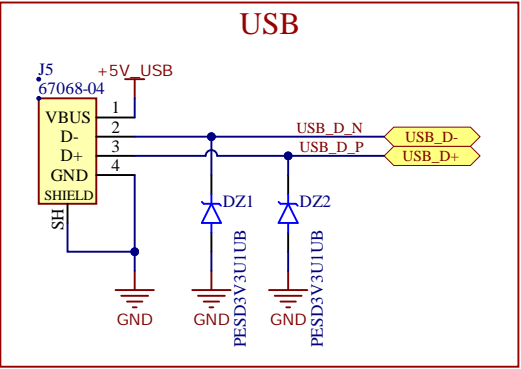


Číslo výkresu /drawing number/: V286-53[No Variations]100		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	cjc.SchDoc	V286-tclogger
	Kreslil /drawn by/ :	Kontroloval /checked by/:
Iliyan Antov		Boris Botev
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	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	mcu.SchDoc	V286-tclogger
	Kreslil/drawn by/:	Kontroloval /checked by/:
Iliyan Antov		Boris Botev
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Číslo výkresu /drawing number/: <b>V286-53[No Variations]100</b>		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	external.SchDoc	V286-tclogger
	Kreslil /drawn by/ :	Kontroloval /checked by/:
Iliyan Antov		Boris Botev
Datum /date/: 2024-05-30		List /sheet/: 10 / 10