

1

2

3

4

A

A

B

B

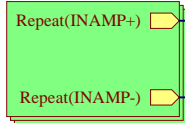
C

C

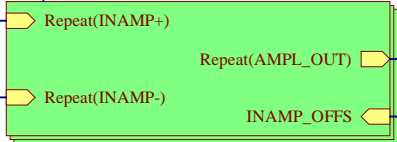
D

D

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filter.SchDoc



Repeat(U_inamp, 1, 4)
inamp.SchDoc

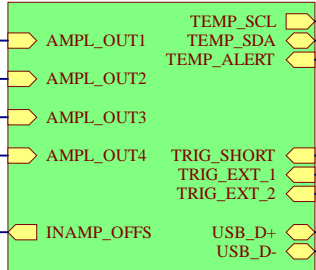


AMPL_OUT

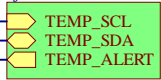
AMPL_OUT1..4

INAMP_OFFS

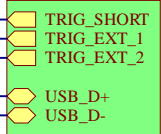
U_mcu
mcu.SchDoc



U_cjc
cjc.SchDoc



U_external
external.SchDoc



U_power
power.SchDoc



U_ProjectInfo
ProjectInfo



Číslo výkresu /drawing number/: V286-53[No Variations]100



Nazev listu/sheet name/:

Top.SchDoc

Kreslil /drawn by/ :

Iliyan Antov

Datum /date/: 2024-05-30

DPS projekt/PCB project/:

V286-tclogger

Kontroloval /checked by/:

Boris Botev

List /sheet/: 1 / 10

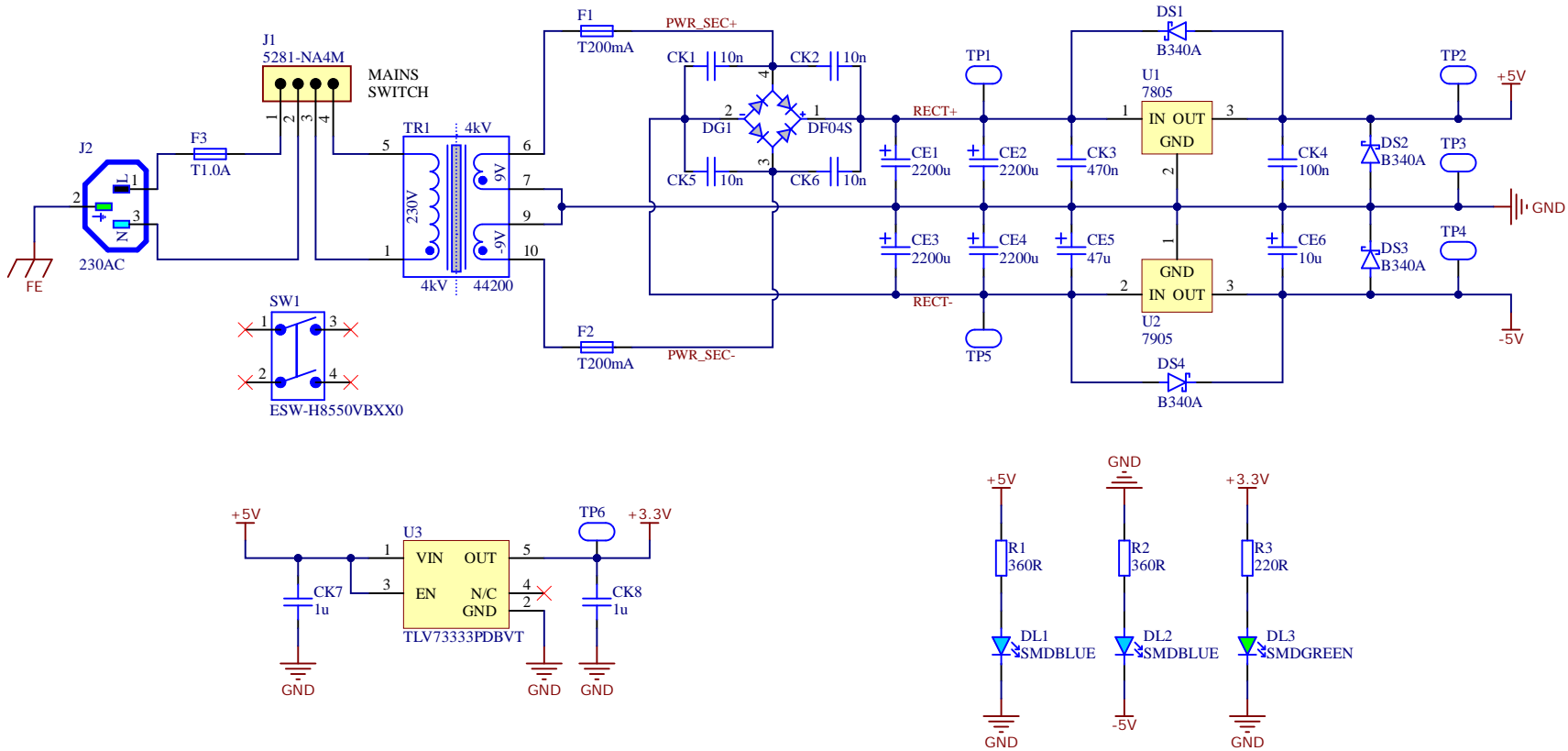
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2

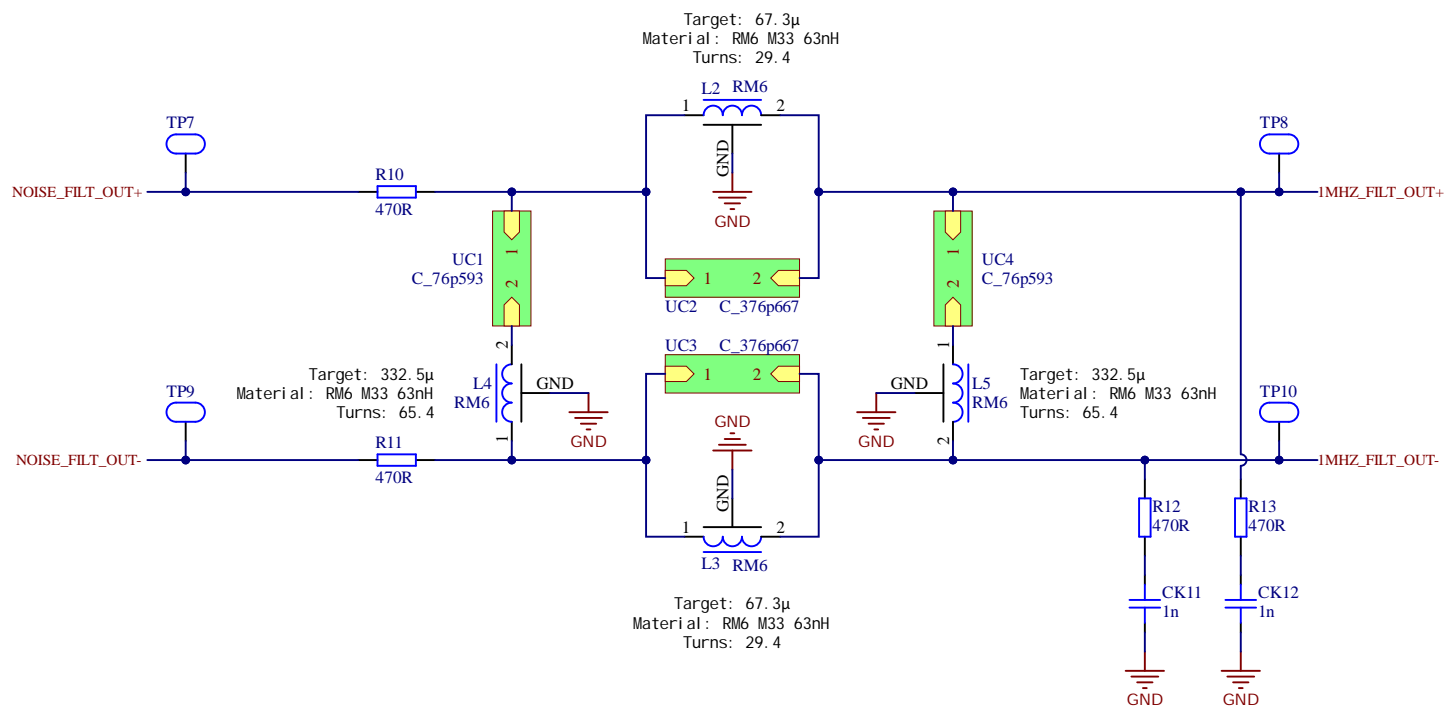
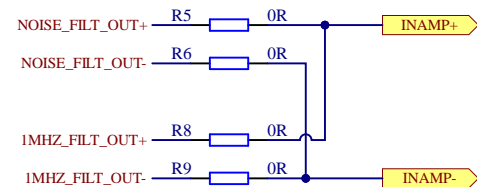
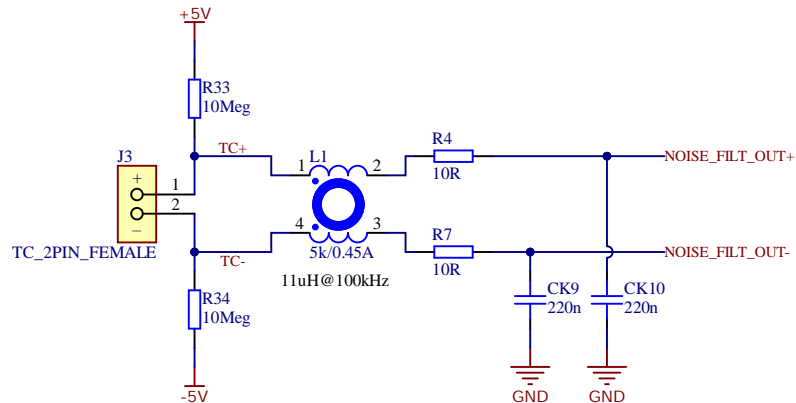
3


4

1		2		3		4																					
A	<div>Revisions table</div> <table><tr><th>Revisions:</th><th>Description of changes:</th><th>Drawn by:</th><th>Checked by:</th></tr><tr><td>100</td><td>Initial version</td><td>Iliyan Antov</td><td>Boris Botev</td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr></table>							Revisions:	Description of changes:	Drawn by:	Checked by:	100	Initial version	Iliyan Antov	Boris Botev												
	Revisions:	Description of changes:	Drawn by:	Checked by:																							
	100	Initial version	Iliyan Antov	Boris Botev																							
B	<div><div>H1</div><div>MTHOLE_NP</div><div>H2</div><div>MTHOLE_NP</div><div>H3</div><div>MTHOLE_NP</div><div>H4</div><div>MTHOLE_NP</div></div>																										
C																											
D	<div><div><div>S1</div><div>BTL</div></div><div><div>S2</div><div>CROSSED BIN</div></div><div><div>LB1</div><div>2D</div></div><div><div>FM1</div><div>FM3</div></div><div><div>FM2</div><div>FM4</div></div></div>																										
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1		2		3		4																					



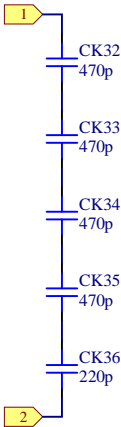
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	Nazev listu/sheet name/: power.SchDoc	DPS projekt/PCB project/: V286-tclogger
	Kreslil /drawn by/: Iliyan Antov	Kontroloval /checked by/: Boris Botev
	Datum /date/: 2024-05-30	List /sheet/: 3 / 10




Číslo výkresu /drawing number/: V286-53[No Variations]100		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	filter.SchDoc	V286-tclogger
	Kreslil/drawn by/:	Kontroloval /checked by/:
	Iliyan Antov	Boris Botev
Datum /date/: 2024-05-30		List /sheet/: 4 / 10

1	2	3	4
A			A
B			B
C			C
D			D
1	2	3	4

Target: 76.190p
Actual : 76.593p
Diff: +0.403p



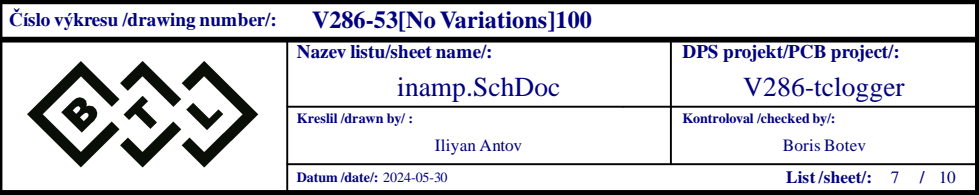
Číslo výkresu /drawing number/: V286-53[No Variations]100		
	Nazev listu/sheet name/: C_76p593.SchDoc	DPS projekt/PCB project/: V286-tclogger
	Kreslil /drawn by/ : Iliyan Antov	Kontroloval /checked by/: Boris Botev
	Datum /date/: 2024-05-30	
		List /sheet/: 5 / 10

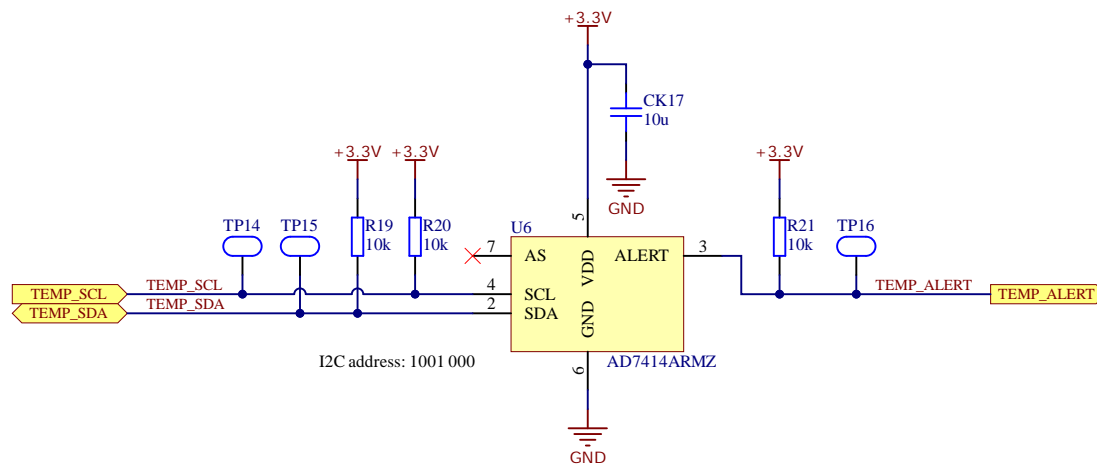
1	2	3	4
A			A
B			B
C			C
D			D


Target: 376.200p
Actual : 376.667p
Diff: +0.467p

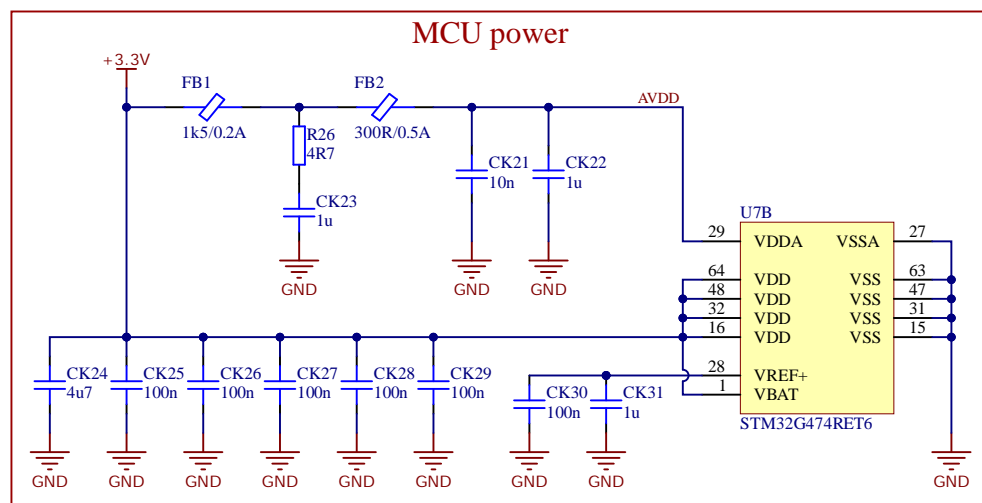
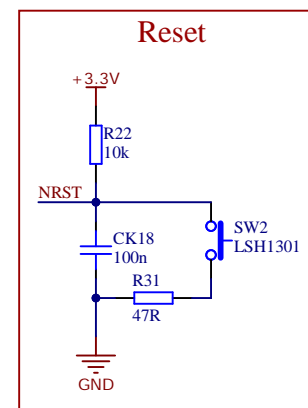
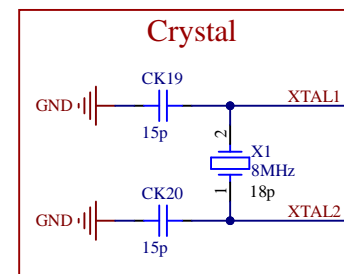
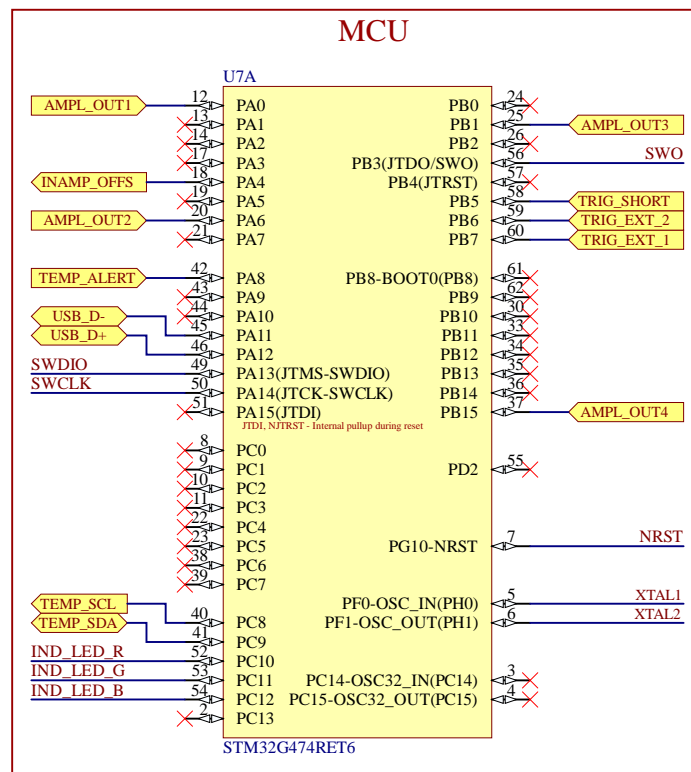
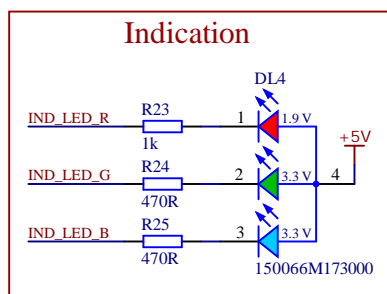
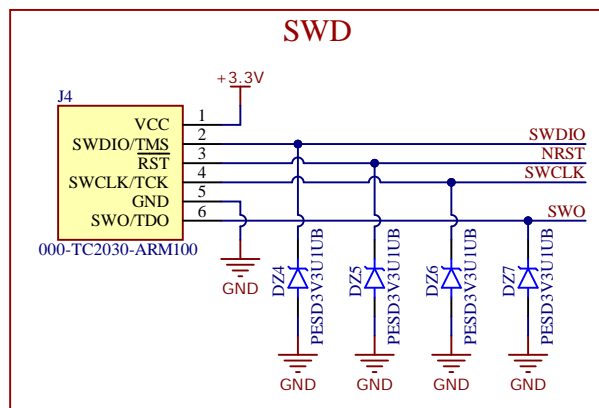
CK37 470p
CK38 470p
CK40 470p
CK39 220p
CVAR1 3.5 - 10pF


Číslo výkresu /drawing number/: V286-53[No Variations]100		
	Nazev listu/sheet name/: C_376p667.SchDoc	DPS projekt/PCB project/: V286-tclogger
	Kreslil /drawn by/: Iliyan Antov	Kontroloval /checked by/: Boris Botev
	Datum /date/: 2024-05-30 List/sheet/: 6 / 10	

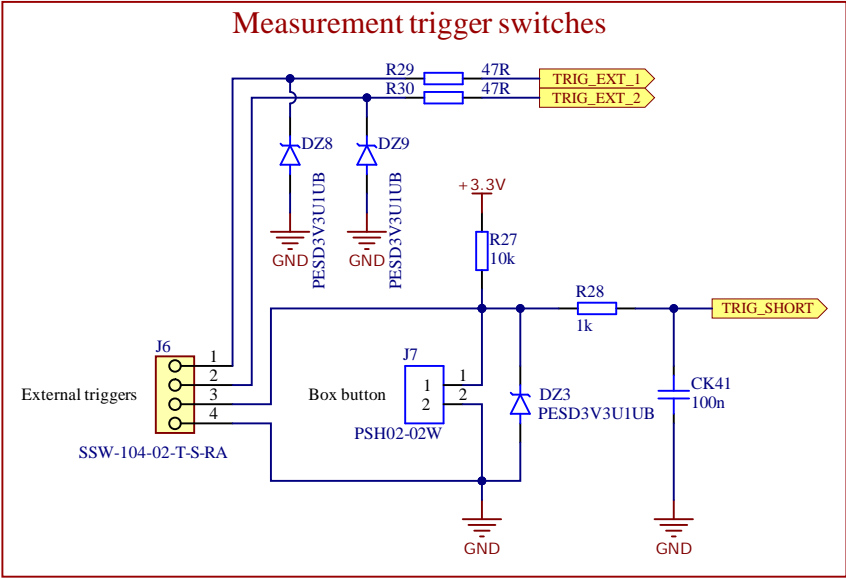
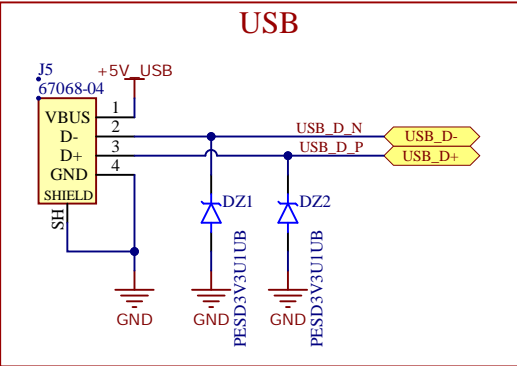





Číslo výkresu /drawing number/: V286-53[No Variations]100		
	Nazev listu/sheet name/:	DPS projekt/PCB project/:
	cjc.SchDoc	V286-tclogger
	Kreslil /drawn by/ :	Kontroloval /checked by/:
	Iliyan Antov	Boris Botev
Datum /date/: 2024-05-30		List /sheet/: 8 / 10



Číslo výkresu /drawing number/: V286-53[No Variations]100		
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Iliyan Antov	Boris Botev	
Datum /date/: 2024-05-30	List/sheet/: 9 / 10	



Číslo výkresu /drawing number/: V286-53[No Variations]100		
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	Datum /date/: 2024-05-30	List /sheet/: 10 / 10