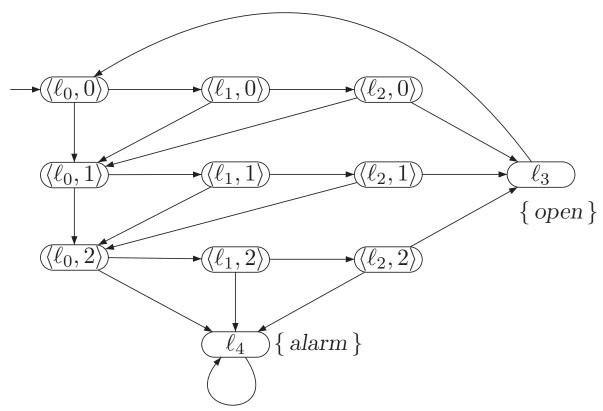
# Abstraction functions: example

## Example: a door opener

The door opener requires a three-digit code d1 d2 d3 as input with di  $\{0, \ldots, 9\}$ . It allows an erroneous digit to be entered, but this may happen at most twice.

The variable *error* keeps track of the number of wrong digits that have been provided, and is initially zero. In case *error* exceeds two, the door opener issues an alarm signal. On a successful input of the door code, the door is opened. Once locked again, it returns to its initial state

## Transition System for the door opener



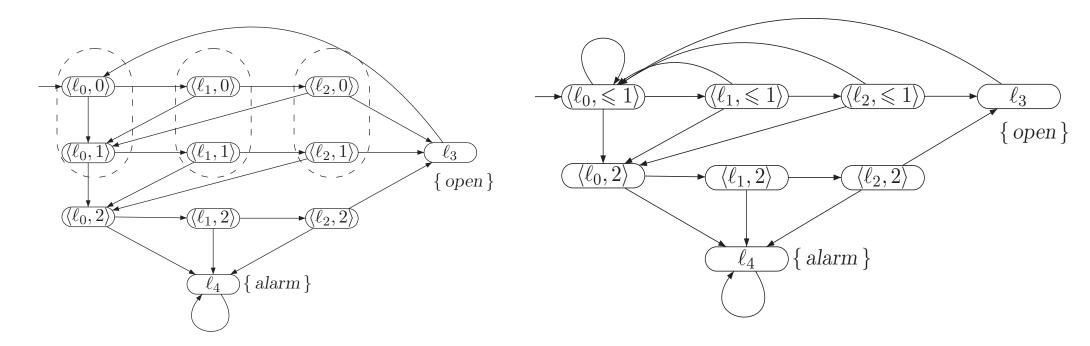
AP = { alarm, open }.

Location  $\ell_i$  (for i = 0, 1, 2) indicates that the first i digits of the code have been correctly entered; the second component of a state indicates the value of the variable *error* (if applicable).

#### A first abstraction function

A data abstraction: domain of the variable error is restricted to { <=1, 2 }, i.e., the values 0 and 1 are not distinguished in the abstract transition system.

$$f(\langle \ell, error = k \rangle) = \begin{cases} \langle \ell, error \leq 1 \rangle & \text{if } k \in \{0, 1\} \\ \langle \ell, error = 2 \rangle & \text{if } k = 2 \end{cases}$$



#### Another abstraction function

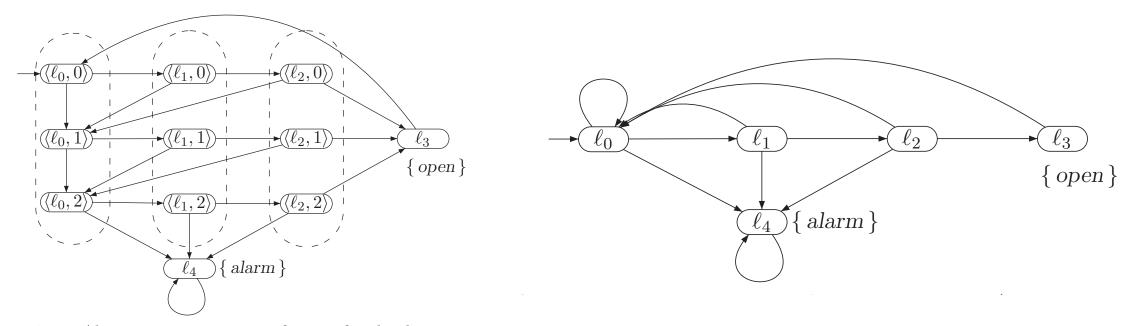


Figure 7.20: Alternative aggregation of states for the door opener.

$$g(\langle \ell, error = k \rangle) = \ell,$$

## Stutter Bisimulation

Some brief remarks

## Stuttering

- Bisimulation R requires for R-equivalent states s1 and s2 that each transition s1 → t1 is matched by some transition s2 → t2 (and vice versa)
- Stutter bisimulation R' allows s1 → t1 to be matched by a path fragment:
- s2 u1 u2 ...un t2 (for n >= 0) such that:
- t1 and t2 are R'-equivalent, and each ui is R'equivalent to s2.
- That is, single transitions may be matched by (suitable) path fragments.

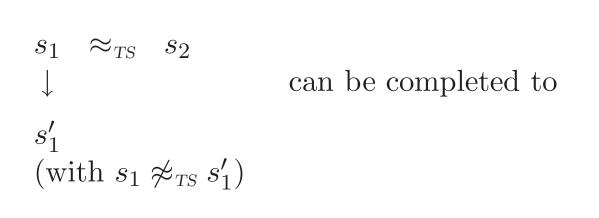
#### Formal definition of Stutter Bisimulation

Let  $TS = (S, Act, \rightarrow, I, AP, L)$  be a transition system. A stutter bisimulation for TS is a binary relation  $\mathcal{R}$  on S such that for all  $(s_1, s_2) \in \mathcal{R}$ :

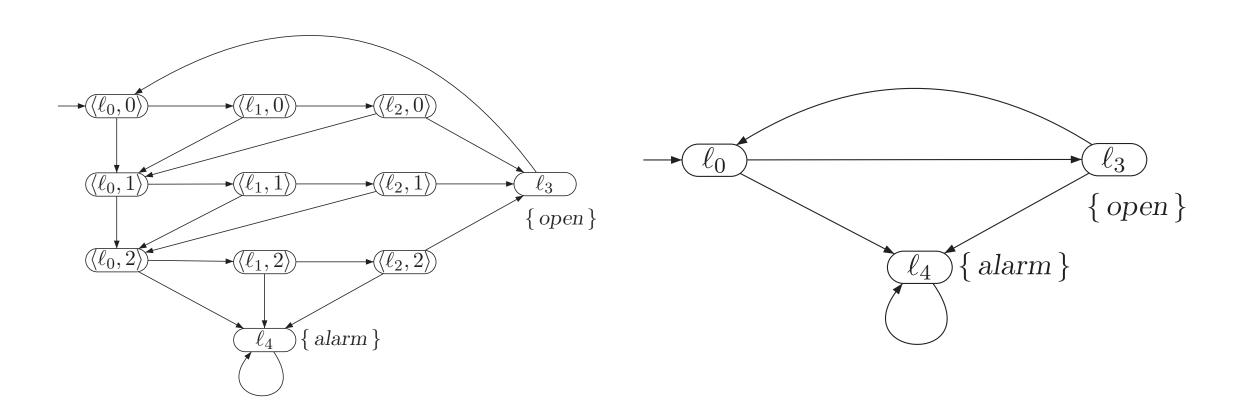
- 1.  $L(s_1) = L(s_2)$ .
- 2. If  $s'_1 \in Post(s_1)$  with  $(s'_1, s_2) \notin \mathcal{R}$ , then there exists a finite path fragment  $s_2 u_1 \ldots u_n s'_2$  with  $n \geqslant 0$  and  $(s_1, u_i) \in \mathcal{R}$ ,  $i = 1, \ldots, n$  and  $(s'_1, s'_2) \in \mathcal{R}$ .
- 3. If  $s'_2 \in Post(s_2)$  with  $(s_1, s'_2) \notin \mathcal{R}$ , then there exists a finite path fragment  $s_1 v_1 \ldots v_n s'_1$  with  $n \ge 0$  and  $(v_i, s_2) \in \mathcal{R}$ ,  $i = 1, \ldots, n$  and  $(s'_1, s'_2) \in \mathcal{R}$ .

 $s_1, s_2$  are stutter bisimulation equivalent (stutter-bisimilar, for short), denoted  $s_1 \approx_{TS} s_2$ , if there exists a stutter bisimulation  $\mathcal{R}$  for TS with  $(s_1, s_2) \in \mathcal{R}$ .

## Condition 2 explained



## Ex: a stutter bisimilar TS of the door opener



## It is an equivalence relation

 $s_1, s_2$  are stutter bisimulation equivalent (stutter-bisimilar, for short), denoted  $s_1 \approx_{TS} s_2$ , if there exists a stutter bisimulation  $\mathcal{R}$  for TS with  $(s_1, s_2) \in \mathcal{R}$ .

For transition system TS with state space S:

- 1.  $\approx_{TS}$  is an equivalence relation on S.
- 2.  $\approx_{TS}$  is a stutter bisimulation for TS.
- 3.  $\approx_{TS}$  is the coarsest stutter bisimulation for TS and coincides with the union of all stutter bisimulations for TS.

## Stutter-trace equivalence

Transition  $s \to s'$  in transition system  $TS = (S, Act, \to, I, AP, L)$  is a stutter step if L(s) = L(s').

Intuitively, a stutter step operates on program or control variables that are either not visible from the outside or viewed to be irrelevant at a certain abstraction level.

The notion of stuttering is lifted to paths as follows. Two paths are called stutterequivalent if their traces only differ in their stutter steps, i.e., if there is a sequence  $A_0A_1A_2...$  of sets of atomic propositions  $A_i \subseteq AP$  such that the traces of both paths have the form  $A_0^+A_1^+A_2^+...$ 

traces  $\sigma_1$  and  $\sigma_2$  over  $2^{AP}$  are stutter-equivalent, denoted  $\sigma_1 \triangleq \sigma_2$ , if they are both of the form  $A_0^+ A_1^+ A_2^+ \dots$  for  $A_0, A_1, A_2, \dots \subseteq AP$ .

Example: stutter equivalent traces of two paths  $\pi_1$ ,  $\pi_2$ 

$$trace(\pi_1) = \underbrace{A_0 \dots A_0}_{n_0 \text{-times}} \underbrace{A_1 \dots A_1}_{n_1 \text{-times}} \underbrace{A_2 \dots A_2}_{n_2 \text{-times}} \dots$$

$$trace(\pi_2) = \underbrace{A_0 \dots A_0}_{m_0 \text{-times}} \underbrace{A_1 \dots A_1}_{m_1 \text{-times}} \underbrace{A_2 \dots A_2}_{m_2 \text{-times}} \dots$$

## Stutter implementation relations

stutter trace inclusion:

$$TS_1 \leq TS_2$$
 iff  $\forall \sigma_1 \in Traces(TS_1) \ \exists \sigma_2 \in Traces(TS_2). \ \sigma_1 \triangleq \sigma_2$ 

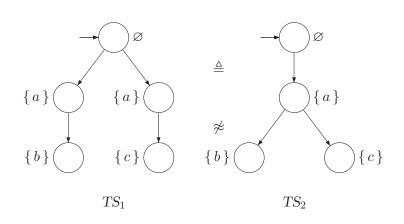
stutter trace equivalence:

$$TS_1 \triangleq TS_2$$
 iff  $TS_1 \subseteq TS_2$  and  $TS_2 \subseteq TS_1$ 

stutter bisimulation equivalence:

$$TS_1 \approx TS_2$$
 iff there exists a stutter bisimulation for  $(TS_1, TS_2)$ 

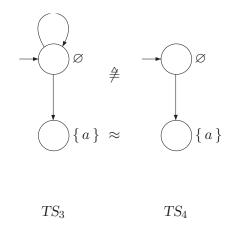
# Stuttering: trace and bisimulation are incomparable



 $TS_1 \triangleq TS_2$ , but  $TS_1 \not\approx TS_2$ .

$$Traces(TS_1) = Traces(TS_2)$$
 and  $TS_1 \not\sim TS_2$ .

TS<sub>1</sub> and TS<sub>2</sub> don't make stutter steps, stuttering is irrelevant.



$$TS_3 \approx TS_4$$
, while  $TS_3 \not\cong TS_4$ .

TS<sub>3</sub> exhibits the trace  $\emptyset^{\omega}$ , whereas TS<sub>4</sub> cannot generate this trace.

NB: the trace  $\emptyset^{\omega}$  just consists of stutter steps.