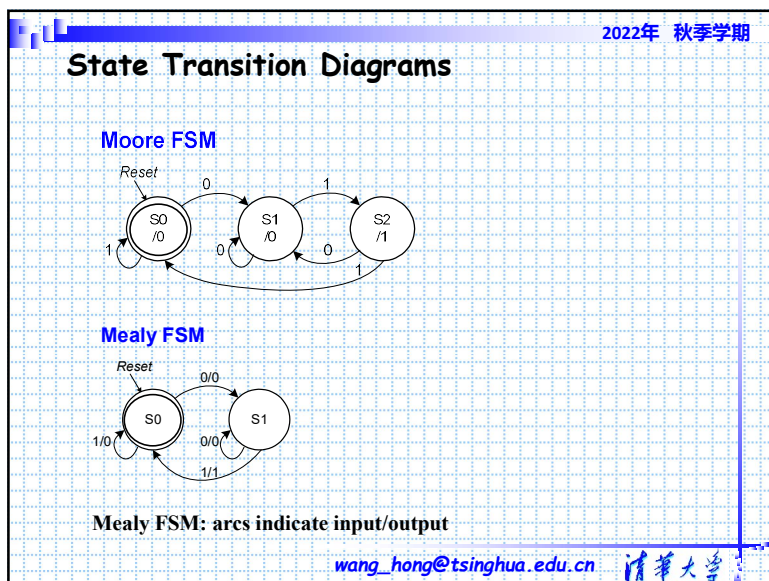


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Moore vs. Mealy FSM

- You have a snail that crawls down a paper tape with 1's and 0's on it. The snail smiles whenever the last two digits it has crawled over are 01.
- Design **Moore** and **Mealy** FSMs of the snail's brain.

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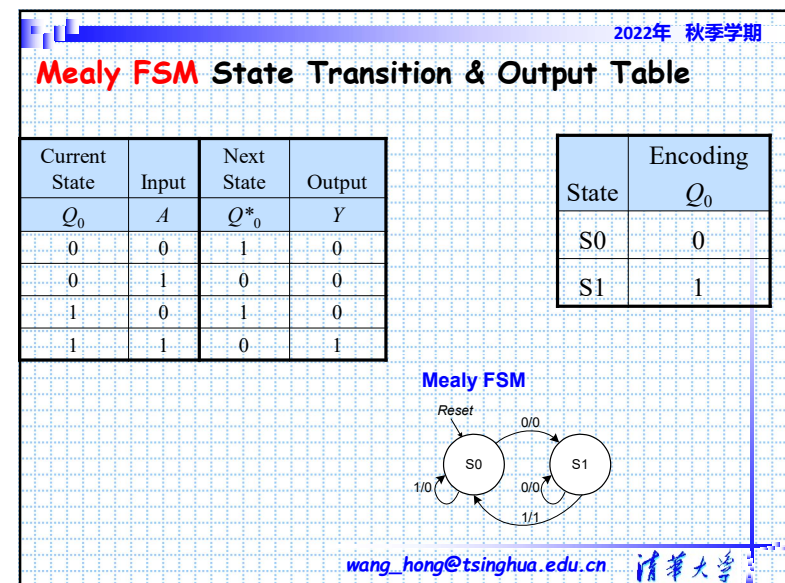
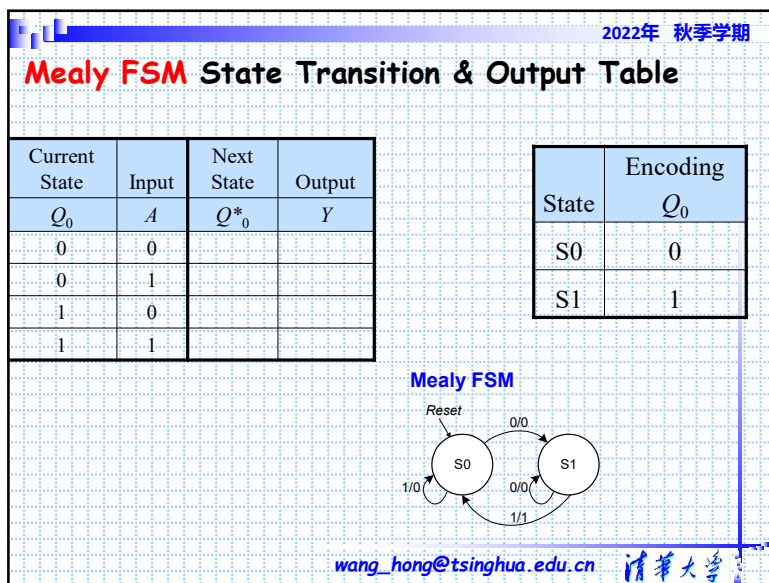
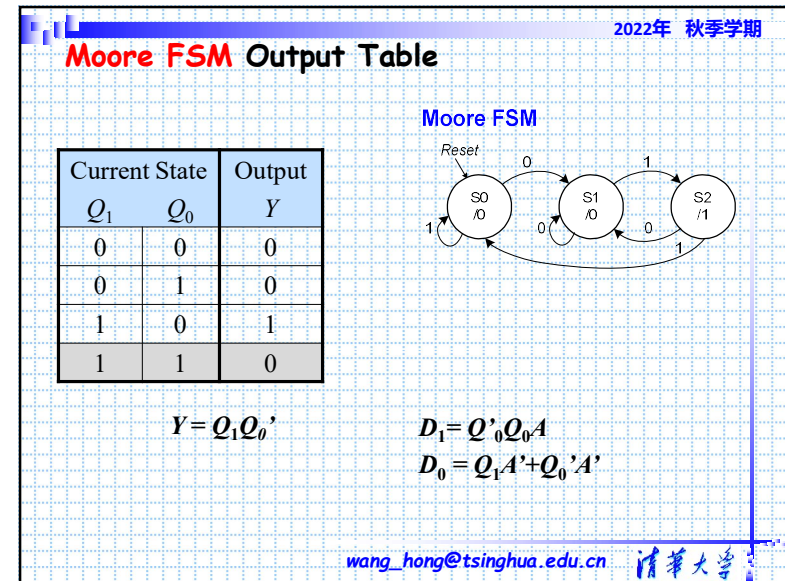
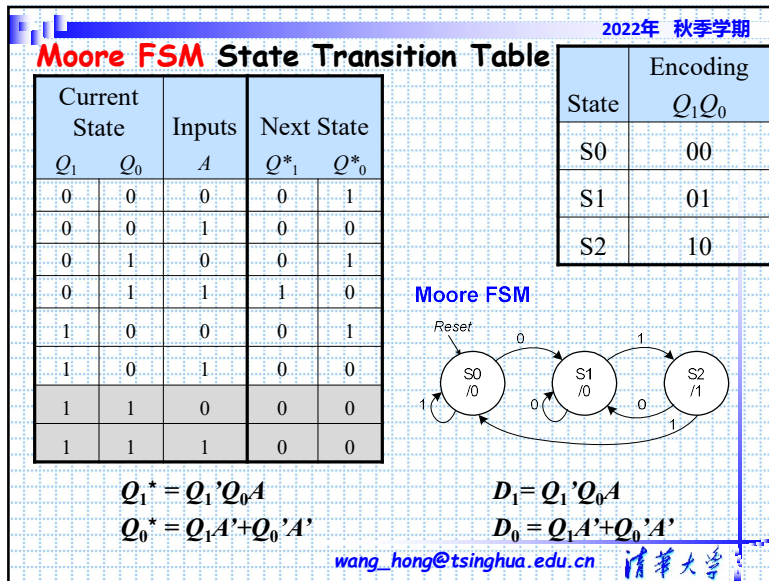
Moore FSM State Transition Table

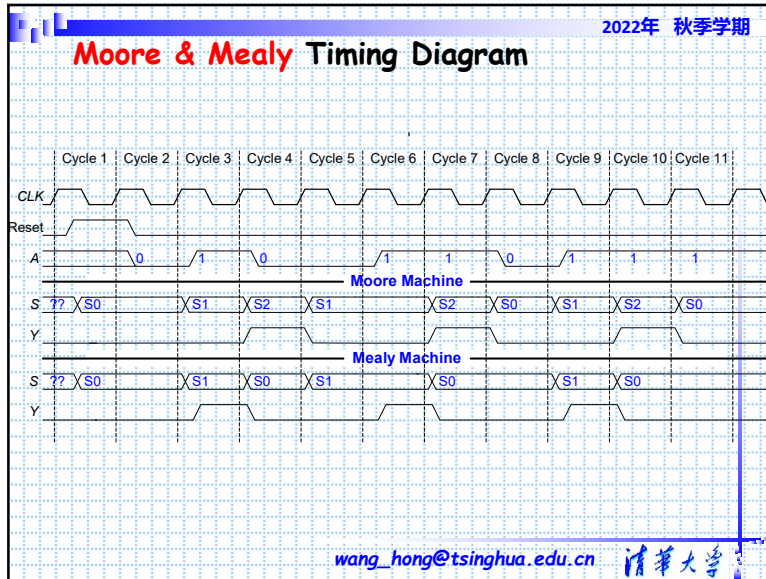
Current State		Inputs A	Next State	
Q_1	Q_0		Q_1^*	Q_0^*
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		

State	Encoding $Q_1 Q_0$
S0	00
S1	01
S2	10

Moore FSM

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Lets build an Ant

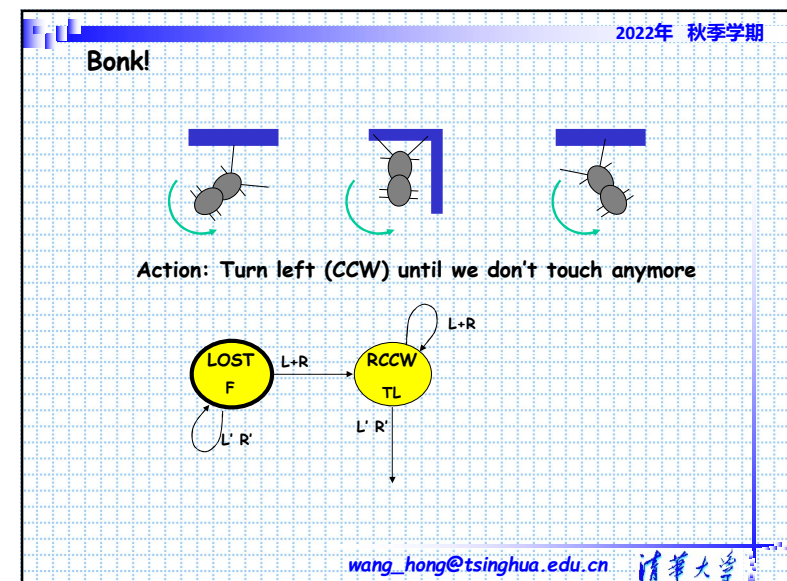
8 legs?

- SENSORS: antennae L and R, each 1 if in contact with something.
- ACTUATORS: Forward Step F, ten-degree turns TL and TR (left, right).

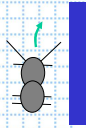
GOAL: Make our ant smart enough to get out of a maze like:

STRATEGY: "Right antenna to the wall"

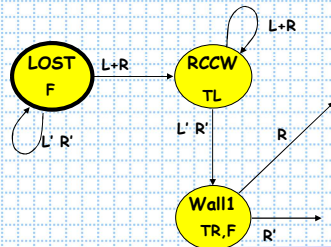
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A little to the right...



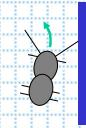
Action: Step and turn right a little, look for wall



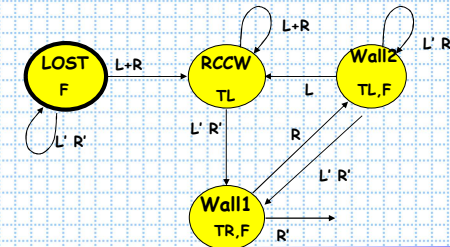
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Then a little to the left



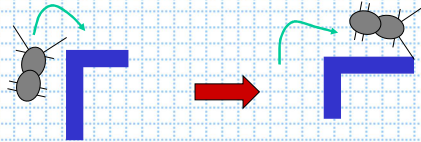
Action: Step and turn left a little, till not touching (again)



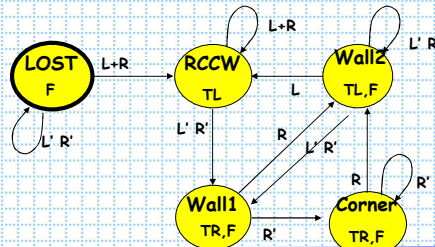
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Dealing with corners



Action: Step and turn right until we hit perpendicular wall



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Equivalent State Reduction

Observation: $S_i \equiv S_j$ if

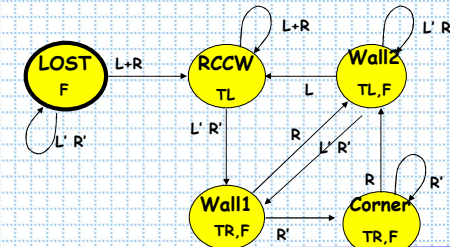
1. States have identical outputs;

AND

2. Every input \rightarrow equivalent states.

Reduction Strategy:

Find pairs of equivalent states, MERGE them.

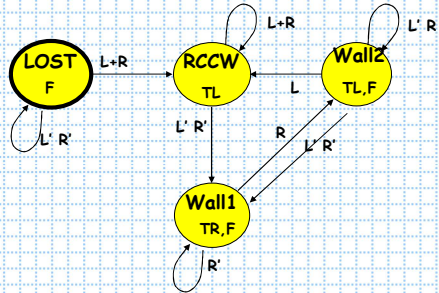


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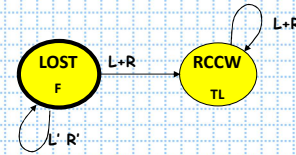
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An Evolutionary Step

Merge equivalent states Wall1 and Corner into a single new, combined state.



Building the Transition Table



S	L	R	S*	TR	TL	F
00	0	0	00	0	0	1
00	1	-	01	0	0	1
00	0	1	01	0	0	1
01	1	-	01	0	1	0
01	0	1	01	0	1	0

Implementation Details

S ₁ S ₀	S	L	R	S*	TR	TL	F
LOST	00	0	0	00	0	0	1
	00	1	-	01	0	0	1
	00	0	1	01	0	0	1
RCCW	01	1	-	01	0	1	0
	01	0	1	01	0	1	0
	01	0	0	10	0	1	0
WALL1	10	-	0	10	1	0	1
	10	-	1	11	1	0	1
	11	1	-	01	0	1	1
WALL2	11	0	0	10	0	1	1
	11	0	1	11	0	1	1

Complete Transition table

S ₁ *	S ₁ S ₀	00	01	11	10
LR	00	0	1	1	1
	01	0	0	1	1
	11	0	0	0	1
	10	0	0	0	1

$$S_1^* = S_1 S_0' + L' S_1 + L' R' S_0$$

S ₀ *	S ₁ S ₀	00	01	11	10
LR	00	0	0	0	0
	01	1	1	1	1
	11	1	1	1	1
	10	1	1	1	0

$$S_0^* = R + L S_1' + L S_0$$

Ant Schematic

